

# DCR806SG

# **Phase Control Thyristor**

Supersedes August 2000 version, DS4642-5.1

DS4642 -6.0 July 2001

### **FEATURES**

- Double Side Cooling
- High Surge Capability
- High Mean Current

### **APPLICATIONS**

- High Power Drives
- High Voltage Power Supplies
- DC Motor Control
- Power Supplies

### **VOLTAGE RATINGS**

Type Number	Repetitive Peak Voltages V <sub>DRM</sub> V <sub>RRM</sub> V	Conditions
DCR806SG28	2800	$T_{v_i} = 0^{\circ} \text{ to } 125^{\circ}\text{C},$
DCR806SG27	2700	$I_{DRM} = I_{RRM} = 50 \text{mA},$
DCR806SG26	2600	$V_{DRM}$ , $V_{RRM}$ $t_p = 10 ms$ ,
DCR806SG25	2500	V <sub>DSM</sub> & V <sub>RSM</sub> =
DCR806SG24	2400	V <sub>DRM</sub> & V <sub>RRM</sub> + 100V
		Respectively

Lower voltage grades available.

## **ORDERING INFORMATION**

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

### DCR806SG26

Note: Please use the complete part number when ordering and quote this number in any future correspondance relating to your order.

## **KEY PARAMETERS**

 $\begin{array}{lll} {\rm V_{DRM}} & 2800 {\rm V} \\ {\rm I_{T(AV)}} & 844 {\rm A} \\ {\rm I_{TSM}} & 11250 {\rm A} \\ {\rm dVdt^*} & 1000 {\rm V/\mu s} \\ {\rm dI/dt} & 500 {\rm A/\mu s} \\ \end{array}$ 

<sup>\*</sup>Higher dV/dt selections available

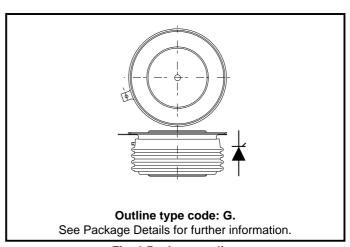


Fig. 1 Package outline



## **CURRENT RATINGS**

 $T_{case} = 60^{\circ}C$  unless stated otherwise

Symbol	Parameter	Conditions	Max.	Units			
Double Sid	Double Side Cooled						
I <sub>T(AV)</sub>	Mean on-state current	Half wave resistive load	844	А			
I <sub>T(RMS)</sub>	RMS value	-	1326	А			
I <sub>T</sub>	Continuous (direct) on-state current	-	1201	А			
Single Side Cooled (Anode side)							
I <sub>T(AV)</sub>	Mean on-state current	Half wave resistive load	555	А			
I <sub>T(RMS)</sub>	RMS value	-	872	А			
I <sub>T</sub>	Continuous (direct) on-state current	-	733	А			

## **CURRENT RATINGS**

 $T_{case} = 80^{\circ}C$  unless stated otherwise

Symbol	Parameter	Conditions	Max.	Units			
Double Sic	Double Side Cooled						
I <sub>T(AV)</sub>	Mean on-state current	Half wave resistive load	670	А			
I <sub>T(RMS)</sub>	RMS value	-	1050	А			
Ι <sub>τ</sub>	Continuous (direct) on-state current	-	875	А			
Single Side Cooled (Anode side)							
I <sub>T(AV)</sub>	Mean on-state current	Half wave resistive load	430	А			
I <sub>T(RMS)</sub>	RMS value	-	675	А			
I <sub>T</sub>	Continuous (direct) on-state current	-	540	А			



## **SURGE RATINGS**

Symbol	Parameter	Conditions	Max.	Units
I <sub>TSM</sub>	Surge (non-repetitive) on-state current	10ms half sine; T <sub>case</sub> = 125°C	9.0	kA
l²t	I <sup>2</sup> t for fusing	V <sub>R</sub> = 50% V <sub>RRM</sub> - 1/4 sine	405 x 10 <sup>3</sup>	A²s
I <sub>TSM</sub>	Surge (non-repetitive) on-state current	10ms half sine; T <sub>case</sub> = 125°C	11.25	kA
l²t	I <sup>2</sup> t for fusing	V <sub>R</sub> = 0	633 x 10 <sup>3</sup>	A²s

## THERMAL AND MECHANICAL DATA

Symbol	Parameter	Conditions		Min.	Max.	Units
$R_{th(j-c)}$	Thermal resistance - junction to case	Double side cooled	dc	-	0.032	°C/W
		Single side cooled	Anode dc	-	0.064	°C/W
			Cathode dc	-	0.064	°C/W
R <sub>th(c-h)</sub>	Thermal resistance - case to heatsink	Clamping force 12.5kN with mounting compound	Double side	-	0.008	°C/W
			Single side	-	0.016	°C/W
$T_{v_{j}}$	Virtual junction temperature	On-state (conducting)		-	135	°C
		Reverse (blocking)		-	125	°C
T <sub>stg</sub>	Storage temperature range			-55	125	°C
-	Clamping force			11.0	13.0	kN



## **DYNAMIC CHARACTERISTICS**

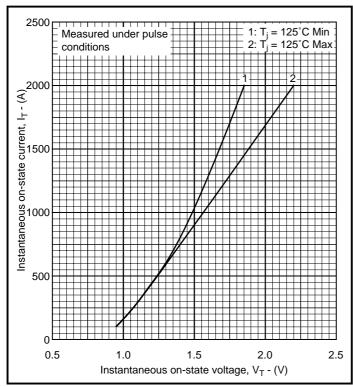
Symbol	Parameter	Conditions		Тур.	Max.	Units
I <sub>RRM</sub> /I <sub>DRM</sub>	Peak reverse and off-state current	At V <sub>RRM</sub> /V <sub>DRM</sub> , T <sub>case</sub> = 125°C		-	50	mA
dV/dt	Maximum linear rate of rise of off-state voltage	To 67% $V_{DRM}$ $T_j$ = 125°C. Gate open circuit.		-	1000	V/µs
		Gate source 1.5A	Repetitive 50Hz	-	300	A/μs
dl/dt	Rate of rise of on-state current		Non-repetitive	-	500	A/μs
V <sub>T(TO)</sub>	Threshold voltage	At T <sub>vj</sub> = 125°C		-	0.91	٧
r <sub>T</sub>	On-state slope resistance	At T <sub>vj</sub> = 125°C		-	0.65	mΩ
t <sub>gd</sub>	Delay time	$V_D = 67\%$ $V_{DRM}$ , Gate source 30V, 15Ω $t_r = 0.5 \mu s$ , $T_j = 25$ °C		-	1.5	μs
t <sub>q</sub>	Turn-off time	$I_T = 500A$ , $t_p = 1ms$ , $T_j = 125$ °C, $V_R = 50V$ , $dI_{RR}/dt = 20A/\mu s$ , $V_{DR} = 67\% V_{DRM}$ , $dV_{DR}/dt = 20V/\mu s$ linear		300	500	μs
IL	Latching current	$T_{j} = 25^{\circ}C, V_{D} = 5V$		550	1000	mA
I <sub>H</sub>	Holding current	$T_{j} = 25^{\circ}C, V_{D} = 5V$		60	100	mA

## **GATE TRIGGER CHARACTERISTICS AND RATINGS**

Symbol	Parameter	Conditions	Max.	Units
V <sub>GT</sub>	Gate trigger voltage	V <sub>DRM</sub> = 5V, T <sub>case</sub> = 25°C	3.5	V
I <sub>GT</sub>	Gate trigger current	$V_{DRM} = 5V$ , $T_{case} = 25^{\circ}C$	200	mA
V <sub>GD</sub>	Gate non-trigger voltage	At V <sub>DRM</sub> T <sub>case</sub> = 125°C	0.25	٧
V <sub>FGM</sub>	Peak forward gate voltage	Anode positive with respect to cathode	30	٧
V <sub>FGN</sub>	Peak forward gate voltage	Anode negative with respect to cathode	0.25	V
$V_{RGM}$	Peak reverse gate voltage		5	٧
I <sub>FGM</sub>	Peak forward gate current	Anode positive with respect to cathode	10	Α
P <sub>GM</sub>	Peak gate power	See table, gate characteristics curve	150	W
P <sub>G(AV)</sub>	Mean gate power		10	W



## **CURVES**



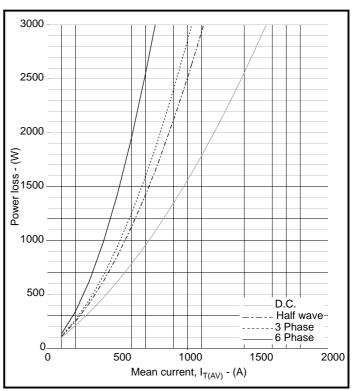


Fig.2 Maximum (limit) on-state characteristics

Fig.3 Dissipation curves

 $V_{\rm TM}$  Equation:-

$$V_{TM} = A + Bln (I_T) + C.I_T + D.\sqrt{I_T}$$

Where A = 0.6102629

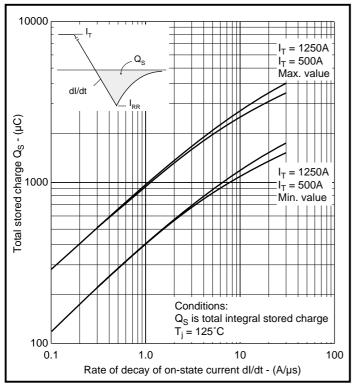
B = 0.08049203

 $C = 7.189037 \times 10^{-4}$ 

D = -0.01028328

these values are valid for  $T_i = 125^{\circ}C$  for  $I_T 500A$  to 2500A





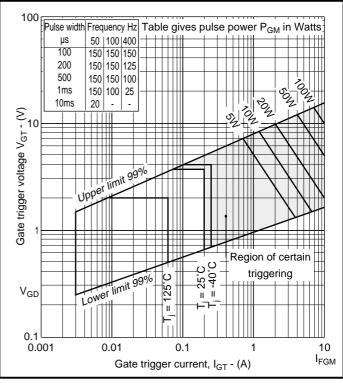


Fig.4 Stored charge

Fig.5 Gate characteristics

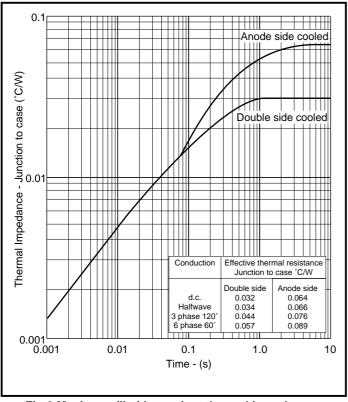


Fig.6 Maximum (limit) transient thermal impedance - junction to case

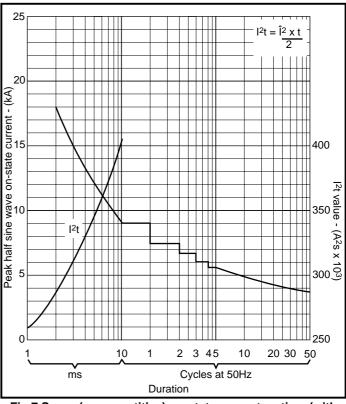
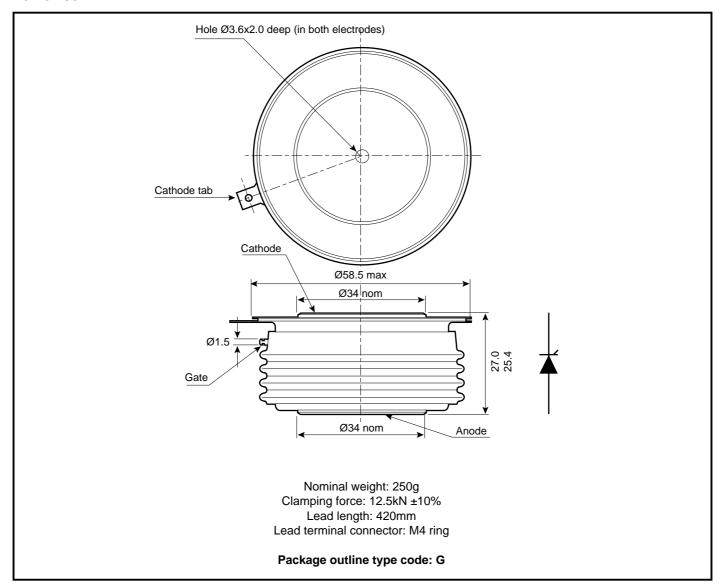


Fig.7 Surge (non-repetitive) on-state current vs time (with 50%  $\rm V_{RRM}$  at  $\rm T_{case}\,125^{\circ}C)$ 



### **PACKAGE DETAILS**

For further package information, please contact your nearest Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.





### **POWER ASSEMBLY CAPABILITY**

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink and clamping systems in line with advances in device voltages and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group continues to offer high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

#### **DEVICE CLAMPS**

Disc devices require the correct clamping force to ensure their safe operation. The PACS range includes a varied selection of preloaded clamps to suit all of our manufactured devices. Types available include cube clamps for single side cooling of 'T' 23mm and 'E' 30mm discs, and bar clamps right up to 83kN of our 'Z' 100mm thyristors and diodes.

Clamps are available for single or double side cooling, with high insulation versions for high voltage assemblies.

Please refer to our application note on device clamping, AN4839

#### **HEATSINKS**

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks. They have been designed to optimize the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or customer service office.



#### http://www.dynexsemi.com

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Preliminary Information: The product is in design and development. The datasheet represents the product as it is understood but details may change.

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