

TO-3PF 16A Triac

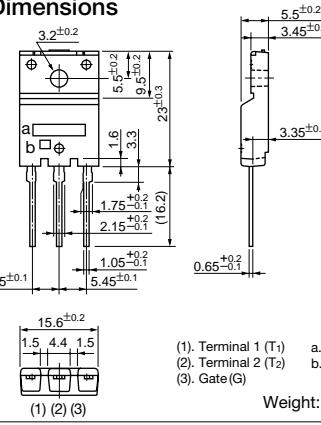
TM1641B-L, TM1661B-L

■ Features

- Repetitive peak off-state voltage: $V_{DRM}=400, 600\text{V}$
- RMS on-state current: $I_{T(\text{RMS})}=16\text{A}$
- Gate trigger current: $I_{GT}=30\text{mA}$ max (MODE I, II, III)
- Rate-of-rise of off-state commutation voltage: $(dv/dt)_c=10\text{V}/\mu\text{s}$ min.
- Isolation voltage: $V_{ISO}=2000\text{V}(\text{AC}, 1\text{min.})$
- UL approved type available

External Dimensions

(Unit: mm)



(1). Terminal 1 (T₁) a. Part Number
 (2). Terminal 2 (T₂) b. Lot Number
 (3). Gate (G)

Weight: Approx. 6.5g

■ Absolute Maximum Ratings

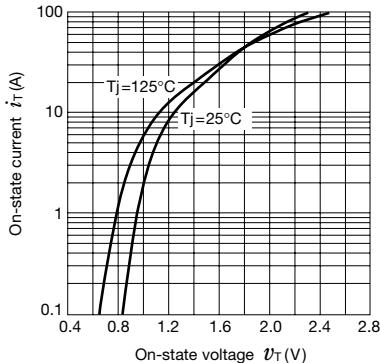
Parameter	Symbol	Ratings		Unit	Conditions
		TM1641B-L	TM1661B-L		
Repetitive peak off-state voltage	V_{DRM}	400	600	V	
RMS on-state current	$I_{T(\text{RMS})}$	16		A	Conduction angle 360°, $T_c=92.5^\circ\text{C}$
Surge on-state current	I_{TSM}	160		A	50Hz full-cycle sinewave, Peak value, Non-repetitive, $T_j=125^\circ\text{C}$
Peak gate voltage	V_{GM}	10		V	$f \geq 50\text{Hz}$, duty $\leq 10\%$
Peak gate current	I_{GM}	2		A	$f \geq 50\text{Hz}$, duty $\leq 10\%$
Peak gate power loss	P_{GM}	5		W	$f \geq 50\text{Hz}$, duty $\leq 10\%$
Average gate power loss	$P_{G(AV)}$	0.5		W	
Junction temperature	T_j	-40 to +125		°C	
Storage temperature	T_{STG}	-40 to +125		°C	
Isolation voltage	V_{ISO}	2000		Vrms	50Hz Sine wave, RMS, Terminal to Case, 1 min.

■ Electrical Characteristics

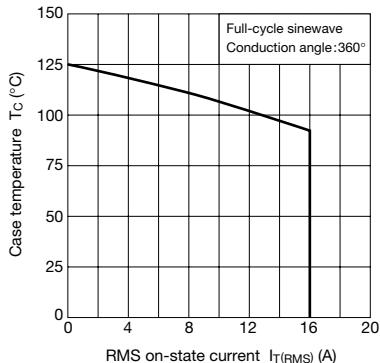
Parameter	Symbol	Ratings			Unit	Conditions
		min	typ	max		
Off-state current	I_{DRM}		0.1	2.0	mA	$V_D=V_{DRM}$, $R_{GK}=\infty$, $T_j=125^\circ\text{C}$
				0.1		$V_D=V_{DRM}$, $R_{GK}=\infty$, $T_j=25^\circ\text{C}$
On-state voltage	V_{TM}			1.6	V	$I_{TM}=20\text{A}$, $T_c=25^\circ\text{C}$
Gate trigger voltage	V_{GT}	I	0.8	1.5	V	$V_D=6\text{V}$, $R_L=10\Omega$, $T_c=25^\circ\text{C}$
		II	0.7	1.5		
		III	0.8	1.5		
		IV	1.0			
Gate trigger current	I_{GT}	I	12	30	mA	$V_D=6\text{V}$, $R_L=10\Omega$, $T_c=25^\circ\text{C}$
		II	16	30		
		III	25	30		
		IV	70			
Gate non-trigger voltage	V_{GD}	0.2			V	$V_D=1/2 \times V_{DRM}$, $T_j=125^\circ\text{C}$
Holding current	I_H		25		mA	$T_j=25^\circ\text{C}$
Rate-of-rise of off-state commutation voltage	$(dv/dt)_c$	10			V/ μs	$V_D=400\text{V}$, $T_j=125^\circ\text{C}$
Thermal resistance	R_{th}			1.8	°C/W	Junction to case

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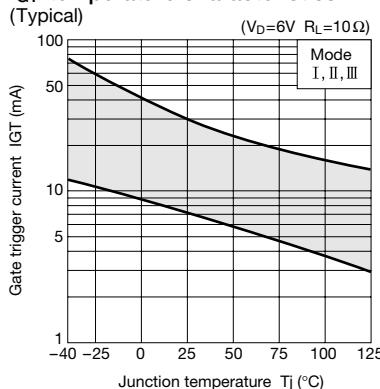
$v_T - i_T$ Characteristics (max)



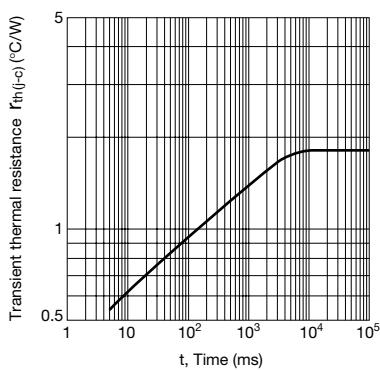
$I_{TRMS} - T_c$ Ratings



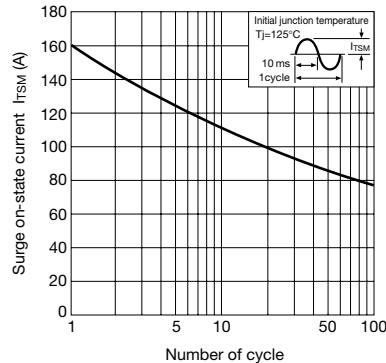
I_{GT} temperature characteristics



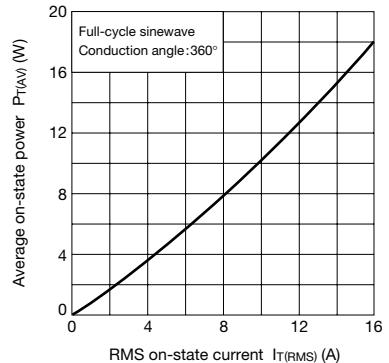
$r_{th(j-c)} - t$ Characteristics



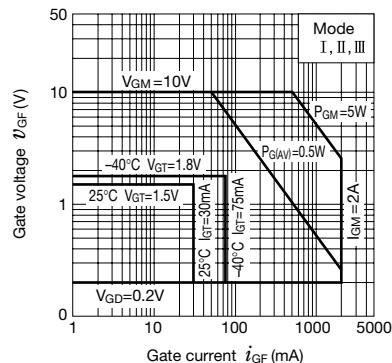
I_{TSM} Ratings



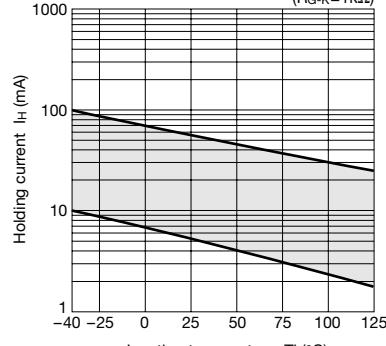
$I_{TRMS} - P_{T(AV)}$ Characteristics



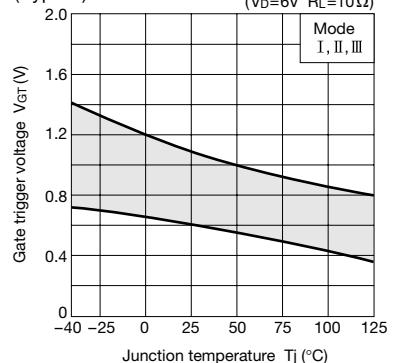
Gate Characteristics



I_H temperature characteristics
(Typical)



V_{GT} temperature characteristics



I_L temperature characteristics
(Typical)

