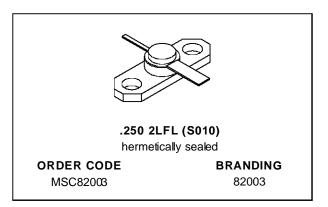


## MSC82003

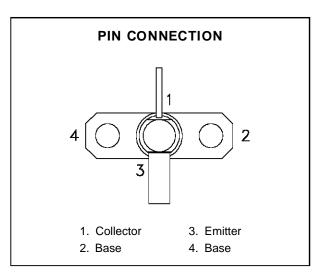
# RF & MICROWAVE TRANSISTORS GENERAL PURPOSE AMPLIFIER APPLICATIONS

- EMITTER BALLASTED
- VSWR CAPABILITY ∞:1 @ RATED CONDITIONS
- REFRACTORY/GOLD METALLIZATION
- HERMETIC STRIPAC® PACKAGE
- P<sub>OUT</sub> = 3.0 W MIN. WITH 7.8 dB GAIN @ 2.0 GHz



#### **DESCRIPTION**

The MSC82003 is a common base hermetically sealed silicon NPN microwave transistor utilizing a fishbone emitter ballasted geometry with a refractory/gold metallization system. This device is capable of withstanding an infinite load VSWR at any phase angle under rated rated conditions. The MSC82003 was designed for Class C amplifier applications in the 1.0 - 2.0 GHz frequency range.



#### **ABSOLUTE MAXIMUM RATINGS** $(T_{case} = 25^{\circ}C)$

Symbol	Parameter	Value	Unit
Poiss	Power Dissipation*	21.8	W
Ic	Device Current*	600	mA
V <sub>CC</sub>	Collector-Supply Voltage*	35	V
TJ	Junction Temperature	200	°C
T <sub>STG</sub>	Storage Temperature	- 65 to +200	°C

#### THERMAL DATA

R <sub>TH(j-c)</sub> Junction-Case Thermal Resistance*	8.0	°C/W
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<sup>\*</sup>Applies only to rated RF amplifier operation

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## **ELECTRICAL SPECIFICATIONS** (T<sub>case</sub> = 25°C)

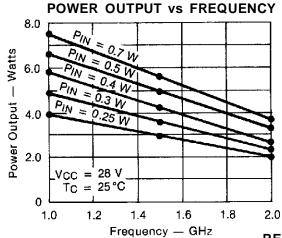
## **STATIC**

Symbol	Test Conditions	Value			11:4		
		Min.	Тур.	Max.	Unit		
ВУсво	I <sub>C</sub> = 1mA	$I_E = 0mA$		45	_	_	V
BV <sub>EBO</sub>	I <sub>E</sub> = 1mA	$I_C = 0mA$		3.5	_	_	V
BV <sub>CER</sub>	IC = 5mA	$R_{BE} = 10\Omega$		45	_	_	V
Ісво	V <sub>CB</sub> = 28V			_	_	1.0	mA
hFE	V <sub>CE</sub> = 5V	$I_C = 200 \text{mA}$		15	_	120	_

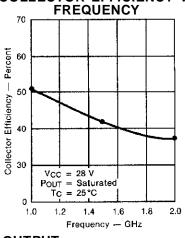
## **DYNAMIC**

Symbol	Test Conditions		Value			Unit	
Syllibol		rest Conditions			Тур.	Max.	Oiiit
Роит	f = 2.0 GHz	$P_{IN} = 0.5 W$	$V_{CC} = 28 V$	3.0	3.3		W
ης	f = 2.0 GHz	$P_{IN} = 0.5 W$	$V_{CC} = 28 V$	35	37	_	%
G <sub>P</sub>	f = 2.0 GHz	$P_{IN}=0.5\;W$	$V_{CC} = 28 \text{ V}$	7.8	8.2	_	dB
СОВ	f = 1 MHz	$V_{CB} = 28 \text{ V}$		_	_	6.5	pF

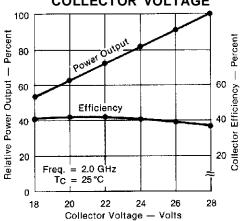
## TYPICAL PERFORMANCE



## **COLLECTOR EFFICIENCY vs**

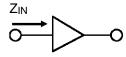


## RELATIVE POWER OUTPUT vs COLLECTOR VOLTAGE

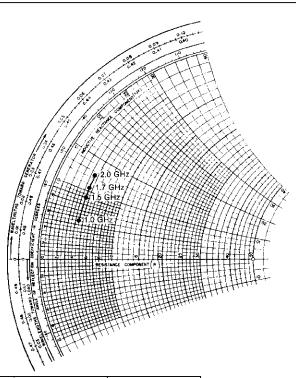


## **IMPEDANCE DATA**



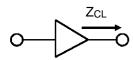


$$\begin{split} P_{IN} &= 0.5 \ W \\ V_{CC} &= 28 \ V \\ Normalized \ to \ 50 \ ohms \end{split}$$

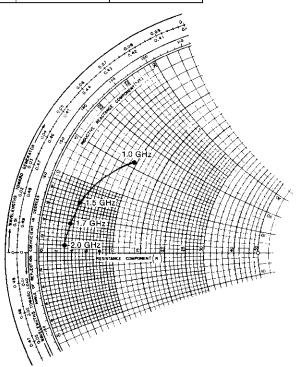


FREQ.	Z <sub>IN</sub> (Ω)	Z <sub>CL</sub> (Ω)
1.0 GHz	4.4 + j 5.5	9.6 + j 16.0
1.5 GHz	4.5 + j 9.0	4.3 + j 7.0
1.7 GHz	4.5 + j 10.5	3.5 + j 4.0
2.0 GHz	4.6 + j 12.5	3.0 + j 1.0

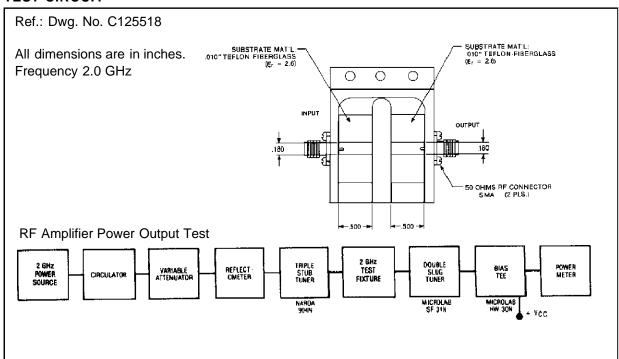
## TYPICAL COLLECTOR LOAD IMPEDANCE



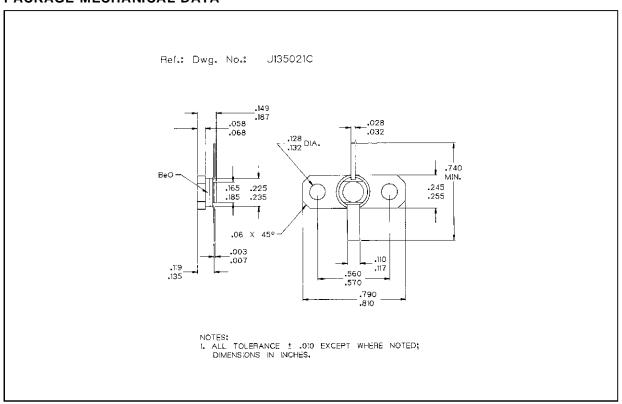
 $P_{OUT} = Saturated$   $V_{CC} = 28 V$ Normalized to 50 ohms



## **TEST CIRCUIT**



## PACKAGE MECHANICAL DATA



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