

FM PROCESSOR

KIA6072AF is an LSI designed for car-tuner. built in FM IF detector, noise canceller and stereo decoder on single chip, the space merit is improved. and as this has the various functions, an FM tuner with excellent characteristic can be achieved.

FUNCTIONS

FM IF Detector STAGE

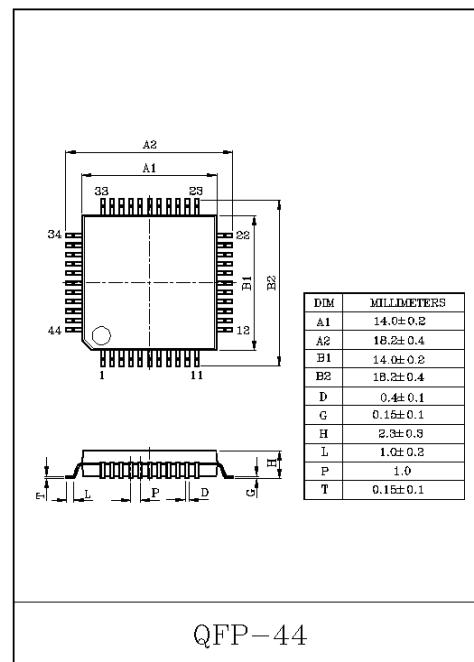
- IF Limitter Amplifier
- Differencial Peak FM Detector
- Meter Output
- Slider
- Radio Field Strength Muting
- Detuning Muting
- Station Detection
- IF Count Request

Noise Canceller STAGE

- Noise Detection
- Noise AGC
- Signal Delay
- Adjustment Free Piolt Cancel
- Signal Holding

MPX STAGE

- Adjustment Free PLL Stereo Decoder
- Pre-Amplifier
- Blender Control
- Tone Control (Low Cut, High Cut)
- Anti-ARI
- Anti-Birdy



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FEATURES

FM IF Detector Stage

- Application of differential peak detection is useful against multi path distortion.
- With single tuning coil, low distortion can be achieved.
: 0.2% typical at 75kHz dev.
- Good AMR performance.
: 60dB Typical at $V_{IN}=80\text{dB}\mu$
- Sensitivity and attenuation of radio field strength muting can freely be set.
- Width of detuning muting and station detector can freely be set.
- Sensitivity of station detector can freely be set.
- Station detection output can be applied to both stop pulse and IF count.
ON/OFF of IF count output can be controlled with request signal from microcomputer.
- Built-in slider which varies a position of the operating width of blender control and tone control with an inclination of meter output.

Noise Canceller Stage

- Built in Low Pass Filter for delay of pulse noise.
- Built in High Pass Filter for noise detection.
- The inclination of HPF at stereo mode is varied. and the effect of noise canceller is improved.
- For adjustment free pilot canceller, initial adjustment of pilot cancel is not necessary.

MPX Stage

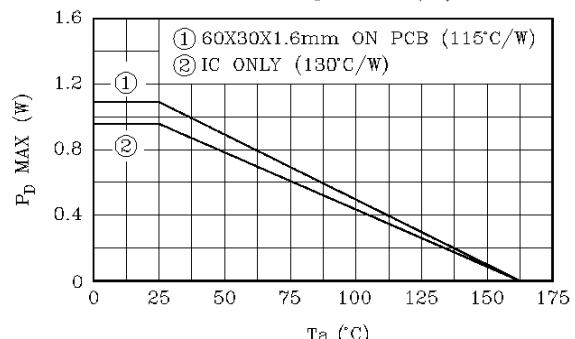
- For adopting adjustment free PLL, adjustment of VCO is not necessary.
- Since tone control function which can do not only high cut but low cut at the weak radio field strength, a feeling of sound is improved.
- Anti-ARI improve the poor stereo separation caused by ARI disturbance.
- Disturbance of birdy noise etc. is improved by Anti-birdy.
- Operating supply voltage : 7~9V ($T_a=25^\circ\text{C}$)

MAXIMUM RAGINGS ($T_a=25^\circ\text{C}$)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Power Supply Voltage	V_{cc}	10	V
Power Dissipation (Note)	P_D	960	mW
Operating Temperature	T_{opr}	-40~85	$^\circ\text{C}$
Storage Temperature	T_{stg}	-55~150	$^\circ\text{C}$

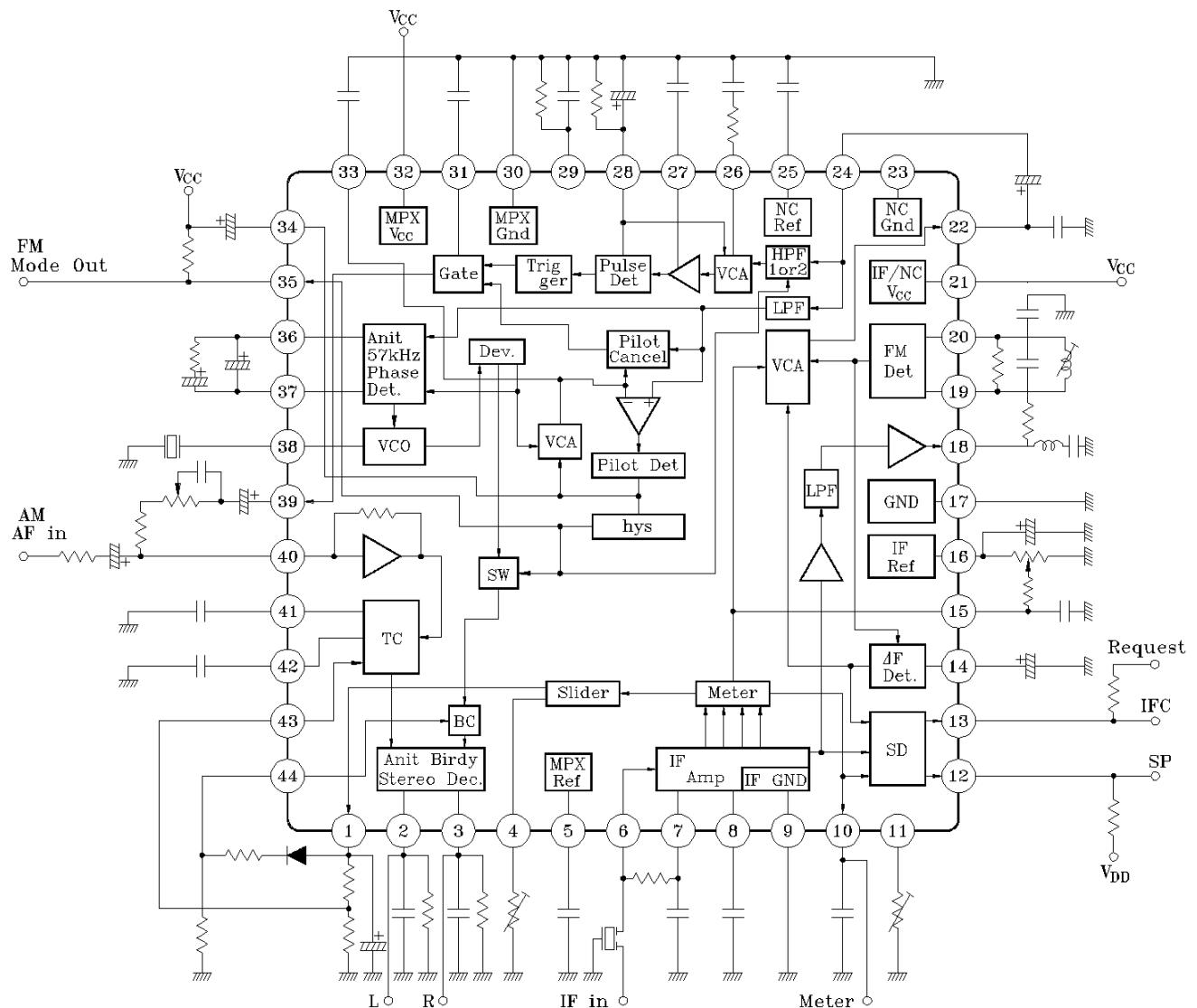
Note : Derated linearly above $T_a=25^\circ\text{C}$ in the proportion of 8mW/ $^\circ\text{C}$. In case actual operation refer to Fig.1

Fig.1 Power Dissipation (P_D)—Operating Ambient Temperature (T_a)



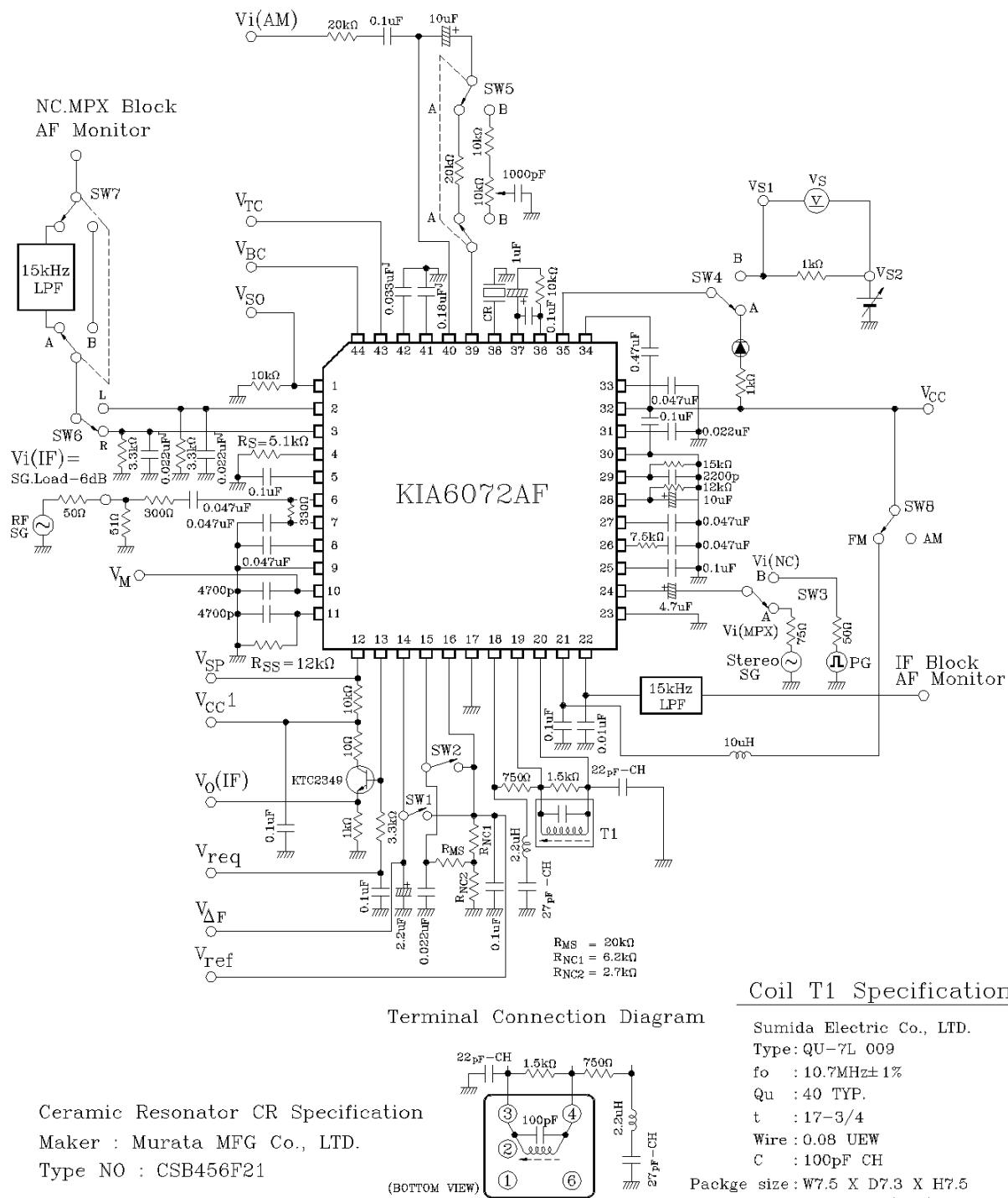
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BLOCK DIAGRAM



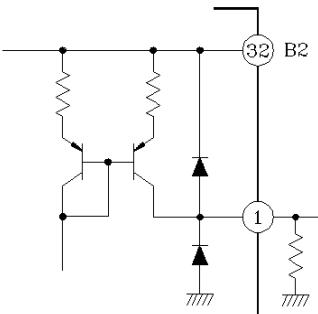
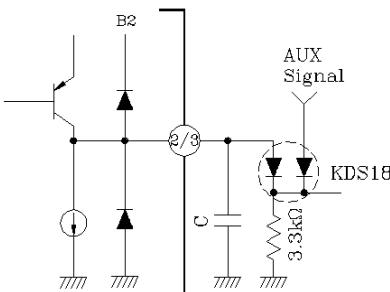
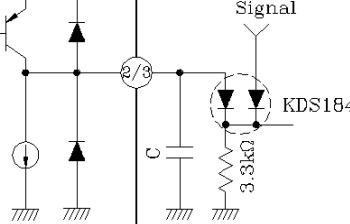
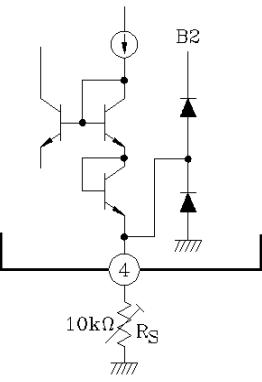
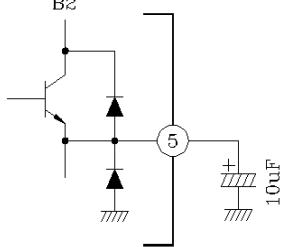
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TEST CIRCUIT



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TERMINAL EXPLANATION (Terminal voltage shows typical value at quiescent signal on test circuit.
 $V_{CC}=8V$, $Ta=25^{\circ}C$)

No.	Name	Terminal Voltage	Equivalent circuit/Typical Value of external port (Internal R/C Value)	Function
1	SO	0		<ul style="list-style-type: none"> Slider Output Blender control or Tone control is made by this output terminal.
2	Lout	2.6		<ul style="list-style-type: none"> MPX output Switching circuit by a diode low distortion by a large bias current
3	Rout	2.6		<ul style="list-style-type: none"> A capacitor value is for de-emphasis condition.
4	Slide	0.7		<ul style="list-style-type: none"> Setting terminal to vary a position of operating width for Blender/Tone control and to side an output signal rising point with a inclination of a meter output characteristic. Connect a variable resistance. (a small sliding by resistance. with a small value.)
5	Ref3	5.2		<ul style="list-style-type: none"> Third reference voltage. Connect a capacitor.

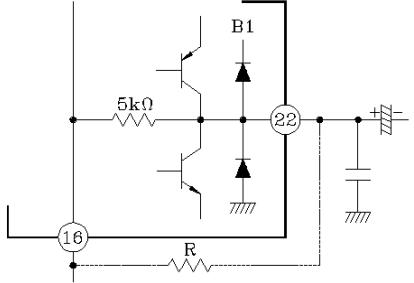
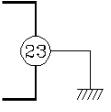
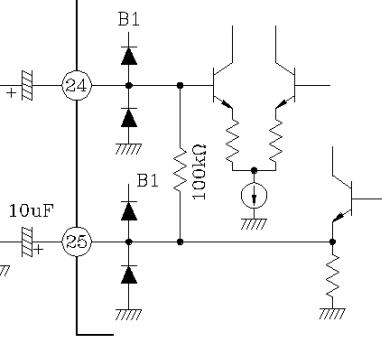
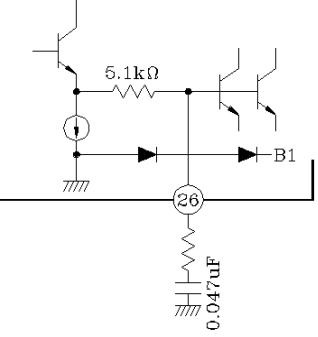
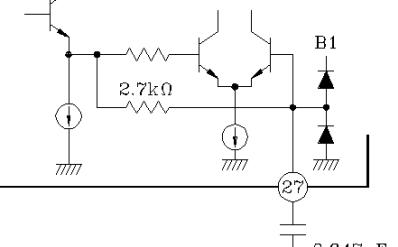
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No.	Name	Terminal Voltage	Equivalent circuit/Typical Value of external port (Internal R/C Value)	Function
6	IFin	3.0		<ul style="list-style-type: none"> • IF amp. Input terminal. • Connect a ceramic filter to this terminal closely. • Supply a bias to pin 11 through the resistor.
7	Bias	3.0		<ul style="list-style-type: none"> • IF Amp Bias. • Connect a capacitor.
8	By1	3.0		<ul style="list-style-type: none"> • IF Amp. Ground terminal. • Connect a ceramic filter to this pin closely.
9	G1	0		
10	Meter	0		<ul style="list-style-type: none"> • FM mode : Meter output AM mode : Meter signal input to the internal slider circuit. voltage level of B1 is "L" • Connect a capacitor to vary a response-speed of a meter output.
11	SD	0.7		<ul style="list-style-type: none"> • Sensitivity setting terminal for the station detector. • Connect a Resistance(Rss) and a capacitor(High Sensitivity by large Rss. C value is for setting a response-speed)
12	SP	0		<ul style="list-style-type: none"> • Stop pulse output for station detection. • Connect a resistance to V_DD.
13	IFC	-		<ul style="list-style-type: none"> • IF connector output for station Detection. • A signal is output by connecting pin 13 to V_DD line through R-3.3kΩ. No usage, open circuit or connect to the GND.
15	SM	4.8		<ul style="list-style-type: none"> • Soft muting characteristic setting terminal. (For a limiting sensitivity, noise convergent point.) • High Sensitivity by large R_MS, and a Capacitor is for setting a response-speed.(Ref.1)

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No.	Name	Terminal Voltage	Equivalent circuit/Typical Value of external port (Internal R/C Value)	Function
14	ΔF	4.8	<p>The diagram shows a bandpass filter circuit. It consists of two operational amplifiers connected in a feedback loop. The input signal is fed into the non-inverting input of the first op-amp. The output of the first op-amp is fed into the inverting input of the second op-amp. A variable resistor B1 is connected between the output of the first op-amp and the inverting input of the second op-amp. A fixed resistor of 16kΩ is connected between the inverting input of the second op-amp and ground. The output of the second op-amp is connected to pin 14. Pin 14 is also connected to a 2.2μF capacitor and a 10μF capacitor in parallel. A 10kΩ variable resistor R_{NC} is connected between the junction of the two capacitors and ground. A 16kΩ resistor is connected between the junction of the capacitors and pin 16.</p>	<ul style="list-style-type: none"> Smoothing terminal of detector output. Connect a capacitor Setting a band width by a resistor value (R_{BW}).
16	Ref1	4.8	<p>The diagram shows a noise level adjustment circuit. Pin 16 is connected to a 10kΩ variable resistor R_{NC}. The other end of R_{NC} is connected to a 10μF capacitor. The junction of the capacitor and R_{NC} is connected to ground. Pin 16 is also connected to a 2.2μF capacitor and a 16kΩ resistor. The output of this stage is connected to pin 14.</p>	<ul style="list-style-type: none"> Ref. 1 voltage terminal. R_{NC} is to adjust a noise level (Vin=20dBμ) for soft mute characteristic. Shifting the blush (for R_{NC}) in the direction of the GND is to lower a noise level (for Vin=20dBμ). for adjustment of FM detector is to adjust a detection coil so as to be zero in potential difference between pin 14 and pin 16.
17	G4	0		<ul style="list-style-type: none"> Substrate GND terminal.
18	IFout	3.0	<p>The diagram shows an IF limiter amplifier circuit. Pin 18 is connected to a resistor R_{THD}. The other end of R_{THD} is connected to the inverting input of a first operational amplifier. The output of the first op-amp is connected to the inverting input of a second op-amp. The output of the second op-amp is connected to pin 18. The non-inverting inputs of both op-amps are connected to ground. The outputs of both op-amps are connected to a common node, which is then connected to pin 19.</p>	<ul style="list-style-type: none"> Output terminal of IF limiter Amp. A Resistor (R_{THD}) can minimize a distortion value on a set. (Detection output level is also varied by an above adjustment.)
19	Det1	3.0	<p>The diagram shows a differential peak detection circuit. Pin 19 is connected to a resistor R_D. The other end of R_D is connected to the inverting input of a third operational amplifier. The output of the third op-amp is connected to pin 19. The non-inverting input of the third op-amp is connected to the output of the second op-amp. The outputs of the second and third op-amps are connected to a common node, which is then connected to pin 20.</p>	<ul style="list-style-type: none"> Differential peak Detection for conducting FM Detection. Connect a Resistor & a capacitor. Resistor (R_D) controls a Detection output level with a fixed Detection Band width. (Station Detection Band width is also varied by an above control.)
20	Det2	3.0		
21	B1	8	<p>The diagram shows a simple circuit for pin 21. Pin 21 is connected to a 0.1μF capacitor and ground.</p>	<ul style="list-style-type: none"> Supply terminal of IF/NC stages. VCO is Stopping in case of B1 = "L" level. Connect a capacitor.

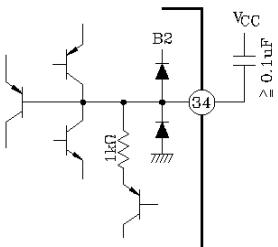
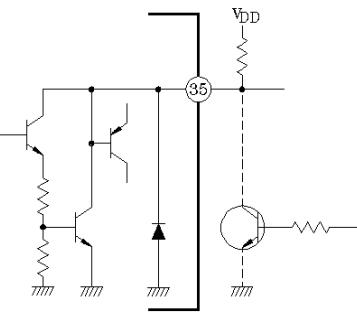
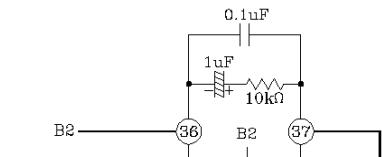
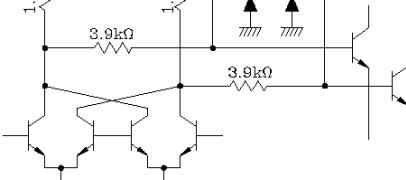
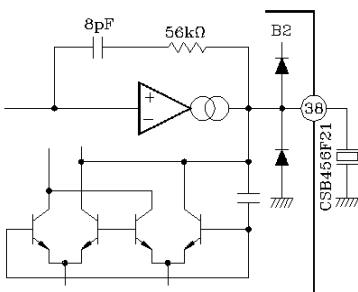
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No.	Name	Terminal Voltage	Equivalent circuit/Typical Value of external port (Internal R/C Value)	Function
22	AFout	4.8		<ul style="list-style-type: none"> Output terminal of an audio signal to be modulated by a FM IF signal. By externally mounting R between pin 16, detection output can be reduced without changing detection bandwidth and station detecting bandwidth.
23	G2	0		<ul style="list-style-type: none"> GND terminal for differential peak detection, reference voltage, meter, Δf, detector station detection, Muting, and Noise canceller.
24	NCin	3.0		<ul style="list-style-type: none"> Noise canceller input. Connect a capacitor.
25	Ref2	3.0		<ul style="list-style-type: none"> Ref. 2 voltage terminal. Connect a capacitor.
26	By2	3.0		<ul style="list-style-type: none"> AGC Amp. by pass terminal for NC section. Noise AGC high sensitivity by a small resistor value.
27	By3	2.3		<ul style="list-style-type: none"> Noise Amp. By-pass terminal. Connect a capacitor.

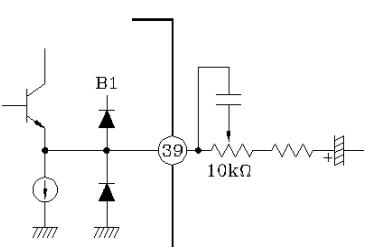
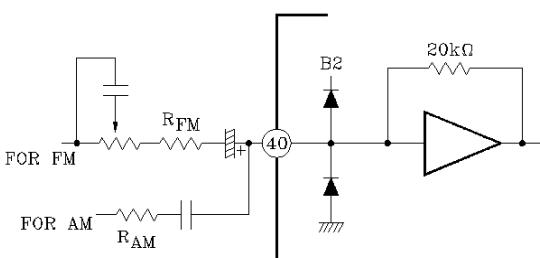
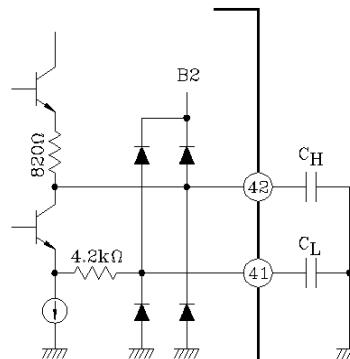
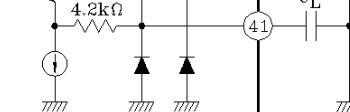
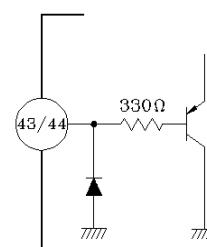
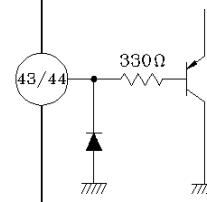
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No.	Name	Terminal Voltage	Equivalent circuit/Typical Value of external port (Internal R/C Value)	Function
28	AGC	0		<ul style="list-style-type: none"> Adjustment of a Noise AGC time constant on NC stage. Connect a capacitor and a resistor. Have a good effect on a Noise with a large periodicity by a large resistor value.
29	PW	0		<ul style="list-style-type: none"> Adjustment of a Triger Pulse width on NC stage. Connect a capacitor/resistor (A large resistor value is to widen a pulse width)
30	G3	0		•GND for MPX Stage.
31	Hold	3.8		<ul style="list-style-type: none"> Composite signal holding terminal on NC Stage. Connect a capacitor.
32	B2	8.0		<ul style="list-style-type: none"> Power Supply for MPX stage Slider circuit is operating in case of B1="L" and B2="H" (in AM mode, also the slider circuit is operating) Connect a capacitor.
33	P	3.0		<ul style="list-style-type: none"> Cancel signal output terminal for pilot canceller. Connect a capacitor.

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No.	Name	Terminal Voltage	Equivalent circuit/Typical Value of external port (Internal R/C Value)	Function
34	PD3	7.0		<ul style="list-style-type: none"> Pilot detection output. By connecting to B2(Vcc), VCO stopping and forced monaural mode are done (also receiving mode output (pin35) is done forced monaural mode at same time) It is desirable to connect a capacitor with a lower temperature coefficient.
35	Mode	8.0		<ul style="list-style-type: none"> Receiving mode output for Stereo /Monaural mode. The current (around 1mA) flows into this pin by connecting the VDD through a resistor. In case that this pin pass "L" level and there is no flowing current to this pin, this pin makes a forced monaural mode (The PLL circuit is operating)
36	PD1	7.3		<ul style="list-style-type: none"> Phase comparator output. Connect a capacitor and a resistor. It is desirable to connect a capacitor with a lower temperature coefficient.
37	PD2	7.3		
38	VCO	4.2		<ul style="list-style-type: none"> VCO oscillating terminal of nonadjusting type. Connect the resonator (Type No. CSB456F21, MURATA MFG. Co., LTD.)

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No.	Name	Terminal Voltage	Equivalent circuit/Typical Value of external port (Internal R/C Value)	Function
39	NCout	2.4		<ul style="list-style-type: none"> • NC output. • Connect a phase correction circuit to set a maximum separation characteristic.
40	MPXin	1.8		<ul style="list-style-type: none"> • MPX Pre-Amp, input for FM composite signal. • It is possible to connect this pin to other signal line by a inverter input structure (with a OP Amp circuit.)
41	LC	1.8		<ul style="list-style-type: none"> • Adjustment a cut-off frequency. • Low cut frequency adjustment by a capacitor (CL), high cut frequency adjustment by a capacitor(CH).
42	HC	3.4		<ul style="list-style-type: none"> • Low cut cancellation by a capacitor(CL) with a large capacity value. • High cut cancellation by open circuit of pin 4.
43	TC	3.7		<ul style="list-style-type: none"> • Tone(Low cut/High cut) control • Low cut control and High cut control are operating at same time. • Impedance should be less than 50kΩ.
44	BC	1.6		<ul style="list-style-type: none"> • Blender control • Impedance should be less than 50kΩ. • Grounded this pin is for a forced monaural mode.

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ELECTRICAL CHARACTERISTICS

IF BLOCK

(Unless otherwise specified, VCC=8V, SW8-FM, Ta=25°C, SW1=SW2=ON, fi(IF)=10.7MHz,
Dev.=±75kHz, fm(IF)=400Hz, Vi(IF)=80dB μ)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Current (FM Mode)	I _{cc1}	V _i (IF)=-20dB μ , SW1=off	30	45	55	mA
Limiting Snensitivity	V _i (lim)1		36	40	43	dB μ
Detection Output Level	V _O	0dB=450mV	-2	0	2	dB
Signal to Noise Ratio	S/N1	Dev.=±75kHz → 0kHz	65	75	-	dB
Total Harminic Distortion	THD1	Dev.=±22.5kHz	-	-54	-46	dB
AM Rejection Ratio	AMR	Dev.=±75kHz → AM=30%, 1kHz, V _i (IF)=100dB μ	47	65	-	dB
Meter Output Level	V _{M1}	Dev.=0kHz	V _i (IF)=40dB μ	0	0.1	0.5
	V _{M2}		V _i (IF)=60dB μ	1.0	1.5	2.0
	V _{M3}		V _i (IF)=80dB μ	2.7	3.3	3.8
	V _{M4}		V _i (IF)=100dB μ	4.9	5.4	6.0
Meter Slider Output Level	V _{SO}			0.8	2.0	2.8
Soft Muting Sensitivity	V _i (lim)2	SW2=off		43	50	58
Soft Muting Attenuation	ATT1		V _i (IF)=80 → 0dB μ	45	55	65
Detuning Muting Attenuation	ATT2	SW1=off SW2=off	fi(IF)=10.7MHz → 10.8MHz	34	45	56
			fi(IF)=10.7MHz → 10.6MHz			dB
Detuning Muting Band Width	BW(M)			105	130	165
Station Detection Band Width	BW(S)	Dev.=0kHz, V _{SP} =8V SW1=off, SW2=off		43	55	73
Station Detection Sensitivity	V _{SS}			45	52	61
IF Count Request Threshold Voltage	V _S (IF)		V _O (IF)=35mW	1.5	2.5	-
IF Count Output Level	V _O (IF)		V _{req} =5Vdc	150	250	-
						mV

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ELECTRICAL CHARACTERISTICS

NC, MPX BLOCK

(Unless otherwise specified, $V_{CC}=8V$, SW8=FM, $T_a=25^\circ C$, SW3=SW4=SW5=SW7=A, SW6=R, $f_i=1kHz$, $V_{I(MPX)}=360mV$ (Main)+40mV(pilot), $V_i(AM)=0$, $V_{BC}=V_{TC}=1.5V$)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gain	G_{V1}	$V_i(MPX)=400mV$ (Monaural)	-3	-1	0	dB
Channel Balance	CB	SW6=R→L	-1.0	0	1.0	dB
Max. Compositte Input Level	$V_i(MAX)$	(Maom/Pilot)=9, THD=-40dB	700	1000	-	mV
Monaural THD	THD2	$V_i(MPX)=400mV$ (Monaural)	-	-60	-48	dB
Stereo THD	THD3		-	-60	-50	dB
Separation	Sep	$V_i(MPX)=360mV$ (Ronly)+40mV(Pilot) SW5=B, SW6=R→L $V_i(MPX)=360mV$ (Lonly)+40mV(Pilot) SW5=B, SW6=L→R	40	50	-	dB
Stereo S/N	S/N2	$V_i(MPX)=360mV$ (Main)→0	72	80	-	dB
Stereo Signal tp Residual Pilot Signal Ratio	S/N3	$V_i(MPX)=360mV$ (Main)→0, SW7=B	37	46	-	dB
Signal to Residual Pulse Noise Ratio	S/N4	SW3=A→B at A $V_i(MPX)=400mV$ (Monaural) at B $V_i(NC)=100mVpp$ $t_w=10\mu S$, $f_i=1kHz$	70	80	-	dB
Gate Open Time	t_G	$V_i(NC)=100mVpp$, $t_w=10\mu S$, $f_i=1kHz$	-	33	-	μS
Pilot Sensitivity	V_S (Pilot)	Turned LED on	6	9	15	mV
Pilot Sensitivity Hysteresis	hy		-	1	3	dB
Stereo Operating Current	I_S	$I_S=V_S/1k$, $V_i(MPX)=360mV$ (Lonly)+40mV(Pilot) at Sep=40dB, SW4=B, SW5=B	-	0.1	0.2	mA

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CHARACTERISTIC	SYMBOL	TEST CONDITION			MIN.	TYP.	MAX.	UNIT	
Baender Control Attenuation	ATT3	V _{BC} =0.9V	Vi(MPX)= 360mV(Sub)+40mV(Pilot)		0	0.5	2.0	dB	
	ATT4	V _{BC} =0.9V			35	45	-		
Tone Control Attenuation	ATT5	V _{Tc} =0.7V	f _i =100Hz	Vi(MPX) =400mV (Monaural)	0	0.5	1.8	dB	
	ATT6	V _{Tc} =0.2V			4.5	7	10.5		
	ATT7	V _{Tc} =0.7V	f _i =10kHz		0	0.5	1.8		
	ATT8	V _{Tc} =0.2V			4	7	10		
Capture Range	CR	Vi(MPX)=40mV(Pilot)			-	+0.8 -1.0	-	%	
Supply Current (AM Mode)	I _{cc2}	SW8=AM	Vi(AM)=Vi(MPX)=0		-	20	25	mA	
Gain (AM Mode)	G _{v2}		Vi(AM)=400mV, f=1kHz, Vi(MPX)=0		-3	-1	0	dB	

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OTHER FUNCTION EXPLANATION AND APPLICATION

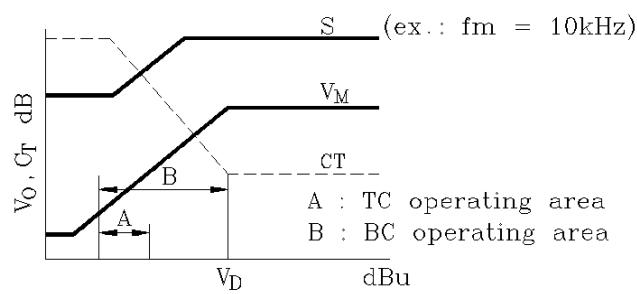
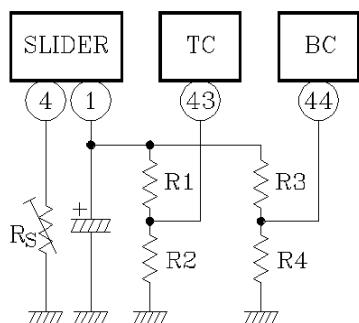
1. Slider application in FM mode

The slider function can slide freely a rising point of Meter output characteristic with a fixed inclination of the Meter output characteristic.

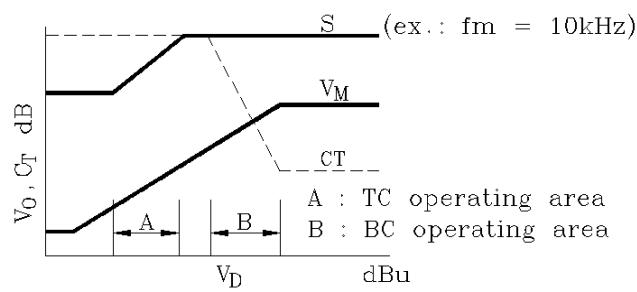
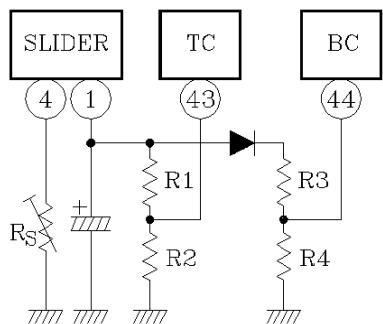
So, the Slider function can set the position of a operating midth for Blender Control(BC) and Tone Control(TC) according to a using place.

Adjusting the BC, TC with the slider function at same time, the slider function can lose a gain diviation of the Front End Stage etc....

a. In case of lengthening a BC operating area till a weak electric field.



b. In case of lengthening a BC operating area till a middle electric field.



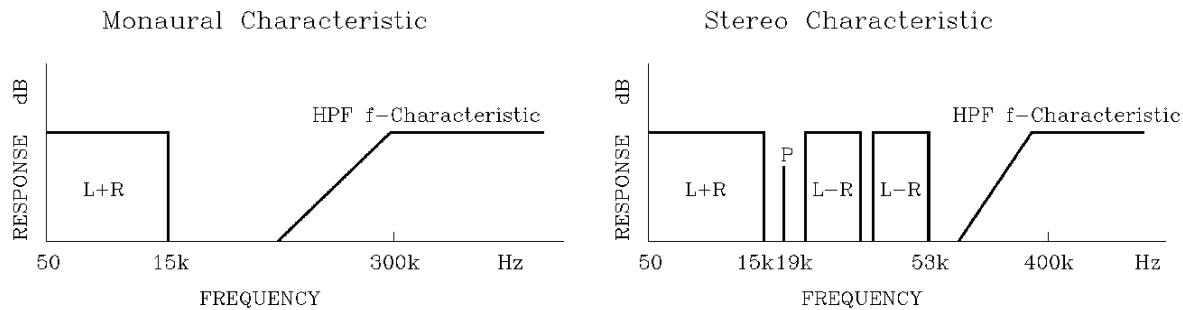
c. Note

- (1) Resistance value(in pin 1) on the parallel circuit should be about $10k\Omega$.
(for the same inclination as between slider output characteristic and meter output characteristic.)
- (2) Resistance value(in pin 43,44) on the parallel circuit should be less than $50k\Omega$.
(for adjusting the BC,TC correctly)

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2. HPF Frequency characteristic selection on a receiving mode at NC stage.

Selecting a frequency characteristic of HPF by stereo mode or mono mode, a detection precision of a pulse noise at each mode is improved, also a noise cancel effect is improved.



3. Auto Pilot Canceller

Building in nonadjustment type auto Pilot canceller with a negative feedback(NF).
it is unnecessary a variable resistor.

4. Anti-ARI type PLL circuit

A usual PLL circuit uses a dividing signal with a square wave. As that signal has many add harmonic component, that signal is disturbed by 57kHz wave(3 times of 19kHz Pilot Signal) and side band wave (ex. ARI-SK/RDS signals)

In the KIA6072AF, as a dividing signal does not have tertiary harmonic component, that signal is not disturbed.

5. Stereo decoder circuit of Anti-Birdy.

In an usual Stereo Decoder circuit, demodulating a L/R signal by switching 38kHz square wave, a beat trouble and a of S/N lowering are occurred by and add harmonic component of 38kHz in to the stereo mode.

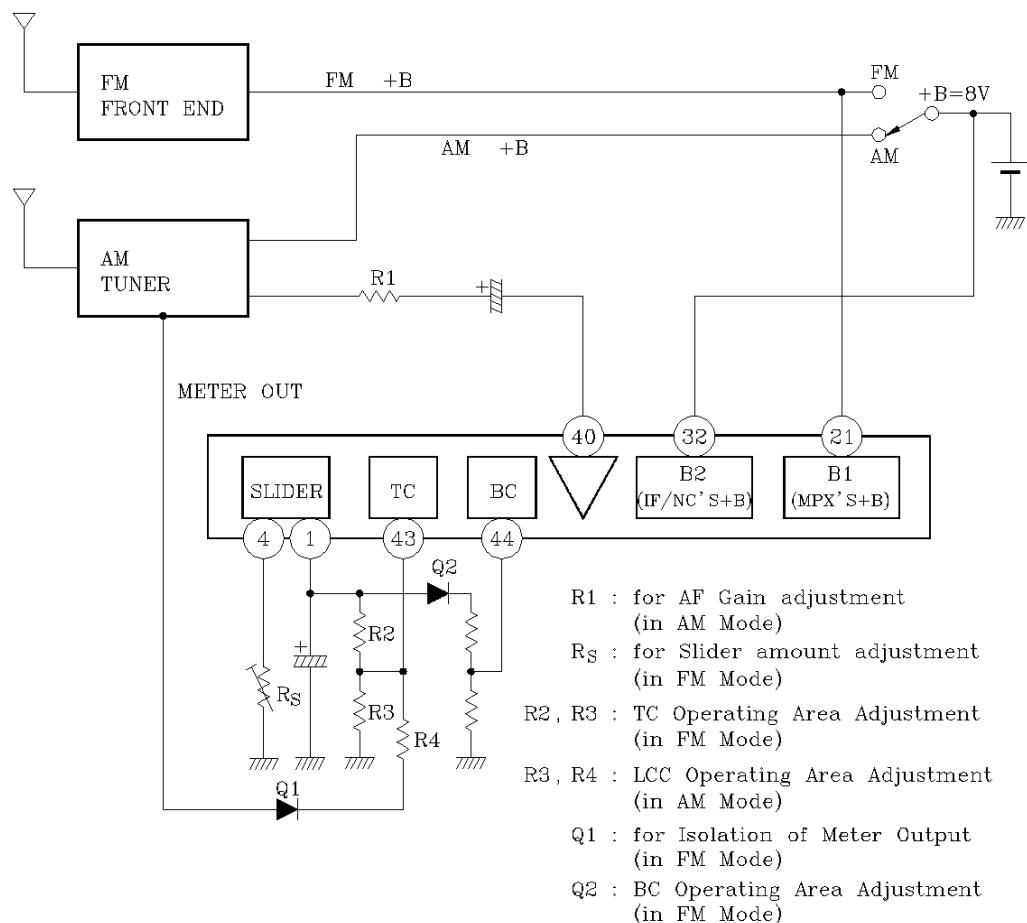
The KIA6072AF built in the stereo decoder circuit under no influence of a tertiary 114kHz Band, prevents a worse of S/N by an interference of adjacent channel signal and an intermodulation interference and a multipath interference.

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6. Low Cut Control

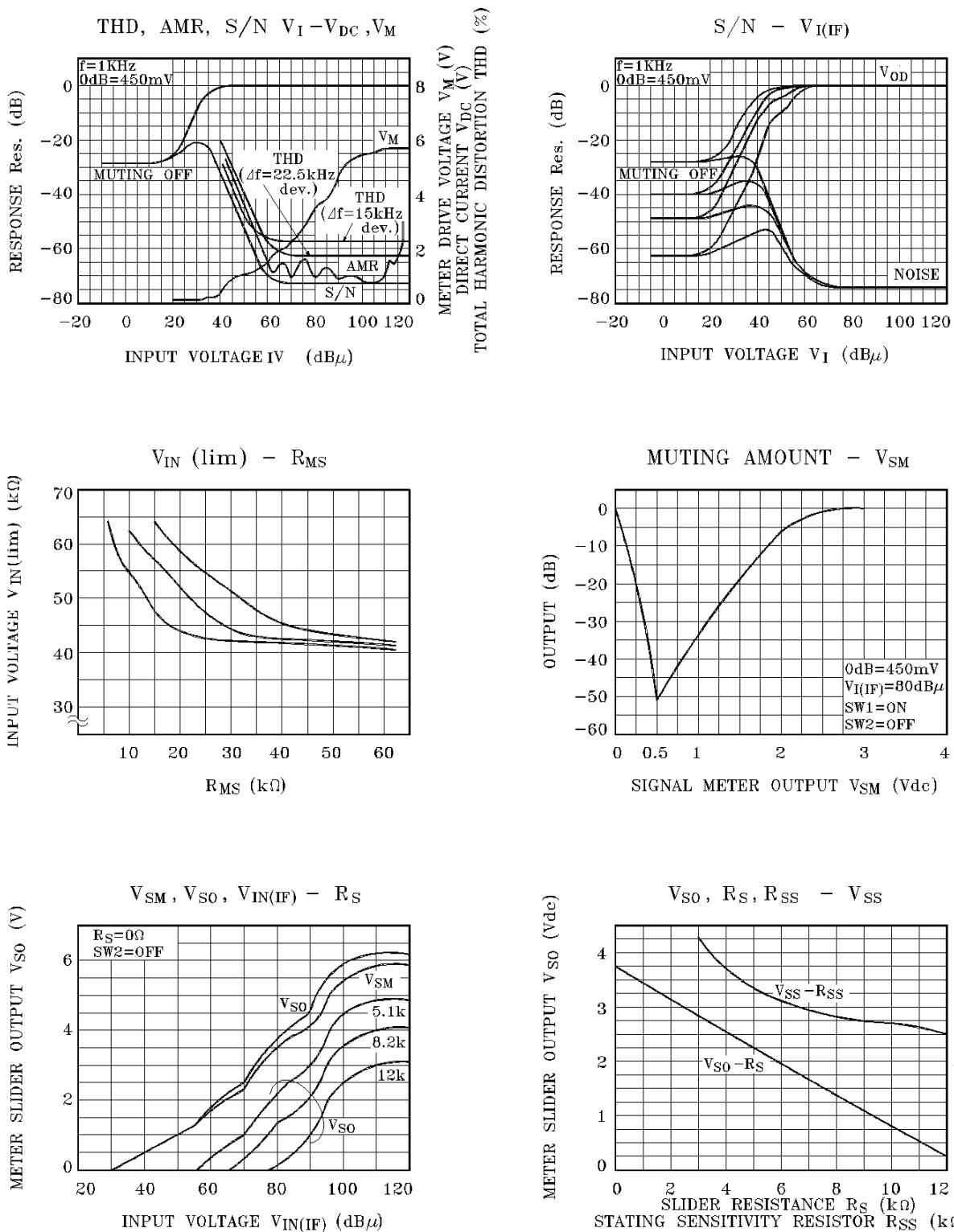
The KIA6072AF built-in High Cut Control(HCC) and Low Cut Control(LCC). So, in case of a mobile-receiving a music to lengthen till a Lower/Higher frequency, and having a distributor function(1 input to 2 outputs) at MPX Stage, distribute a AM tuner demodulation output to a L/R signals and also TC is operated at same time, and S/N, practical sensitivity on a weak electric field are improved by the LCC function.

Regarding a LCC application in AM mode, it is necessary to construct an application circuit based on the above mentioned "Slider Application in FM mode".



In case of B1= "L" level in AM mode, use Slider input terminal (pin 10) which AM tuner output signal goes to the slider circuit (but, In case of FM mode, this pin is for meter output).

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