## FM811/812

## Microprocessor Reset Circuits

## Features

- Feature
- Precision Voltage Monitor for 3V, 3.3V or 5V Power Supplies
- $6 \mu \mathrm{~A}$ Supply Current
- 140 ms Minimum Reset Pulse Width

- Active Low Manual Reset Input
- No External Components
- 4-Pin SOT-143 Package


## Applications

- Critical Microprocessor Power Monitoring
- Portable Equipment
- Intelligent Instruments
- Computers \& Printers
- Controllers


## Description

The FM811/FM812 is a low cost microprocessor supervisory circuit that asserts a reset if the power supply drops below a designated threshold or the manual reset pin is forced low. Several different reset thresholds are available to accommodate systems operating at $3 \mathrm{~V}, 3.3 \mathrm{~V}$ or 5 V .

The FM811 has an active low $\overline{\text { RESET }}$ output, while the FM812 offers an active high RESET output. The reset output is guaranteed to remain asserted for a minimum of 140 ms after $\mathrm{V}_{\mathrm{CC}}$ has risen above the designated reset threshold. The FM811/FM812 is available in a 4-pin SOT-143.

## Typical Applications



## Pin Assignments

Top View


## Pin Definitions

| Pin Name | Pin Number |  | Description |
| :---: | :---: | :---: | :---: |
|  | FM811 | FM812 |  |
| GND | 1 | 1 | Ground Pin |
| $\overline{\text { RESET }}$ | 2 | N/A | $\overline{\text { RESET }}$ goes low if $\mathrm{V}_{\text {CC }}$ falls below the reset threshold and remains asserted for one reset time-out period ( 140 ms min.) after $\mathrm{V}_{\mathrm{CC}}$ exceeds the reset threshold. |
| RESET | N/A | 2 | RESET goes high if $\mathrm{V}_{\mathrm{CC}}$ falls below the reset threshold and remains asserted for one reset time-out period ( 140 ms min.) after $\mathrm{V}_{\mathrm{CC}}$ exceeds the reset threshold. |
| $\overline{\mathrm{MR}}$ | 3 | 3 | Manual reset input. A logic low on $\overline{\mathrm{MR}}$ forces a reset. The reset will remain asserted as long as MR is held low and for one reset time-out period ( 140 ms min.) after $\overline{\mathrm{MR}}$ goes high. This input can be shorted to ground via a switch or driven from CMOS or TTL logic. Float if unused. |
| $\mathrm{V}_{\mathrm{CC}}$ | 4 | 4 | Power supply input, 3V, 3.3V, or 5V |

## Internal Block Diagram



Absolute Maximum Ratings

| Parameter | Symbol | Ratings | Units |
| :--- | :---: | :---: | :---: |
| Terminal Voltage | $\mathrm{V}_{\mathrm{CC}}$ | -0.3 to 6.0 | V |
|  | $\overline{\mathrm{MR}}$ | -0.3 to $\left(\mathrm{V}_{\mathrm{CC}}+0.3\right)$ | V |
| Input Current | $\mathrm{V}_{\mathrm{CC}}, \overline{\mathrm{MR}}$ | 20 | mA |
| Output Current | $\mathrm{RESET}, \overline{\mathrm{RESET}}$ | 20 | mA |
| Rate of Rise | $\mathrm{V}_{\mathrm{CC}}$ | 100 | $\mathrm{~V} / \mathrm{ss}$ |
| Operating Temperature Range | $\mathrm{T}_{\mathrm{A}}$ | -40 to +85 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature Range |  | -65 to +150 | ${ }^{\circ} \mathrm{C}$ |
| Lead Temperature Range |  | 300 | ${ }^{\circ} \mathrm{C}$ |
| Power Dissipation $\left(\mathrm{T}_{\mathrm{A}}=+70^{\circ} \mathrm{C}\right)$ | 320 | mW |  |

Stresses above those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent device failure. Functionality at or above these limits is not implied. Exposure to absolute maximum ratings for extended periods may affect device reliability. Operating ranges define those limits between which the functionality of the device is guaranteed.

## Electrical Characteristics

$\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ for $\mathrm{FM} 81 \_\mathrm{L} / \mathrm{M} / \mathrm{J}, \mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V}$ for $\mathrm{FM} 81 \_\mathrm{S} / \mathrm{T}, \mathrm{V}_{\mathrm{CC}}=3 \mathrm{~V}$ for $\mathrm{FM} 81 \_\mathrm{R}, \mathrm{T}_{\mathrm{A}}=$ Operating Temperature Range, unless otherwise noted.

| Parameter | Conditions | Min. | Typ. | Max. | Units |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Operating Voltage Range | $\begin{aligned} & \mathrm{T}_{\mathrm{A}}=0^{\circ} \mathrm{C} \text { to } 70^{\circ} \mathrm{C} \\ & \mathrm{~T}_{\mathrm{A}}=-40^{\circ} \mathrm{C} \text { to } 85^{\circ} \mathrm{C} \end{aligned}$ | $\begin{aligned} & 1.4 \\ & 1.6 \end{aligned}$ |  | $\begin{aligned} & 5.5 \\ & 5.5 \end{aligned}$ | V |
| Supply Current, $\mathrm{I}_{\text {CC }}$ | FM811L/M/J, FM812L/M/J $\mathrm{V}_{\mathrm{CC}}<3.6 \mathrm{~V}, \mathrm{FM} 811 \mathrm{R} / \mathrm{S} / \mathrm{T}$, FM812R/S/T |  | $\begin{aligned} & 9 \\ & 6 \end{aligned}$ | $\begin{aligned} & 15 \\ & 10 \end{aligned}$ | $\mu \mathrm{A}$ |
| Reset Voltage Threshold, $\mathrm{V}_{\mathrm{TH}}$ | FM811L, FM812L FM811M, FM812M FM811J, FM812J FM811T, FM812T FM811S, FM812S FM811R, FM812R | $\begin{aligned} & 4.50 \\ & 4.25 \\ & 3.89 \\ & 3.00 \\ & 2.85 \\ & 2.55 \end{aligned}$ | $\begin{aligned} & 4.63 \\ & 4.38 \\ & 4.00 \\ & 3.08 \\ & 2.93 \\ & 2.63 \end{aligned}$ | $\begin{aligned} & 4.75 \\ & 4.50 \\ & 4.10 \\ & 3.15 \\ & 3.00 \\ & 2.70 \end{aligned}$ | V |
| Reset Time-out Period, $\mathrm{t}_{\mathrm{R}}$ |  | 140 | 240 | 560 | ms |
| RESET Output Voltage, $\mathrm{V}_{\text {OH }}$ | $I_{\text {SOURCE }}=800 \mu \mathrm{~A}, \mathrm{FM} 811 \mathrm{~L} / \mathrm{M} / \mathrm{J}$ $I_{\text {SOURCE }}=500 \mu \mathrm{~A}, \mathrm{FM} 811 \mathrm{R} / \mathrm{S} / \mathrm{T}$ | $\begin{aligned} & \hline \mathrm{V}_{\mathrm{CC}}-1.5 \\ & 0.8 \times \mathrm{V}_{\mathrm{CC}} \end{aligned}$ |  |  | V |
| $\overline{\text { RESET Output Voltage, } \mathrm{V}_{\mathrm{OL}} \text { }}$ | $\mathrm{V}_{\mathrm{CC}}=\mathrm{V}_{\text {TH }}$ Min., $\mathrm{I}_{\mathrm{SINK}}=3.2 \mathrm{~mA}$, FM811L/M/J <br> $\mathrm{V}_{\mathrm{CC}}=\mathrm{V}_{\mathrm{TH}}$ Min., $\mathrm{I}_{\text {SINK }}=1.2 \mathrm{~mA}, \mathrm{FM} 811 \mathrm{R} / \mathrm{S} / \mathrm{T}$ <br> $\mathrm{V}_{\mathrm{CC}}>1.4 \mathrm{~V}, \mathrm{I}_{\mathrm{SINK}}=50 \mu \mathrm{~A}, \mathrm{~T}_{\mathrm{A}}=0^{\circ} \mathrm{C}$ to $70^{\circ} \mathrm{C}$ <br> $\mathrm{V}_{\mathrm{CC}}>1.6 \mathrm{~V}, \mathrm{I}_{\text {SINK }}=50 \mu \mathrm{~A}, \mathrm{~T}_{\mathrm{A}}=-40^{\circ} \mathrm{C}$ to $85^{\circ} \mathrm{C}$ |  |  | $\begin{aligned} & 0.4 \\ & 0.3 \\ & 0.3 \\ & 0.3 \end{aligned}$ | V |
| RESET Output Voltage, $\mathrm{V}_{\text {OH }}$ | $1.8 \mathrm{~V}<\mathrm{V}_{\text {CC }}<\mathrm{V}_{\text {TH }}$ Min., $\mathrm{I}_{\text {SOURCE }}=150 \mu \mathrm{~A}$ | $0.8 \times \mathrm{V}_{\text {C }}$ |  |  | V |
| RESET Output Voltage, $\mathrm{V}_{\mathrm{OL}}$ | $\mathrm{I}_{\mathrm{SINK}}=3.2 \mathrm{~mA}, \mathrm{FM} 810 \mathrm{~L} / \mathrm{M} / \mathrm{J}$ <br> $\mathrm{I}_{\mathrm{SINK}}=1.2 \mathrm{~mA}, \mathrm{FM} 810 \mathrm{R} / \mathrm{S} / \mathrm{T}$ |  |  | $\begin{aligned} & 0.4 \\ & 0.3 \end{aligned}$ | V |
| $\overline{\text { MR Minimum Pulse Width }}$ |  | 10 |  |  | $\mu \mathrm{s}$ |
| $\overline{\mathrm{MR}}$ to Reset Delay |  |  | 0.5 |  | $\mu \mathrm{s}$ |
| $\overline{\mathrm{MR}}$ Input Threshold, $\mathrm{V}_{\mathrm{IH}}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}>\mathrm{V}_{T H} \text { Max., FM81_L/M/J } \\ & \text { FM81_R/S/T } \end{aligned}$ | $\begin{gathered} 2.3 \\ 0.7 \times \vee_{C C} \end{gathered}$ |  |  | V |
| $\overline{\overline{M R}}$ Input Threshold, $\mathrm{V}_{\text {IL }}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}>\mathrm{V}_{\text {TH }} \text { Max., FM81_L/M/J } \\ & \text { FM81_R/S/T } \end{aligned}$ |  |  | $\begin{gathered} 0.8 \\ 0.25 \times \mathrm{V}_{\mathrm{CC}} \end{gathered}$ |  |
| $\overline{\text { MR Pull-Up Resistance }}$ |  | 10 | 20 | 30 | k $\Omega$ |
| $\overline{\mathrm{MR}}$ Glitch Immunity |  |  | 100 |  | ns |

## Circuit Description

## Microprocessor Reset

The $\overline{\text { RESET }}$ pin is asserted whenever $\mathrm{V}_{\mathrm{CC}}$ falls below the reset threshold voltage or if $\overline{\mathrm{MR}}$ (manual reset) is forced low. The reset pin remains asserted for a period of 240 ms after $\mathrm{V}_{\mathrm{CC}}$ has risen above the reset threshold voltage or $\overline{\mathrm{MR}}$ has returned high. The reset function ensures the microprocessor is properly reset and powers up into a known condition after a power failure. $\overline{\text { RESET }}$ will remain valid with $\mathrm{V}_{\mathrm{CC}}$ as low as 1.4 V .


## $\mathrm{V}_{\mathrm{cc}}$ Transients

The FM811/ FM812 are relatively immune to negativegoing $\mathrm{V}_{\mathrm{CC}}$ glitches below the reset threshold. Typically, a negative-going transient 125 mV below the reset threshold with a duration of 50 ms ( 25 ms for FM81_R/S/T) or less will not cause an unwanted reset.

## Interfacing to Bidirectional Reset Pins

The FM811/ FM812 can interface with $\mu$ Ps with
bidirectional reset pins by connecting a $4.7 \mathrm{k} \Omega$ resistor in series with the FM811/ FM812 output and the $\mu$ P reset pin.


## RESET Valid to OV

A resistor can be added from the $\overline{\text { RESET }}$ pin to ground to ensure the $\overline{\text { RESET }}$ output remains low with $\mathrm{V}_{\mathrm{CC}}$ down to 0 V . A $100 \mathrm{k} \Omega$ resistor connected from $\overline{\mathrm{RESET}}$ to ground is recommended. The size of the resistor should be large enough to not load the RESET output and small enough to pull-down any stray leakage currents.

## Packaging Information

## 4-Pin SOT-143



Dimensions are in inches


Tape and Reel Information


Dimensions are in millimeters

## Ordering Information

| PART <br> NUMBER | TOP <br> MARKING* | RESET <br> THRESHOLD (V) | OUTPUT TYPE | PACKAGE | PACKING <br> METHOD |
| :---: | :---: | :---: | :---: | :---: | :---: |
| FM811LUX | KLYY | 4.63 | Push-Pull, active LOW | 4-Pin, SOT143 | 3000 units in T\&R |
| FM811MUX | KMYY | 4.38 | Push-Pull, active LOW | 4-Pin, SOT143 | 3000 units in T\&R |
| FM811JUX | KJYY | 4.00 | Push-Pull, active LOW | 4-Pin, SOT143 | 3000 units in T\&R |
| FM811TUX | KTYY | 3.08 | Push-Pull, active LOW | 4-Pin, SOT143 | 3000 units in T\&R |
| FM811SUX | KSYY | 2.93 | Push-Pull, active LOW | 4-Pin, SOT143 | 3000 units in T\&R |
| FM811RUX | KRYY | 2.63 | Push-Pull, active LOW | 4-Pin, SOT143 | 3000 units in T\&R |
| FM812LUX | LLYY | 4.63 | Push-Pull, active HIGH | 4-Pin, SOT143 | 3000 units in T\&R |
| FM812MUX | LMYY | 4.38 | Push-Pull, active HIGH | 4-Pin, SOT143 | 3000 units in T\&R |
| FM812JUX | LJYY | 4.00 | Push-Pull, active HIGH | 4-Pin, SOT143 | 3000 units in T\&R |
| FM812TUX | LTYY | 3.08 | Push-Pull, active HIGH | 4-Pin, SOT143 | 3000 units in T\&R |
| FM812SUX | LSYY | 2.93 | Push-Pull, active HIGH | 4-Pin, SOT143 | 3000 units in T\&R |
| FM812RUX | LRYY | 2.63 | Push-Pull, active HIGH | 4-Pin, SOT143 | 3000 units in T\&R |

* YY = Lot Code


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