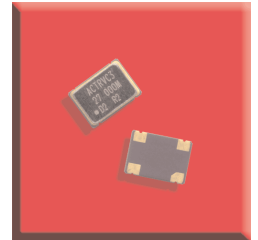


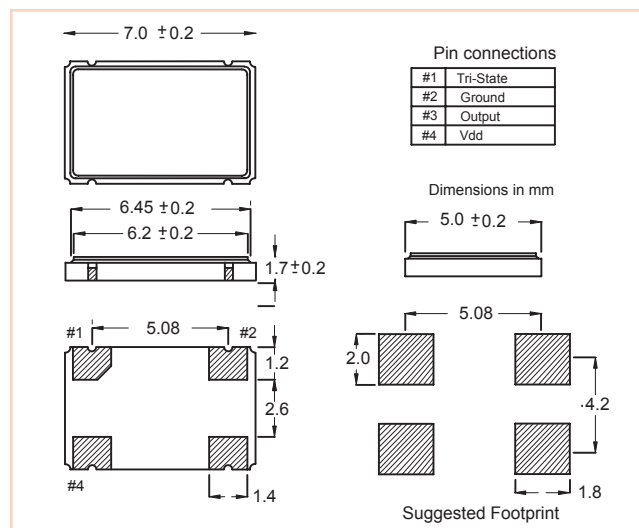
ACT9200

The Act9200 family is a miniature low profile Surface Mount package, housed in a ceramic leadless chip carrier with a seam welded metal lid.

This 7 x 5mm device can be used in a wide range of applications where board space dictates small size and automatic assembly is a requirement. It is suitable for design into many applications including Communications equipment, Audio Visual units, Measuring Instruments and many more



Parameter	Symbol	Specification		Condition
Supply Voltage	V _{DD}	3.3V_{DC} ± 10%		5.0V_{DC} ± 10%
Frequency Range	f ₀	1.000 ~ 155.00MHz	1.500 ~ 106.250MHz	
Frequency Stability	Δf/f ₀	±25ppm ~ ±100ppm		(& ±15ppm 3.3V)
Temp Operating Range	T _{opr}	0 ~ +70°C Std	(-40 ~ +85°C available)	
Temp Storage Range	T _{stg}	-55 to +125°C		
Supply Current (max)	I _{op}	16mA (1.000 ~ 34.999MHz) 25mA (35.000 ~ 60.00MHz) 40mA (60.001 ~ 99.999MHz) 50mA (100.00 ~ 155.00MHz)	20mA (1.50 ~ 23.90MHz) 40mA (24.00 ~ 49.99MHz) 50mA (50.00 ~ 59.90MHz) 60mA (90.00 ~ 99.90MHz) 80mA (100.00 ~ 106.25MHz)	
Duty Cycle	T _w /t	40/60% (45/55% available)		Please specify
Output Level '0'	V _{OH}	0.4V _{DC} max (TTL)	10% V _{DD} max (HCMOS)	
Output Level '1'	V _{OL}	2.4V _{DC} min (TTL)	90% V _{DD} min (HCMOS)	
Rise & Fall Time (max)	T _r /T _f	10nS (1.000 ~ 34.999MHz) 5nS (35.00 ~ 99.999MHz) 2.5nS x (100.00 ~ 157.00MHz)	10nS (1.50 ~ 49.999MHz) 5nS (50.00MHz ~ 70.00MHz) 3nS (100.00 ~ 106.25MHz)	
Output Load		10TTL / 15pF (30pF available)		Please specify
Star-up Time		10mS max		
Enable / Disable Function		Pin 1	Pin 3	
(Pin 1 : E/D Control Pin)		OPEN	Active	
(Pin 3 : Output Pin)		2.2V	Active	
		0.8V	High Impedance	
Aging		±3ppm, ±5ppm / year max		@25°C
Period Jitter (Absolute)		100pS max		
Period Jitter (one sigma)		25pS max		



Please note that not all parameters can necessarily be specified in a single device

ISO9002 Registered - Registration number 6830/2

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