**TOSHIBA** MP4203

TOSHIBA POWER MOS FET MODULE SILICON P CHANNEL MOS TYPE (L2-π-MOSIII 4 IN 1)

# MP4203

HIGH POWER SWITCHING APPLICATIONS

HAMMER DRIVE, PULSE MOTOR DRIVE AND INDUCTIVE LOAD **SWITCHING** 

- 4V Gate Drive Available
- Small Package by Full Molding (SIP 10 Pin)
- High Collector Power Dissipation (4 Devices Operation)

 $: P_T = 4W (Ta = 25^{\circ}C)$ 

Low Drain-Source ON Resistance :  $R_{DS(ON)} = 0.3\Omega$  (Typ.)

Low Leakage Current :  $I_{GSS} = \pm 10 \mu A \text{ (Max.)} \text{ (V}_{GS} = \pm 16 \text{V)}$ 

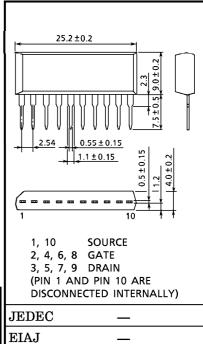
 $I_{DSS} = -100 \mu A \text{ (Max.) (V}_{DS} = -60 \text{ V)}$ 

:  $V_{th} = -0.8 \sim -2.0 V (I_D = -1 mA)$ Enhancement-Mode

### MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Drain-Source Voltage	$V_{ m DSS}$	-60	V
Gate-Source Voltage	VGSS	±20	V
Drain Current	$I_{\mathbf{D}}$	-5	A
Peak Drain Current	I <sub>DP</sub>	-10	A
Drain Power Dissipation (1 Device Operation)	$P_{\mathbf{D}}$	2.0	w
Drain Power Dissipation (4 Devices Operation)	$P_{DT}$	4.0	W
Chennel Temperature	$\mathrm{T_{ch}}$	150	°C
Storage Temperature Range	$\mathrm{T_{stg}}$	-55~150	°C

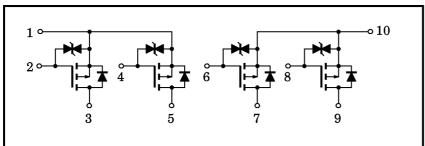
## INDUSTRIAL APPLICATIONS Unit in mm



JEDEC	_
EIAJ	<del>_</del>
TOSHIBA	2-25A1C

Weight: 2.1g (Typ.)

#### ARRAY CONFIGURATION



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## THERMAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance of Channel to Ambient (4 Devices Operation, Ta=25°C)	$\Sigma R_{ ext{th}( ext{ch-a})}$	31.2	°C/W
Maximum Lead Temperature for Soldering Purposes (3.2mm from Case for 10s)	${ m T_L}$	260	°C

This Transistor is an Electrostatic Sensitive Device. Please Handle with Caution.

# ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARAC	CTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage	Current	$I_{GSS}$	$V_{GS} = \pm 16V, V_{DS} = 0$	_	_	±10	$\mu$ A
Drain Cut-off	Current	$I_{ m DSS}$	$V_{DS} = -60V, V_{GS} = 0$	_	_	-100	$\mu$ A
Drain-Source I Voltage	Breakdown	V (BR) DSS	$I_D = -10 \text{mA}, V_{GS} = 0$	-60	_	_	V
Gate Threshol	d Voltage	$V_{ m th}$	$V_{DS} = -10V, I_D = -1mA$	-0.8	_	-2.0	V
Drain Sauras	ON Pagistanas	D	$V_{GS} = -4V, I_D = -2.5A$	_	0.45	0.8	
Drain-Source ON Resistance		R <sub>DS</sub> (ON)	$V_{GS} = -10V, I_D = -2.5A$	_	0.30	0.4	Ω
Forward Trans	sfer Admittance	$ Y_{fs} $	$V_{DS} = -10V, I_D = -2.5A$	1.0	2.0	_	S
Input Capacita	ance	$C_{iss}$		_	380	_	
Reverse Transfer Capacitance		$\mathrm{C}_{\mathrm{rss}}$	$V_{DS} = -10V, V_{GS} = 0, f = 1MHz$	_	90	-	pF
Output Capacitance		Coss		_	270	_	
Switching Time	Rise Time	t <sub>r</sub>	V <sub>GS</sub> 0 -10V ID=-2.5A V <sub>IN</sub> V <sub>OUT</sub> V <sub>OUT</sub> V <sub>OUT</sub> V <sub>OUT</sub> V <sub>OD</sub> = -30V	_	30	_	ns
	Turn-on Time	t <sub>on</sub>		_	50	_	
	Fall Time	tf		_	48	_	
	Turn-off Time	toff	$V_{ ext{IN}}:  ext{t}_{ ext{r}},  ext{t}_{ ext{f}} < 5  ext{ns} \  ext{Duty} \le 1\%   ext{t}_{ ext{W}} = 10 \mu  ext{s}$	_	120	_	
Total Gate Ch (Gate-Source I	arge Plus Gate-Drain)	$\mathbf{Q}_{\mathbf{g}}$	$V_{DD} = -48V, V_{GS} = -10V,$	_	20	_	
Gate-Source C	harge	$\mathbf{Q}_{\mathbf{g}\mathbf{s}}$	$I_{D} = -5A$	_	12	_	nC
Gate-Drain ("I	Gate-Drain ("Miller") Charge			_	8	_	

# SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Drain Reverse Current	${ m I}_{ m DR}$	_	_	_	-5	A
Peak Drain Reverse Current	$I_{ m DRP}$	_	_	_	-10	Α
Diode Forward Voltage	$V_{ m DSF}$	$I_{DR} = -5A$ , $V_{GS} = 0$	_	1.0	1.6	V
Reverse Recovery Time	t <sub>rr</sub>	$I_{DR} = -5A$ , $V_{GS} = 0$	_	170	_	ns
Reverse Recovery Charge	$Q_{rr}$	$dI_{ m DR}$ / $dt$ = $-20A$ / $\mu  m s$	_	0.42	_	$\mu$ C

