



STS 2622

SamHop Microelectronics Corp.

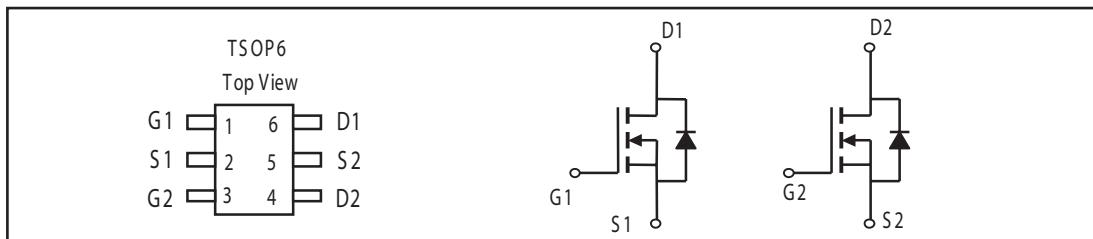
Feb,25 2005 Ver1.1

Dual N-Channel Enhancement Mode Field Effect Transistor

PRODUCT SUMMARY		
V _{DSS}	I _D	R _{DS(ON)} (mΩ) Max
20V	2.5A	80 @ V _{GS} = 4.5V 110 @ V _{GS} = 2.5V

FEATURES

- Super high dense cell design for low R_{DS(ON)}.
- Rugged and reliable.
- TSOP6 package.



ABSOLUTE MAXIMUM RATING (T_A=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	20	V
Gate-Source Voltage	V _{GS}	±10	V
Drain Current-Continuous ^a @ T _c =25°C -Pulsed ^b	I _D	2.5	A
	I _{DM}	8	A
Drain-Source Diode Forward Current ^a	I _S	1.25	A
Maximum Power Dissipation ^a	P _D	1	W
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55 to 150	°C

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient ^a	R _{thJA}	125	°C/W
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ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ ^c	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0V, I_D = 250\mu A$	20			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 16V, V_{GS} = 0V$		1		μA
Gate-Body Leakage	I_{GSS}	$V_{GS} = \pm 10V, V_{DS} = 0V$		± 100		nA
ON CHARACTERISTICS^b						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	0.5	0.8	1.5	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS} = 4.5V, I_D = 2A$		65	80	m-ohm
		$V_{GS} = 2.5V, I_D = 1A$		90	110	m-ohm
On-State Drain Current	$I_{D(ON)}$	$V_{DS} = 5V, V_{GS} = 4.5V$	6			A
Forward Transconductance	g_F	$V_{DS} = 5V, I_D = 2.5A$		7		S
DYNAMIC CHARACTERISTICS^c						
Input Capacitance	C_{ISS}	$V_{DS} = 10V, V_{GS} = 0V$ $f = 1.0MHz$		220		pF
Output Capacitance	C_{OSS}			67		pF
Reverse Transfer Capacitance	C_{RSS}			50		pF
SWITCHING CHARACTERISTICS^c						
Turn-On Delay Time	$t_{D(ON)}$	$V_{DD} = 10V,$ $I_D = 1A,$ $V_{GS} = 4.5V,$ $R_{GEN} = 6 \text{ ohm}$		10.2		ns
Rise Time	t_r			8.3		ns
Turn-Off Delay Time	$t_{D(OFF)}$			13.5		ns
Fall Time	t_f			12.7		ns
Total Gate Charge	Q_g	$V_{DS} = 10V, I_D = 2.5A,$ $V_{GS} = 4.5V$		4		nC
Gate-Source Charge	Q_{gs}			1.5		nC
Gate-Drain Charge	Q_{gd}			0.7		nC

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ELECTRICAL CHARACTERISTICS ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ ^c	Max	Unit
DRAIN-SOURCE DIODE CHARACTERISTICS ^b						
Diode Forward Voltage	V_{SD}	$V_{GS} = 0V, I_S = 1.25A$		0.84	1.2	V

Notes

- a. Surface Mounted on FR4 Board, $t \leq 10\text{sec}$.
- b. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
- c. Guaranteed by design, not subject to production testing.

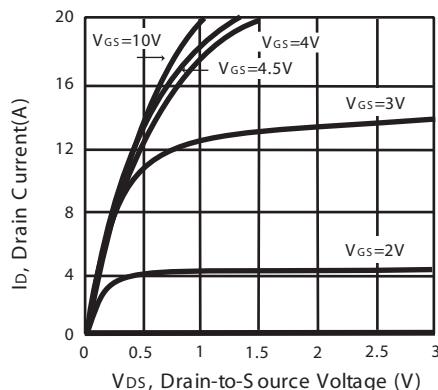


Figure 1. Output Characteristics

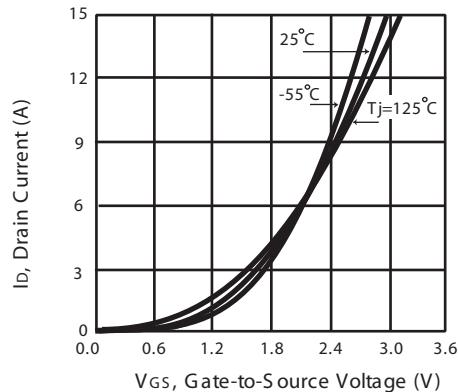


Figure 2. Transfer Characteristics

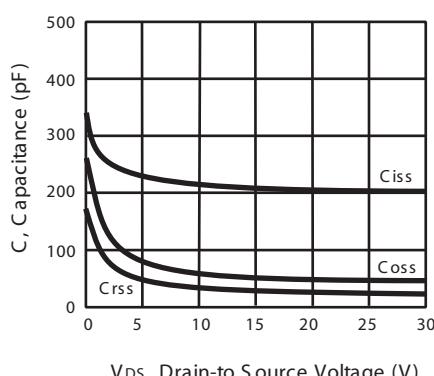


Figure 3. Capacitance

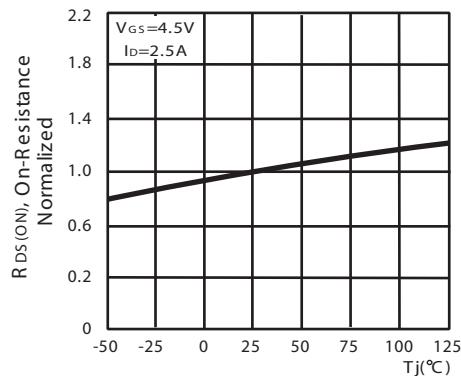
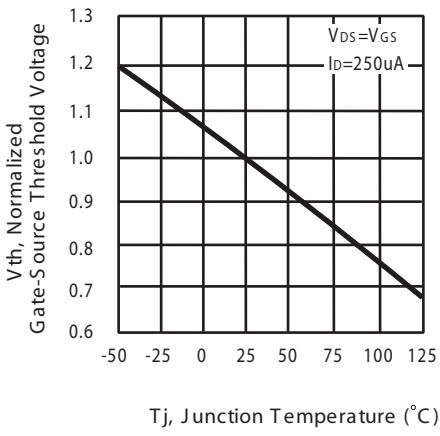
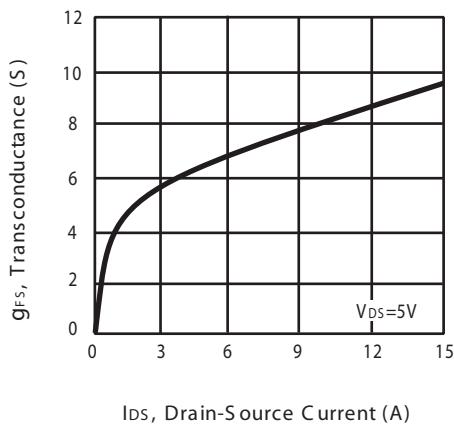


Figure 4. On-Resistance Variation with Temperature

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with Temperature



I_{DS} , Drain-Source Current (A)

Figure 7. Transconductance Variation with Drain Current

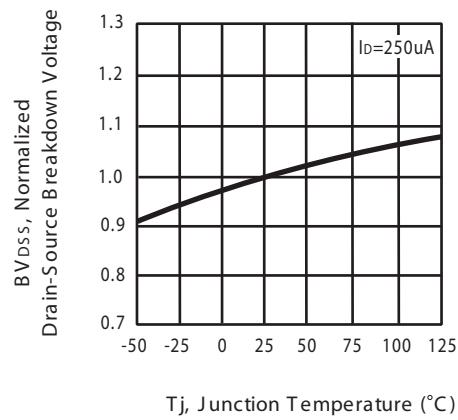
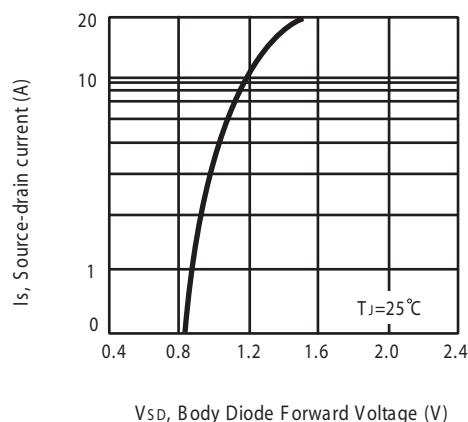
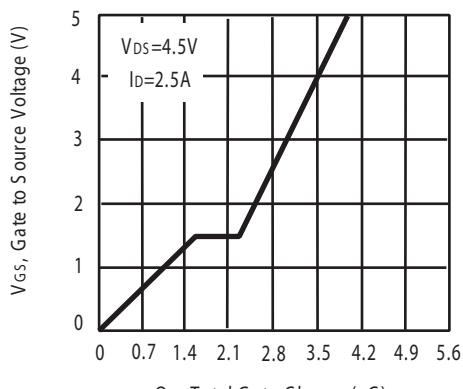


Figure 6. Breakdown Voltage Variation with Temperature



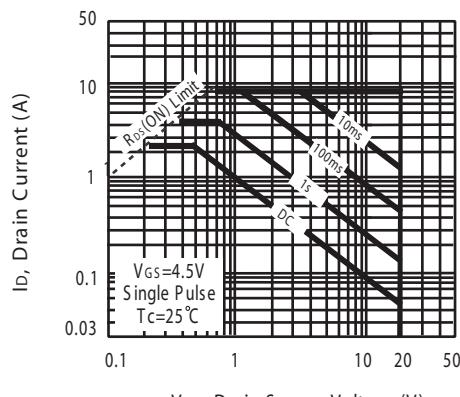
V_{SD} , Body Diode Forward Voltage (V)

Figure 8. Body Diode Forward Voltage Variation with Source Current



Q_g , Total Gate Charge (nC)

Figure 9. Gate Charge



V_{DS} , Drain-Source Voltage (V)

Figure 10. Maximum Safe Operating Area

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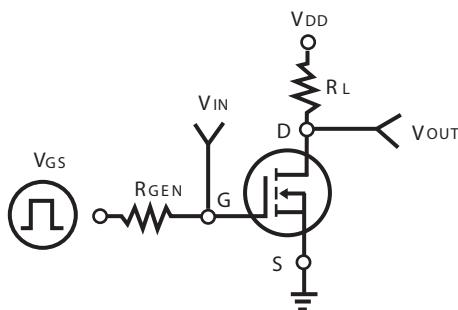


Figure 11. Switching Test Circuit

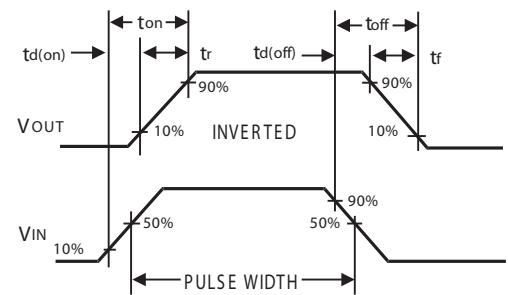
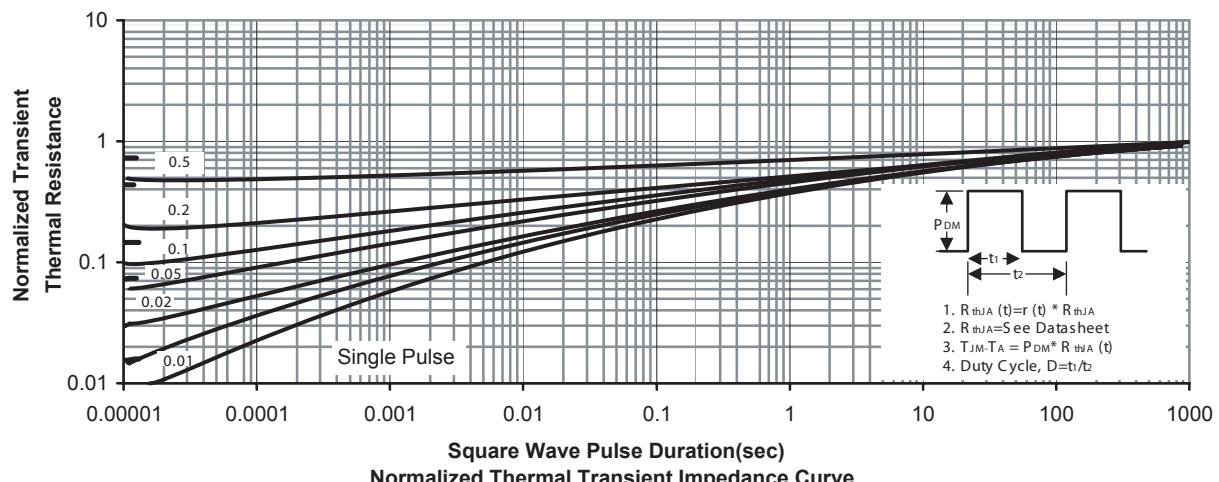
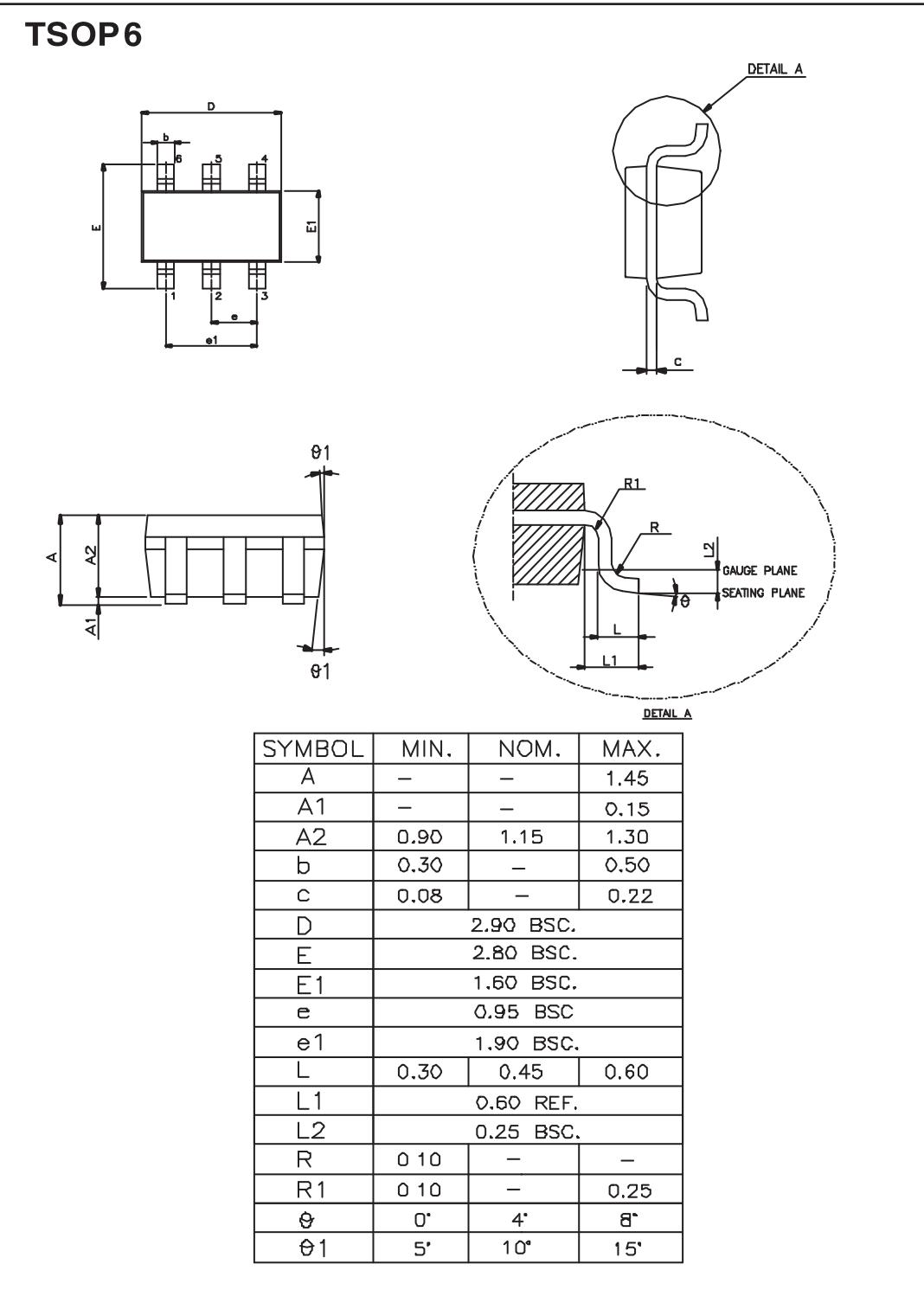


Figure 12. Switching Waveforms



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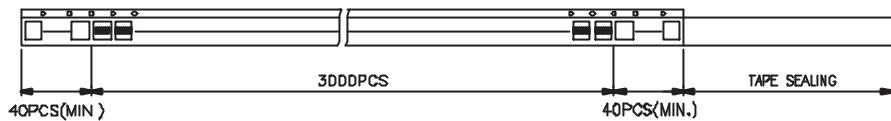
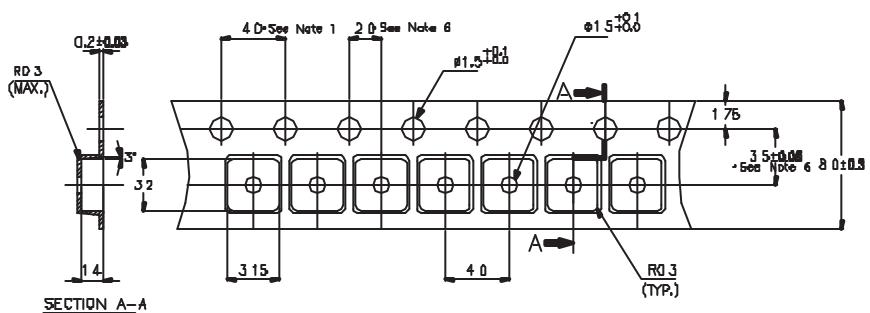
PACKAGE OUTLINE DIMENSIONS



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TSOP6 Tape and Reel Data

TSOP6 Carrier Tape



TSOP6 Reel

