

# **BAS69 Series**

## Low capacitance small signal Schottky diodes

## Main product characteristics

I <sub>F</sub>	10 mA	
V <sub>RRM</sub>	15 V	
C (typ)	<1 pF	
T <sub>j</sub> (max)	150° C	

## Features and benefits

- Low diode capacitance
- Designed for RF applications
- Low profile packages
- Very low parasitic inductor and resistor

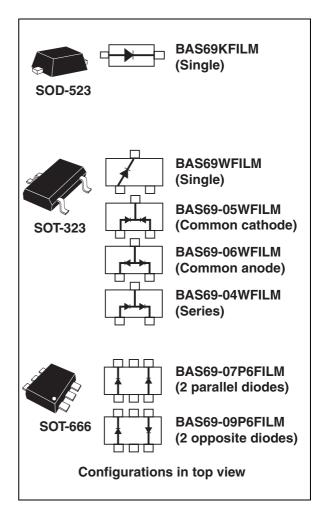
## Description

The BAS69 series use 15V barrier, with extremely low junction capacitance, suitable for the detection of an RF signal and the compensation of the voltage drift with the temperature. The presented packages make the device ideal in applications where space saving is critical.

The low junction capacitance will reduce the disturbance on the RF signal.

## Order codes

Part Number	Marking
BAS69WFILM	23
BAS69-04WFILM	24
BAS69-05WFILM	25
BAS69-06WFILM	26
BAS69KFILM	65
BAS69-09P6FILM	69
BAS69-07P6FILM	67



# 1 Characteristics

#### Table 1. Absolute ratings (limiting values at $T_j = 25^{\circ}$ C, unless otherwise specified)

Symbol	Parameter	Value	Unit		
V <sub>RRM</sub>	Repetitive peak reverse voltage	15	V		
١ <sub>F</sub>	Continuous forward current	10	mA		
I <sub>FSM</sub>	Surge non repetitive forward current	2	А		
T <sub>stg</sub>	Storage temperature range	-65 to +150			
Тj	Maximum operating junction temperature (1)	150	°C		
ΤL	Maximum soldering temperature <sup>(1)</sup> 260				

1. Pulse test: t<sub>p</sub> = 380  $\mu$ s,  $\delta$  < 2 %

#### Table 2. Thermal parameters

Symbol	Parameter	Parameter			
В	Junction to ambient <sup>(1)</sup>	SOT-323	550	° C/W	
R <sub>th(j-a)</sub>		SOD-523, SOT-666	600	C/ VV	

1. Epoxy printed circuit board with recommended pad layout

#### Table 3. Static electrical characteristics

Symbol	Parameter	Test conditions		Min.	Тур	Max.	Unit
		$T_j = 25^\circ C$	V <sub>B</sub> = 1 V			0.035	μA
ا <sub>R</sub> <sup>(1)</sup>	Povoraa laakaga aurrant	T <sub>j</sub> = 125° C	v <sub>R</sub> = 1 v		6	30	
'R` ′	<sup>1)</sup> Reverse leakage current	T <sub>j</sub> = 25° C	V <sub>R</sub> = 15 V			0.23	
		T <sub>j</sub> = 125° C			10	100	
		$T_j = 25^\circ C$	I <sub>F</sub> = 1 mA		350	380	
V <sub>F</sub> <sup>(1)</sup>	Forward voltage drop	T <sub>j</sub> = 125° C			230	260	mV
VF'	Forward voltage drop	$T_j = 25^\circ C$	- I <sub>F</sub> = 10 mA		500	570	mv
		$T_j = 125^\circ C$			460	510	

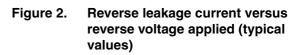
1. Pulse test:  $t_p \le 250 \text{ ms}, \delta \le 2 \%$ 

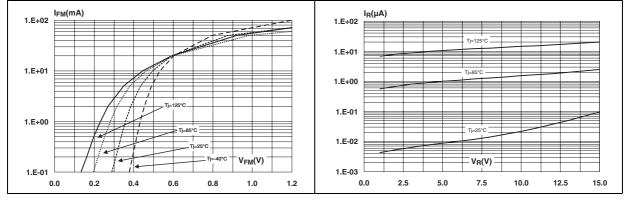
#### Table 4. Dynamic characteristics

Symbol	Parameter	Test conditions		Тур	Max.	Unit
С	Diode capacitance	V <sub>R</sub> = 0 V, F = 1 MHz			1.0	pF
R <sub>F</sub>	Forward resistance I <sub>F</sub> = 5 mA, F = 100 MHz			15		Ω
L <sub>S</sub>	-S Series inductance			1.5		nH



# Figure 1. Forward voltage drop versus forward current (typical values)





# Figure 3. Differential forward resistance versus forward current (typical values)

Figure 4. Junction capacitance versus reverse voltage applied (typical values)

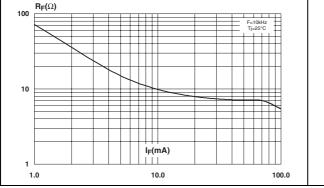


Figure 5. Relative variation of thermal impedance junction to ambient versus pulse duration (SOT-323)

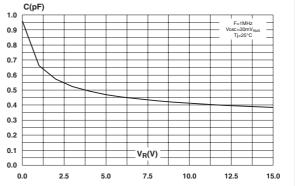
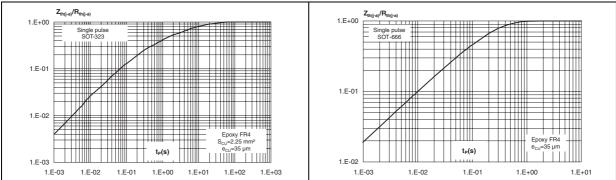
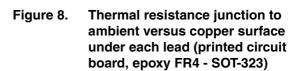


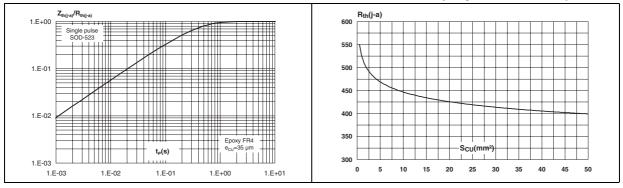
Figure 6. Relative variation of thermal impedance junction to ambient versus pulse duration (SOT-666)



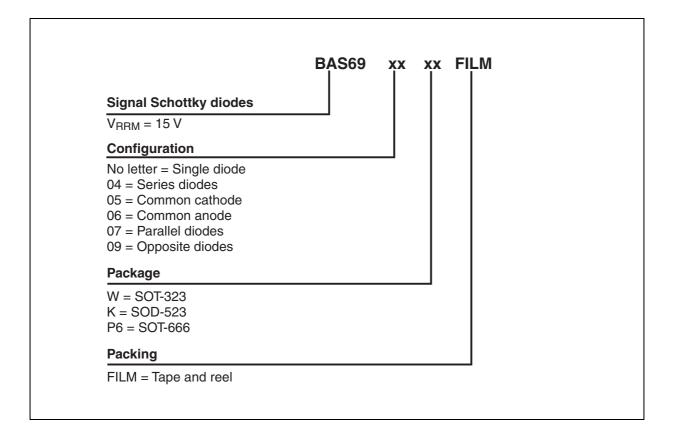


#### Figure 7. Relative variation of thermal impedance junction to ambient versus pulse duration (SOD-523)





2 Ordering information scheme

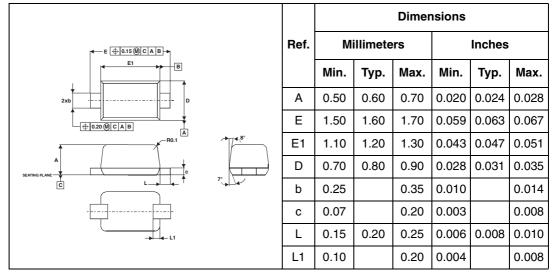




## **3** Package information

Epoxy meets UL94, V0

#### Table 5. SOD-523 dimensions



#### Figure 9. SOD-523 footprint (dimensions in mm)

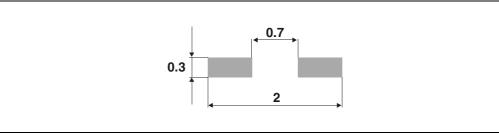
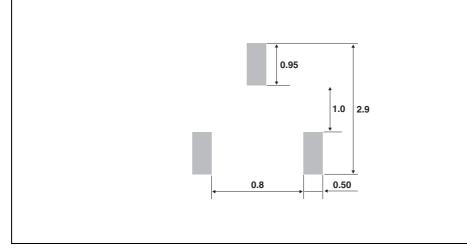


Table 0.	501-525 u	Internationa							
						Dimer	nsions		
			Ref.	М	illimete	rs		Inches	
		A		Min.	Тур.	Max.	Min.	Тур.	Max.
			А	0.8		1.1	0.031		0.043
*	e		A1	0.0		0.1	0.0		0.004
b <u>↓</u>		D	b	0.25		0.4	0.010		0.016
		A1	С	0.1		0.26	0.004		0.010
	!		D	1.8	2.0	2.2	0.071	0.079	0.086
Æ			Е	1.15	1.25	1.35	0.045	0.049	0.053
c Z			е		0.65			0.026	
	H ↔		Н	1.8	2.1	2.4	0.071	0.083	0.094
			L	0.1	0.2	0.3	0.004	0.008	0.012
			q	0		30°	0		30°

Table 6.SOT-323 dimensions







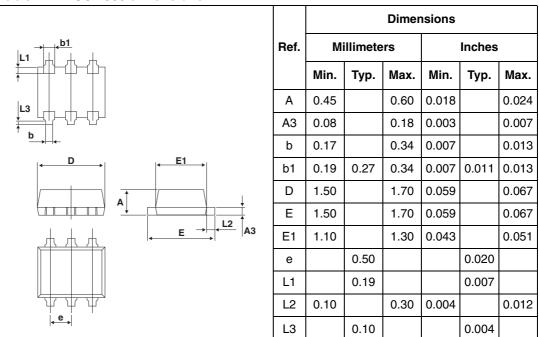
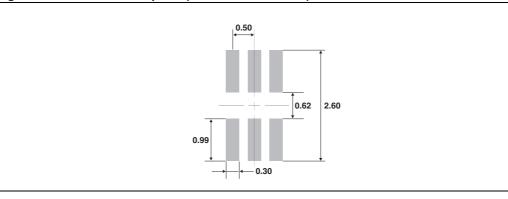


Table 7. SOT-666 dimensions

Figure 11. SOT-666 footprint (dimensions in mm)



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

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# 4 Ordering information

Part Number	Marking	Package	Weight	Base qty	Delivery mode	
BAS69WFILM	23	SOT-323 Single	6 mg	3000	Tape and reel	
BAS69-04WFILM	24	SOT-323 Series	6 mg	3000	Tape and reel	
BAS69-05WFILM	25	SOT-323 Common cathode	6 mg	3000	Tape and reel	
BAS69-06WFILM	26	SOT-323 Common anode	6 mg	3000	Tape and reel	
BAS69KFILM	65	SOD-523 Single	1.4 mg	3000	Tape and reel	
BAS69-09P6FILM	69	SOT-666 Opposite 2.9 mg 3000		Tape and reel		
BAS69-07P6FILM	67	SOT-666 Parallel 2.9 mg 3000		Tape and reel		

# 5 Revision history

Date	Revision	Description of Changes
24-Jul-2006	1	First issue



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