

UNIDIRECTIONAL THYRISTOR SURGE SUPPRESSOR

APPLICATIONS

- ✓ SLIC Line Card
- ✓ DBX Branch Exchange Switches
- ✓ FCC Part 68 Customer Premise Equipment
- ✓ Line Interface Modem
- ✓ ISDN Architecture Interface

IEC COMPATIBILITY (EN61000-4)

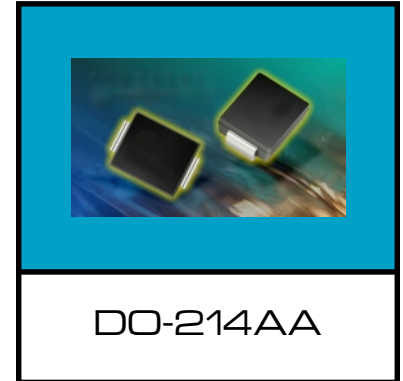
- ✓ 61000-4-2 (ESD): Air - 15kV, Contact - 8kV
- ✓ 61000-4-4 (EFT): 40A - 5/50ns
- ✓ 61000-4-5 (Surge): 8/20 μ s - 95A, L4(Line-Gnd), 48A, L4(Line-Line) & 83A, L2(Power)

FEATURES

- ✓ *Complies with: FCC Part 68, UL 1459, Bellcore 1089, ITU-K.20 & K.21*
- ✓ Peak Off-State Voltage: 80 Volts
- ✓ Surge Current Capability(See Table 1)
- ✓ ESD Protection > 40 kilovolts

MECHANICAL CHARACTERISTICS

- ✓ Molded Plastic DO-214AA Package
- ✓ Weight 2.5 grams (Approximate)
- ✓ Flammability Rating UL 94V-0
- ✓ Device Marking: Logo, Marking Code & Polarity Band or Notch on Top Surface



DEVICE SYMBOL
(UNIDIRECTIONAL)



TABLE 1 - SURGE RATINGS							
SERIES	I_{PP} 2 X 10 μ s AMPS	I_{PP} 10 X 160 μ s AMPS	I_{PP} 10 X 560 μ s AMPS	I_{PP} 10 X 1000 μ s AMPS	I_{TSM} 60 Hz AMPS	di/dt AMPS/ μ s (See Note 1)	dv/dt AMPS/ μ s (See Note 1)
SB	300	150	100	80	32	500	2000
SC	500	200	200	100	60	500	2000

Note 1: Critical Rate of Rise for On-State Current (di/dt) and Off-State Voltage (dv/dt).

PP1101SB & PP01101SC

DEVICE CHARACTERISTICS

MAXIMUM RATINGS @ 25°C Unless Otherwise Specified			
PARAMETER	SYMBOL	VALUE	UNITS
Surge Current - 50/60 Hz (SB/SC)	I_{TSM}	60/32	Amps
Junction Temperature	T_J	-40 to 150	°C
Storage Temperature	T_{STG}	-55 to 150	°C
Thermal Resistance(Junction) - SB Series	$R_{\theta jc}$	28	°C/Watt
Thermal Resistance(Junction) - SC Series	$R_{\theta jc}$	26	°C/Watt
Thermal Resistance(Ambient) - SB Series	$R_{\theta ja}$	90	°C/Watt
Thermal Resistance(Ambient) - SC Series	$R_{\theta ja}$	85	°C/Watt

ELECTRICAL CHARACTERISTICS PER LINE @ 25°C Unless Otherwise Specified									
PART NUMBER	DEVICE MARKING CODE	REPETITIVE PEAK OFF-STATE VOLTAGE	SWITCHING VOLTAGE	MINIMUM HOLDING CURRENT (See Fig. 4)	SWITCHING CURRENT	MAXIMUM OFF-STATE CURRENT (See Fig. 3)	MAXIMUM ON-STATE VOLTAGE (See Fig. 3)	ON-STATE CURRENT	TYPICAL CAPACITANCE
		V_{DRM} VOLTS	@100V/ μ s V_S VOLTS	$di/dt = 1A/ms$ I_H mA	I_S mA	@ V_{DRM} I_{DRM} μ A	@ I_T V_T VOLTS	I_T AMPS	@50V, 1 MHz C pF
PP1101SB	JS	95	130	150	800	5	5	1.0	80
PP1101SC	JT	95	130	150	800	5	5	1.0	120

