

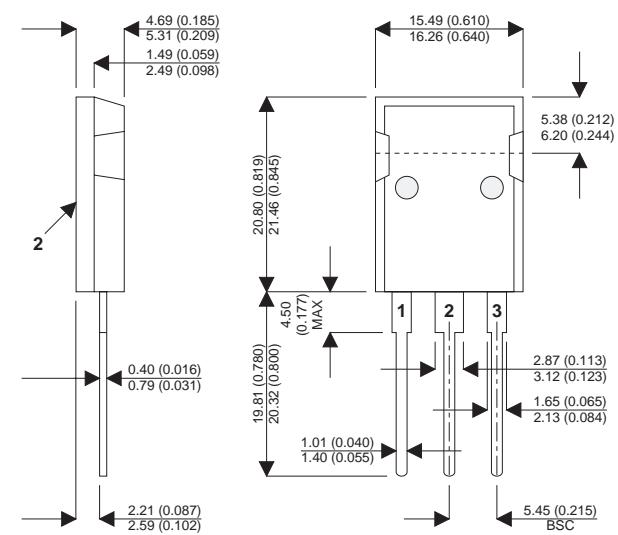


**SEMELAB**

**SML10T75XX**

### T247clip Package Outline.

Dimensions in mm (inches)



Pin 1 – Gate

Pin 2 – Drain

Pin 3 – Source

### N-CHANNEL ENHANCEMENT MODE HIGH VOLTAGE POWER MOSFETS

**V<sub>DSS</sub>** 100V

**I<sub>D(cont)</sub>** 100A

**R<sub>DS(on)</sub>** 0.011Ω

- Faster Switching
- Lower Leakage
- 100% Avalanche Tested
- New T247clip Package  
(Clip-mounted TO-247 Package)

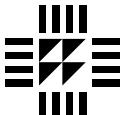
StarMOS is a new generation of high voltage N-Channel enhancement mode power MOSFETs. This new technology minimises the JFET effect, increases packing density and reduces the on-resistance. StarMOS also achieves faster switching speeds through optimised gate layout.

### ABSOLUTE MAXIMUM RATINGS (T<sub>case</sub> = 25°C unless otherwise stated)

V <sub>DSS</sub>	Drain – Source Voltage	100	V
I <sub>D</sub>	Continuous Drain Current	100	A
I <sub>DM</sub>	Pulsed Drain Current <sup>1</sup>	400	A
V <sub>GS</sub>	Gate – Source Voltage	±30	V
V <sub>GSM</sub>	Gate – Source Voltage Transient	±40	
P <sub>D</sub>	Total Power Dissipation @ T <sub>case</sub> = 25°C	520	W
	Derate Linearly	4.16	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to 150	°C
		300	
I <sub>AR</sub>	Avalanche Current <sup>1</sup> (Repetitive and Non-Repetitive)	100	A
E <sub>AR</sub>	Repetitive Avalanche Energy <sup>1</sup>	50	mJ
E <sub>AS</sub>	Single Pulse Avalanche Energy <sup>2</sup>	2500	

1) Repetitive Rating: Pulse Width limited by maximum junction temperature.

2) Starting T<sub>J</sub> = 25°C, L = 500μH, R<sub>G</sub> = 25Ω, Peak I<sub>L</sub> = 100A



**SEME  
LAB**

**SML10T75XX**

**STATIC ELECTRICAL RATINGS** ( $T_{case} = 25^\circ C$  unless otherwise stated)

	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain – Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	100			V
$I_{DSS}$	Zero Gate Voltage Drain Current ( $V_{GS} = 0V$ )	$V_{DS} = V_{DSS}$			250	$\mu A$
		$V_{DS} = 0.8V_{DSS}, T_C = 125^\circ C$			1000	
$I_{GSS}$	Gate – Source Leakage Current	$V_{GS} = \pm 30V, V_{DS} = 0V$			$\pm 100$	nA
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 2.5mA$	2		4	V
$I_{D(ON)}$	On State Drain Current <sup>2</sup>	$V_{DS} > I_{D(ON)} \times R_{DS(ON)}$ Max $V_{GS} = 10V$	100			A
$R_{DS(ON)}$	Drain – Source On State Resistance <sup>2</sup>	$V_{GS} = 10V, I_D = 0.5 I_D$ [Cont.]			0.011	$\Omega$

**DYNAMIC CHARACTERISTICS**

	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
$C_{iss}$	Input Capacitance	$V_{GS} = 0V$		8600		$pF$
$C_{oss}$	Output Capacitance			3250		
$C_{rss}$	Reverse Transfer Capacitance			1180		
$Q_g$	Total Gate Charge <sup>3</sup>	$V_{GS} = 10V$		310		$nC$
$Q_{gs}$	Gate – Source Charge			95		
$Q_{gd}$	Gate – Drain (“Miller”) Charge			120		
$t_{d(on)}$	Turn-on Delay Time	$V_{GS} = 15V$		16		$ns$
$t_r$	Rise Time			33		
$t_{d(off)}$	Turn-off Delay Time			46		
$t_f$	Fall Time		$R_G = 0.6\Omega$	8		

**SOURCE – DRAIN DIODE RATINGS AND CHARACTERISTICS**

	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current	(Body Diode)			100	A
$I_{SM}$	Pulsed Source Current <sup>1</sup>				400	
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	$V_{GS} = 0V, I_S = -I_D$ [Cont.]			1.3	V
$t_{rr}$	Reverse Recovery Time	$I_S = -I_D$ [Cont.], $dI_S / dt = 100A/\mu s$		250		ns
$Q_{rr}$	Reverse Recovery Charge	$I_S = -I_D$ [Cont.], $dI_S / dt = 100A/\mu s$			2.5	$\mu C$

**THERMAL CHARACTERISTICS**

	Characteristic	Min.	Typ.	Max.	Unit
$R_{\theta JC}$	Junction to Case	0.24			$^\circ C/W$
$R_{\theta JA}$	Junction to Ambient			40	

1) Repetitive Rating: Pulse Width limited by maximum junction temperature.

2) Pulse Test: Pulse Width < 380 $\mu s$ , Duty Cycle < 2%

3) See MIL-STD-750 Method 3471



CAUTION — Electrostatic Sensitive Devices. Anti-Static Procedures Must Be Followed.