

Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Collector-emitter voltage	V _{CEO}	50	V
Output withstand voltage	V _{CE (SUS)}	50	V
Output current	I _{OUT}	500	mA
Input voltage	V _{IN}	-0.5~30	V
Clamp diode reverse voltage	V _R	50	V
Clamp diode forward current	I _F	500	mA
Power dissipation	P _D (Note 4)	0.78	W
Saturated thermal resistance	R _{th (j-a)} (Note 4)	160	°C/W
	R _{th (j-c)} (Note 5)	25	
Operating temperature	T _{opr}	-40~85	°C
Storage temperature	T _{stg}	-55~150	°C

Note 4: 114.3 × 76.2 × 1.6 mm glass epoxy film substrate Cu heat dissipation pattern 100 mm²

Note 5: When an infinite heat sink is mounted.

Recommended Operating Condition (Ta = -40~85°C)

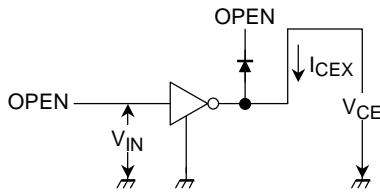
Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Output withstand voltage		V _{CEO}	—	0	—	50	V
Output current		I _{OUT}	Ta = 60°C, T _J = 105°C	—	—	220	mA
Input voltage		V _{IN}	—	0	—	24	V
Input voltage	Output ON	V _{IN (ON)}	I _{OUT} = 400 mA, h _{FE} = 800	2.8	—	24	V
	Output OFF	V _{IN (OFF)}	—	0	—	0.7	
Clamp diode reverse voltage		V _R	—	—	—	50	V
Clamp diode forward current		I _F	—	—	—	350	mA

Electrical Characteristics (Ta = 25°C)

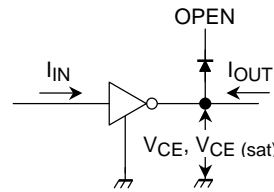
Characteristics		Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Output leakage current		I _{CEX}	1	V _{CE} = 50 V, V _{IN} = OPEN	—	—	10	μA
Output saturation voltage		V _{CE (sat)}	2	I _{OUT} = 300 mA, I _{IN} = 500 μA	—	1.1	1.3	V
				I _{OUT} = 200 mA, I _{IN} = 350 μA	—	1.0	1.2	
DC current amplification ratio		h _{FE}	2	V _{CE} = 2.0 V, I _{OUT} = 350 mA	1000	—	—	
Input current	Output ON	I _{IN (ON)}	3	V _{IN} = 2.4 V, I _{OUT} = 350 mA	—	0.4	0.7	mA
Input voltage	Output ON	V _{IN (ON)}	4	V _{CE} = 2 V, h _{FE} = 800	—	—	2.6	V
	Output ON						I _{OUT} = 200 mA	
Clamp diode leakage current		I _R	5	V _R = 50 V	—	—	10	μA
Clamp diode forward voltage		V _F	6	I _F = 350 mA	—	1.6	2.0	V
Turn-on delay		t _{ON}	7	V _{OUT} = 50 V, R _L = 125 Ω, C _L = 15 pF	—	0.02	—	μs
Turn-off delay		t _{OFF}			—	1.0	—	

Test Circuit

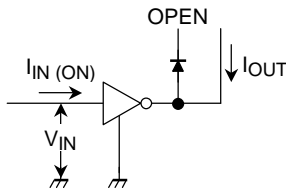
1. I_{CEX}



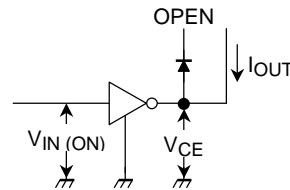
2. $V_{CE(sat)}$, h_{FE}



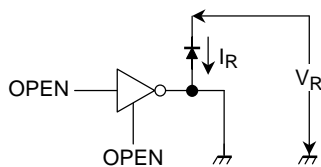
3. $I_{IN(ON)}$



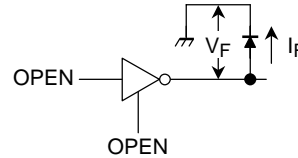
4. $V_{IN(ON)}$



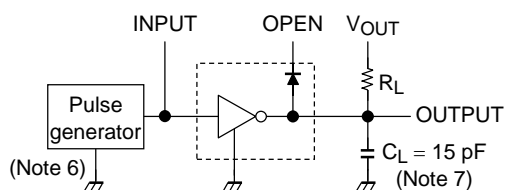
5. I_R



6. V_F

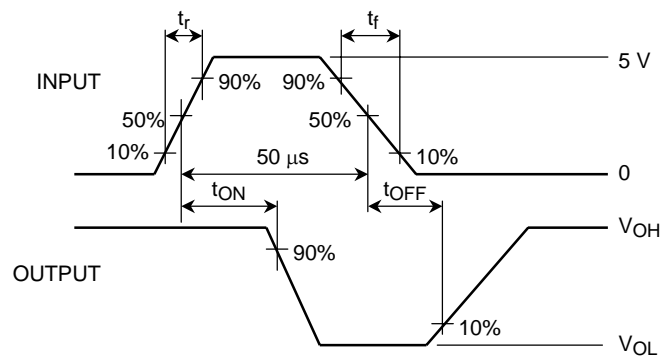


7. t_{ON} , t_{OFF}



Note 6: Pulse width 50 μ s, Duty cycle 10%
Output impedance 50 Ω , $t_r \leq 5$ ns, $t_f \leq 10$ ns

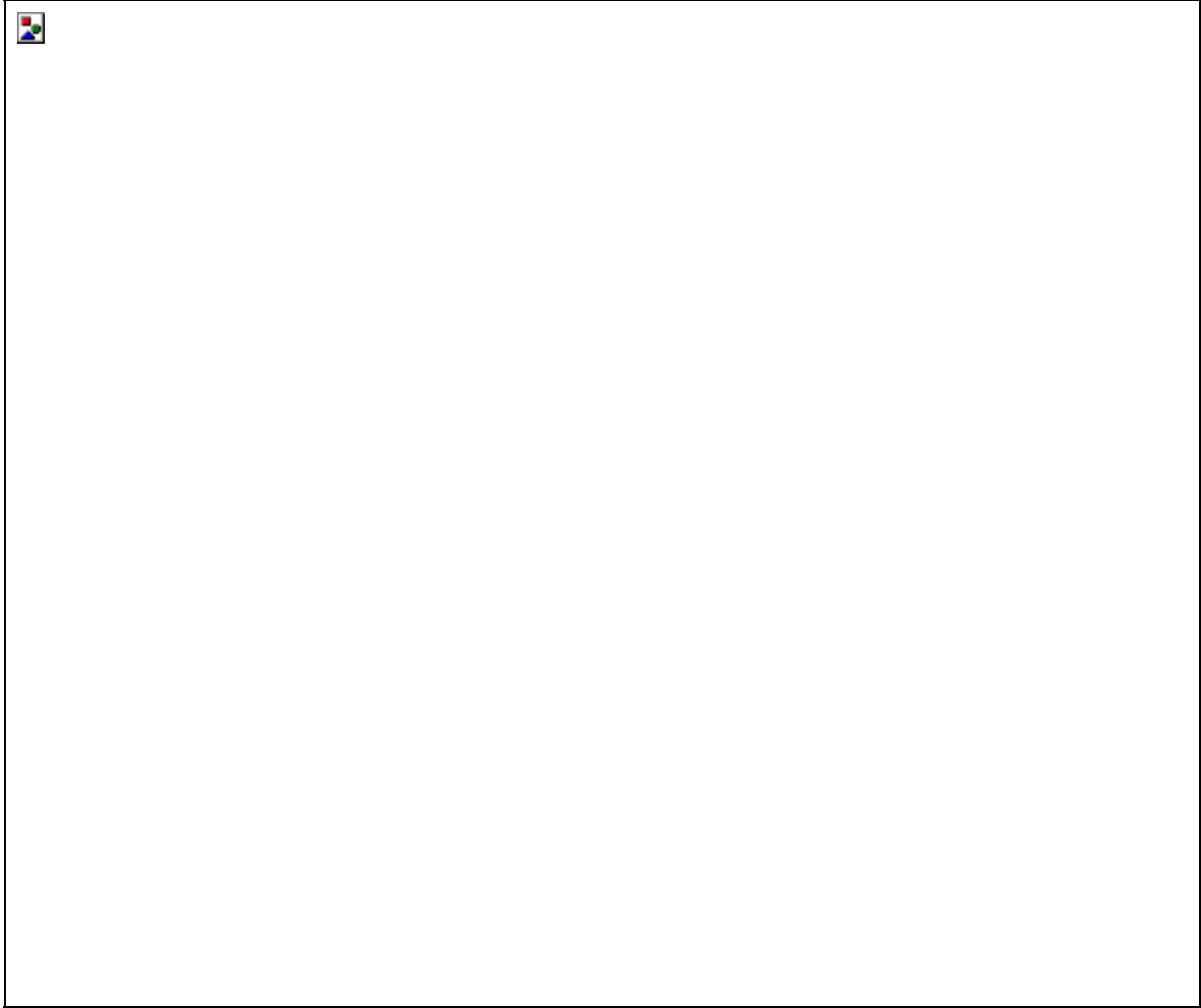
Note 7: C_L includes probe and jig capacitance.



Caution on Application

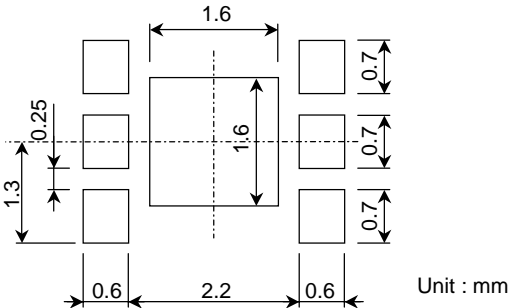
- The device does not include protectors such as an overcurrent protector and an overvoltage protector. Applying excessive current or voltage may damage the device. Thus, design with great care to prevent excessive current or voltage from being applied to the device. The device may also be damaged by short-circuits between outputs and power supply/ground. Take care when designing output, VCC and GND line.
- Be sure to mount the device in the correct orientation. Make sure that the positive and negative power supply pins are connected the right way round. Otherwise, the absolute maximum current and power dissipation ratings may be exceeded and the device may break down or undergo performance degradation, causing it to catch fire or explode, and resulting in injury.

Package Dimensions

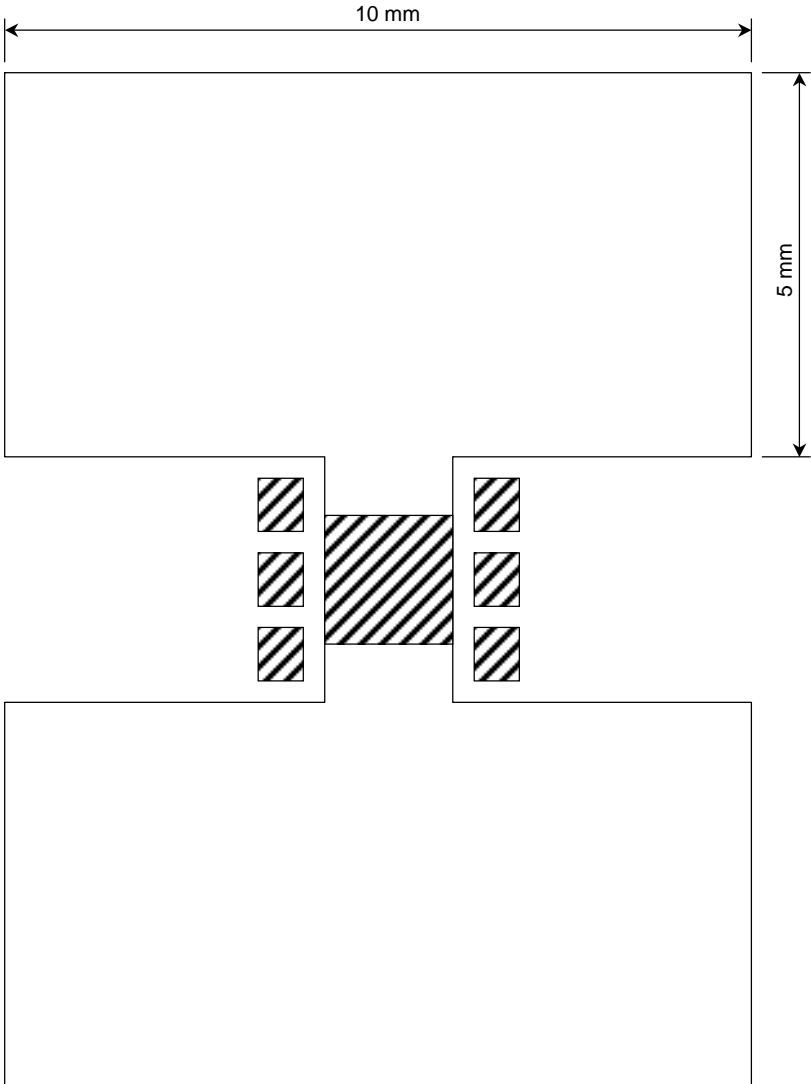


Weight: 0.017 g (typ.)

Preliminary land pattern



Preliminary PCB trace dimension



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