

# **Electrical Specifications**

Nominal Frequency (F<sub>o</sub>): 59.535MHz

#### Frequency Stability

Over Temperature, <±0.5ppm (w/offset applied) 1st Year Aging, ±1ppm, maximum 30-years Aging, ±5ppm, maximum

### **Frequency Adjust**

Method, External Voltage, 0V to +5<sub>VDC</sub>
Range, sufficient for 30-years aging correction plus an additional ±1.5ppm
Modulation Bandwidth, 500Hz, minimum
Slope, positive

#### **Power Supply**

Voltage (V<sub>CC</sub>), +5.0V<sub>DC</sub> ±5% Current Consumption, 25*mA*, maximum

## **Output (ACMOS)**

Load, 15pF, nominal Level, High,  $^390\%$  of  $V_{CC}$ ; Low,  $\leq 10\%$  of  $V_{CC}$ Duty Cycle, 40% to 60% Rise (10% to 90%)/Fall (90% to 10%) times,  $\leq 3nsec$ 

#### SSB Phase Noise (maximum)

- -75dBc/Hz @ 10Hz offset
- -105dBc/Hz @ 100Hz offset
- -130dBc/Hz @ 1kHz offset
- -145dBc/Hz @ 10kHz offset

#### **Temperature Range**

Operating, 0°C to +70°C Storage, -55°C to +85°C

