



# STB25NM60N/-1 - STF25NM60N STP25NM60N - STW25NM60N

N-channel 600V - 0.140Ω - 20A - TO-220 /FP- I<sup>2</sup>/D<sup>2</sup>PAK - TO-247  
Second generation MDmesh™ Power MOSFET

## General features

Type	V <sub>DSS</sub> (@T <sub>jmax</sub> )	R <sub>DS(on)</sub>	I <sub>D</sub>
STB25NM60N	650V	<0.170Ω	20A
STB25NM60N-1	650V	<0.170Ω	20A
STF25NM60N	650V	<0.170Ω	20A <sup>(1)</sup>
STP25NM60N	650V	<0.170Ω	20A
STW25NM60N	650V	<0.170Ω	20A

1. Limited only by maximum temperature allowed

- 100% avalanche tested
- Low input capacitance and gate charge
- Low gate input resistance

## Description

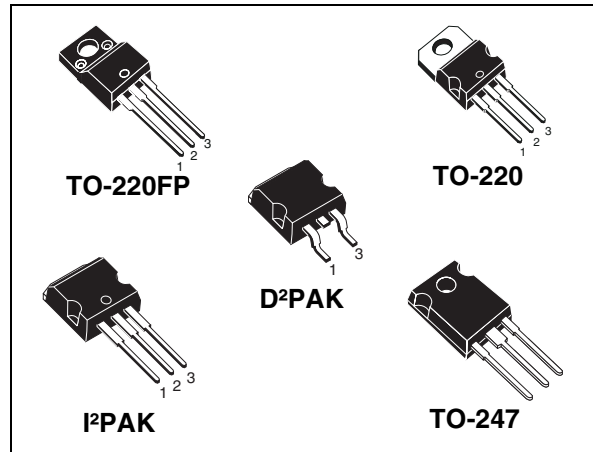
This series of devices is realized with the second generation of MDmesh™ Technology. This revolutionary MOSFET associates a new vertical structure to the Company's strip layout to yield one of the world's lowest on-resistance and gate charge. It is therefore suitable for the most demanding high efficiency converters

## Applications

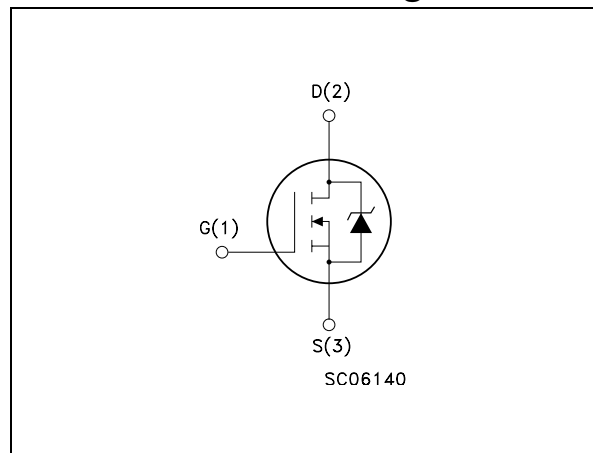
- Switching application

## Order codes

Part number	Marking	Package	Packaging
STB25NM60N	B25NM60N	D <sup>2</sup> PAK	Tape & reel
STB25NM60N-1	B25NM60N-1	I <sup>2</sup> PAK	Tube
STF25NM60N	F25NM60N	TO-220FP	Tube
STP25NM60N	P25NM60N	TO-220	Tube
STW25NM60N	W25NM60N	TO-247	Tube



## Internal schematic diagram



## Contents

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# 1 Electrical ratings

**Table 1. Absolute maximum ratings**

Symbol	Parameter	Value		Unit
		TO-220 - I <sup>2</sup> PAK D <sup>2</sup> PAK - TO-247	TO-220FP	
V <sub>DS</sub>	Drain-source voltage (V <sub>GS</sub> = 0)	600		V
V <sub>DGR</sub>	Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)	600		V
V <sub>GS</sub>	Gate- source voltage	±25		V
I <sub>D</sub>	Drain current (continuous) at T <sub>C</sub> = 25°C	20	20 <sup>(1)</sup>	A
I <sub>D</sub>	Drain current (continuous) at T <sub>C</sub> = 100°C	12.8	12.8 <sup>(1)</sup>	A
I <sub>DM</sub> <sup>(2)</sup>	Drain current (pulsed)	80	80 <sup>(1)</sup>	A
P <sub>TOT</sub>	Total dissipation at T <sub>C</sub> = 25°C	160	40	W
	Derating factor	1.28	0.32	W/°C
V <sub>ISO</sub>	Insulation withstand voltage (RMS) from all three leads to external heat sink (t=1s; T <sub>C</sub> =25°C)	--	2500	V
dv/dt <sup>(3)</sup>	Peak diode recovery voltage slope	15		V/ns
T <sub>stg</sub>	Storage temperature	-55 to 150		°C
T <sub>j</sub>	Max. operating junction temperature	150		°C

1. Limited only by maximum temperature allowed
2. Pulse width limited by safe operating area
3. I<sub>SD</sub> ≤ 22A, di/dt ≤ 400 A/μs, V<sub>DD</sub> = 80% V<sub>(BR)DSS</sub>

**Table 2. Thermal data**

Symbol	Parameter	Value		Unit
		TO-220 - I <sup>2</sup> PAK D <sup>2</sup> PAK - TO-247	TO-220FP	
R <sub>thj-case</sub>	Thermal resistance junction-case Max	0.78	3.1	°C/W
R <sub>thj-amb</sub>	Thermal resistance junction-ambient Max	62.5		°C/W
T <sub>l</sub>	Maximum lead temperature for soldering purpose	300		°C

**Table 3. Avalanche characteristics**

Symbol	Parameter	Value	Unit
I <sub>AR</sub>	Avalanche current, repetitive or not-repetitive (pulse width limited by T <sub>j</sub> Max)	10	A
E <sub>AS</sub>	Single pulse avalanche energy (starting T <sub>j</sub> =25°C, I <sub>d</sub> =I <sub>ar</sub> , V <sub>dd</sub> =50V)	850	mJ

## 2 Electrical characteristics

( $T_{CASE}=25^{\circ}C$  unless otherwise specified)

**Table 4. On/off states**

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_{(BR)DSS}$	Drain-source breakdown voltage	$I_D = 1mA, V_{GS} = 0$	600			V
$dv/dt^{(1)}$	Drain source voltage slope	$V_{DD}=480V, I_D=25A, V_{GS}=10V$		48		V/ns
$I_{DSS}$	Zero gate voltage drain current ( $V_{GS} = 0$ )	$V_{DS} = \text{Max rating}$ $V_{DS} = \text{Max rating}, @125^{\circ}C$			1 10	$\mu A$ $\mu A$
$I_{GSS}$	Gate-body leakage current ( $V_{DS} = 0$ )	$V_{GS} = \pm 20V$			100	nA
$V_{GS(th)}$	Gate threshold voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	2	3	4	V
$R_{DS(on)}$	Static drain-source on resistance	$V_{GS} = 10V, I_D = 10A$		0.140	0.170	$\Omega$

1. Characteristic value at turn off on inductive load

**Table 5. Dynamic**

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$g_{fs}^{(1)}$	Forward transconductance	$V_{DS}=15V, I_D = 11A$		17		S
$C_{iss}$ $C_{oss}$ $C_{rss}$	Input capacitance Output capacitance Reverse transfer capacitance	$V_{DS} = 25V, f = 1MHz,$ $V_{GS} = 0$		2540 511 73		pF pF pF
$C_{oss\ eq.}^{(2)}$	Equivalent output capacitance	$V_{GS} = 0V, V_{DS} = 0V \text{ to } 480V$		310		pF
$Q_g$ $Q_{gs}$ $Q_{gd}$	Total gate charge Gate-source charge Gate-drain charge	$V_{DD} = 480V, I_D = 20A,$ $V_{GS} = 10V,$ <i>(see Figure 18)</i>		84 14 44		nC nC nC
$R_g$	Gate input resistance	$f=1MHz$ Gate DC Bias=0 Test signal level=20mV open drain		1.6		$\Omega$

1. Pulsed: Pulse duration = 300  $\mu s$ , duty cycle 1.5 %

2.  $C_{oss\ eq.}$  is defined as a constant equivalent capacitance giving the same charging time as  $C_{oss}$  when  $V_{DS}$  increases from 0 to 80%  $V_{DS}$

**Table 6. Switching times**

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on delay time	$V_{DD} = 300V, I_D = 10A$ $R_G = 4.7\Omega, V_{GS} = 10V$ (see Figure 17)		24.5		ns
$t_r$	Rise time			18		ns
$t_{d(off)}$	Turn-off delay time			94		ns
$t_f$	Fall time			24		ns

**Table 7. Source drain diode**

Symbol	Parameter	Test conditions	Min	Typ.	Max	Unit
$I_{SD}$	Source-drain current				20	A
$I_{SDM}^{(1)}$	Source-drain current (pulsed)				80	A
$V_{SD}^{(2)}$	Forward on voltage	$I_{SD} = 20A, V_{GS} = 0$			1.3	V
$t_{rr}$	Reverse recovery time	$I_{SD} = 20A, di/dt = 100A/\mu s$ $V_{DD} = 100V, T_j = 25^\circ C$ (see Figure 22)		427		ns
$Q_{rr}$	Reverse recovery charge			7.2		$\mu C$
$I_{RRM}$	Reverse recovery current			33.6		A
$t_{rr}$	Reverse recovery time	$I_{SD} = 20A, di/dt = 100A/\mu s$ $V_{DD} = 100V, T_j = 150^\circ C$ (see Figure 22)		526		ns
$Q_{rr}$	Reverse recovery charge			9.1		$\mu C$
$I_{RRM}$	Reverse recovery current			34.5		A

1. Pulse width limited by safe operating area

2. Pulsed: Pulse duration = 300  $\mu s$ , duty cycle 1.5 %

## 2.1 Electrical characteristics (curves)

Figure 1. Safe operating area for TO-220 / D<sup>2</sup>PAK / I<sup>2</sup>PAK

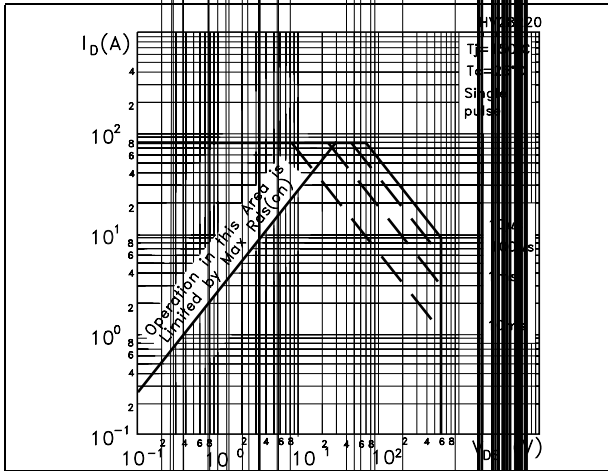


Figure 2. Thermal impedance for TO-220 / D<sup>2</sup>PAK / I<sup>2</sup>PAK

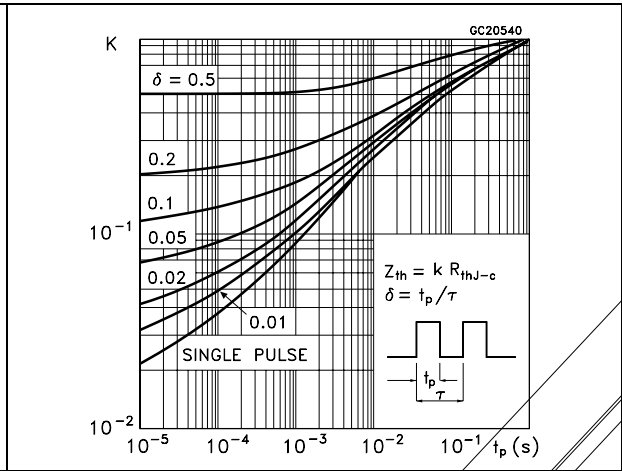


Figure 3. Safe operating area for TO-220FP

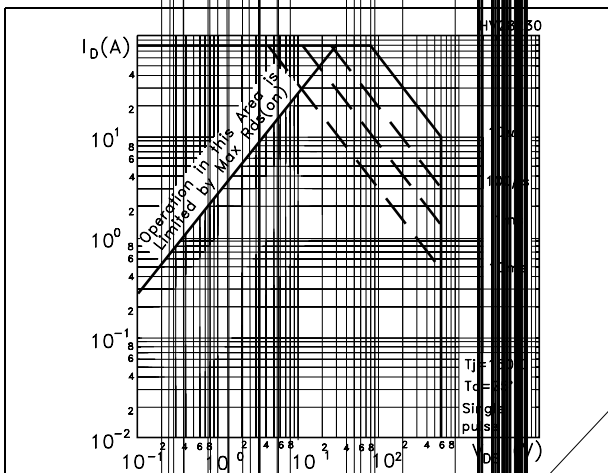


Figure 4. Thermal impedance for TO-220FP

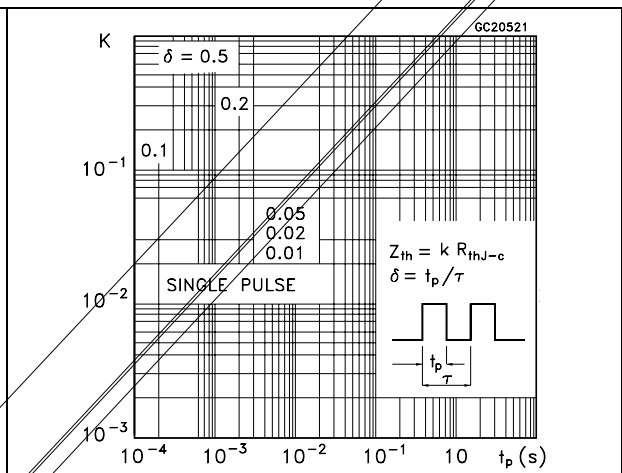


Figure 5. Safe operating area for TO-247

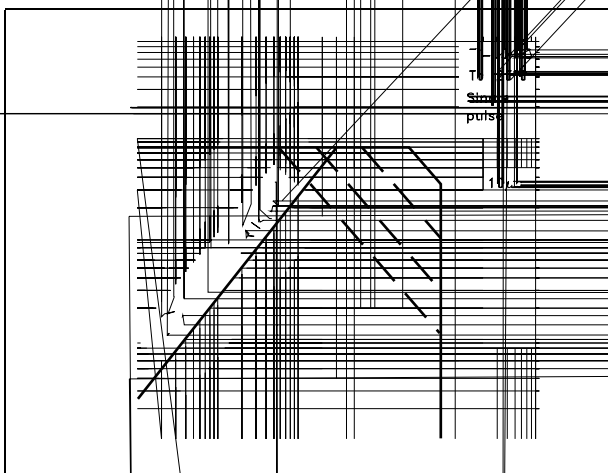


Figure 6. Thermal impedance for TO-247

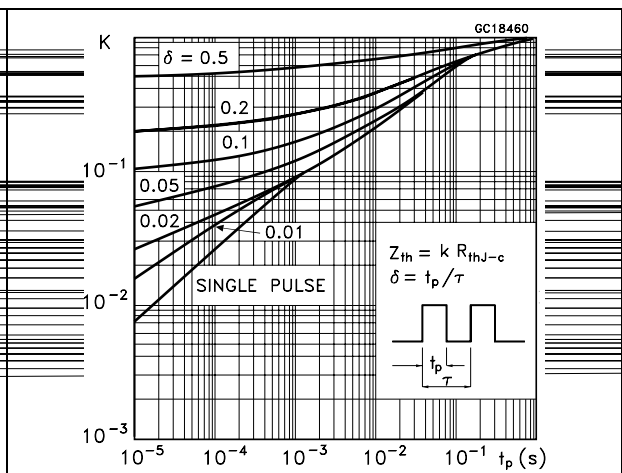


Figure 7. Output characteristics

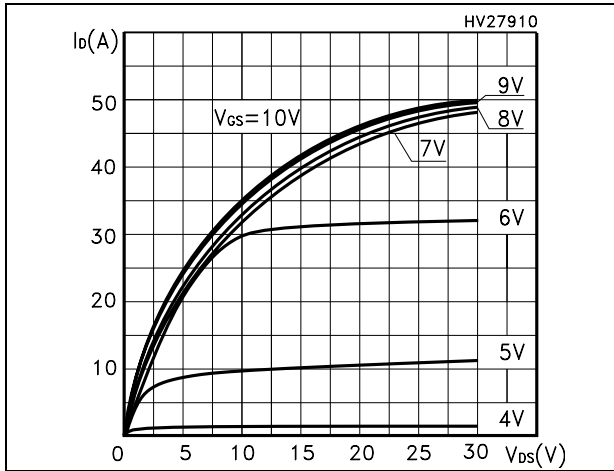


Figure 8. Transfer characteristics

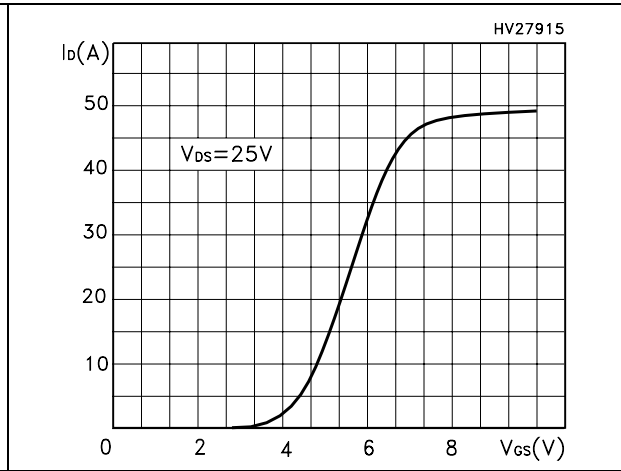


Figure 9. Transconductance

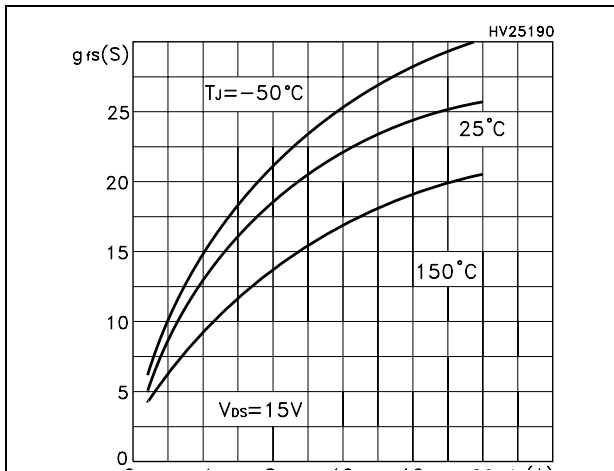


Figure 10. Static drain-source on resistance

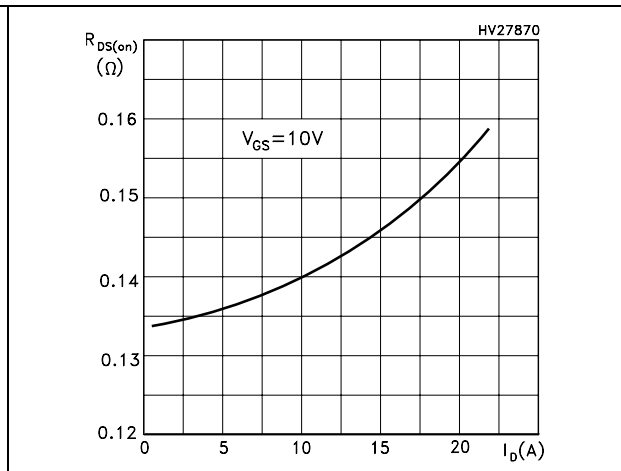


Figure 11. Gate charge vs gate-source voltage Figure 12. Capacitance variations

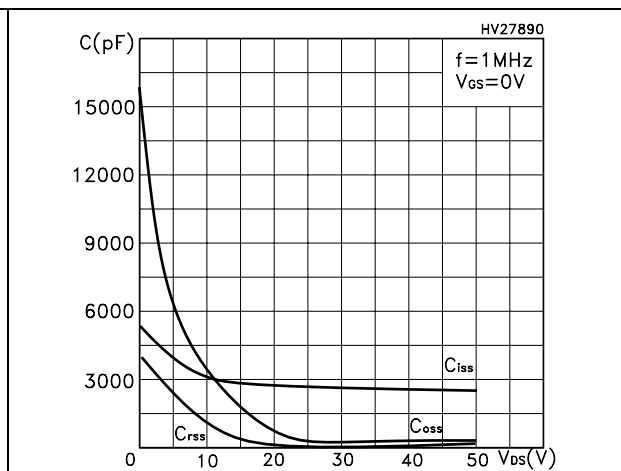
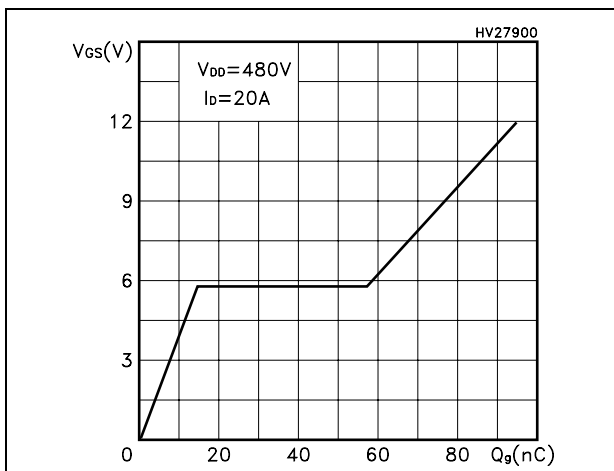


Figure 13. Normalized gate threshold voltage vs temperature

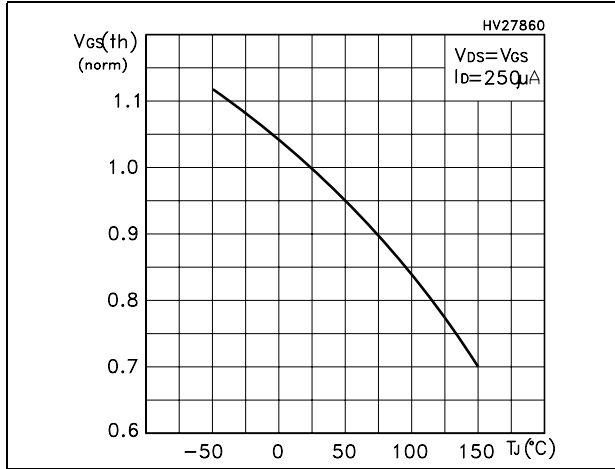


Figure 14. Normalized on resistance vs temperature

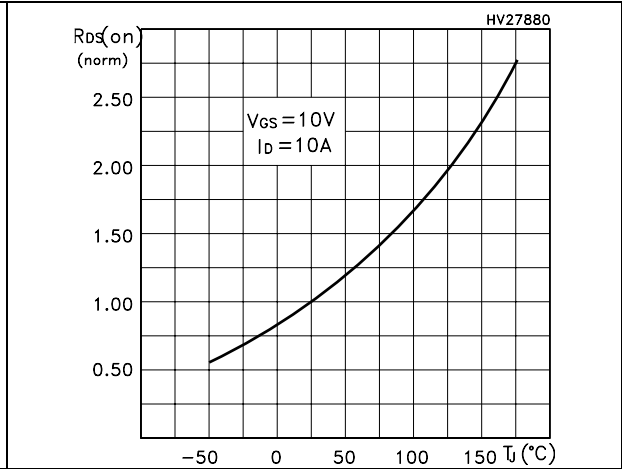


Figure 15. Source-drain diode forward characteristics

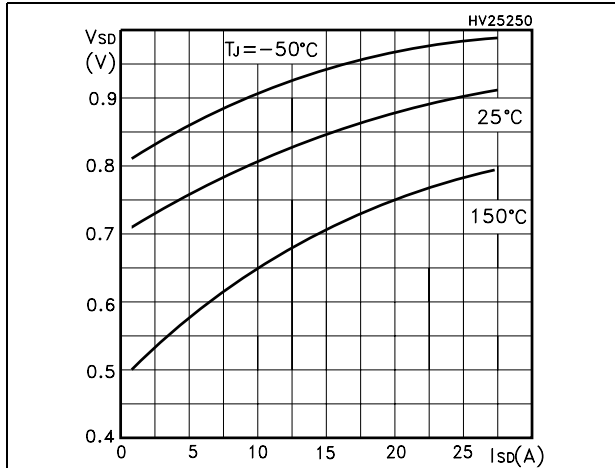
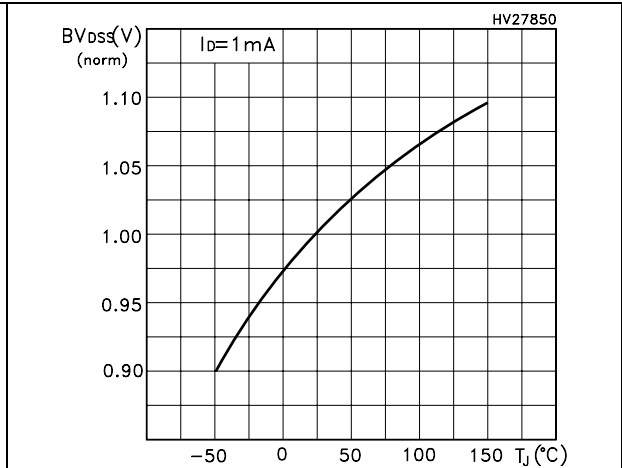


Figure 16. Normalized  $B_{VDSS}$  vs temperature





### 3 Test circuit

Figure 17. Switching times test circuit for resistive load



Figure 18. Gate charge test circuit



Figure 19. Test circuit for inductive load switching and diode recovery times

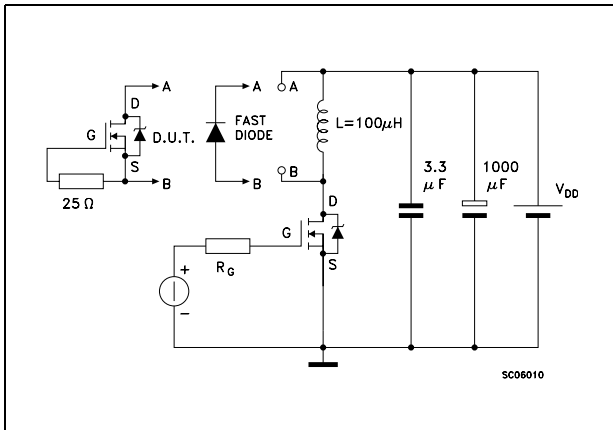


Figure 20. Unclamped Inductive load test circuit



Figure 21. Unclamped inductive waveform



Figure 22. Switching time waveform



## 4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: [www.st.com](http://www.st.com)

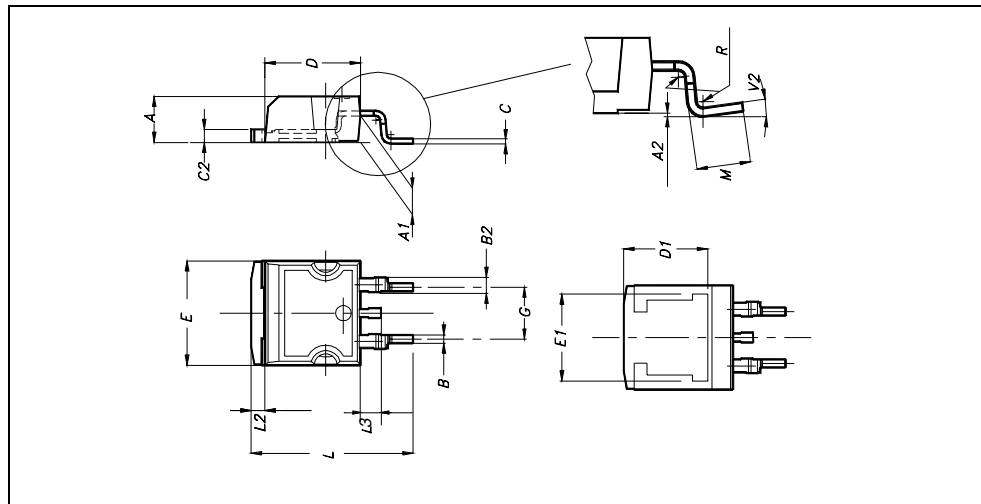
**TO-220 MECHANICAL DATA**

DIM.	mm.			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.40		4.60	0.173		0.181
b	0.61		0.88	0.024		0.034
b1	1.15		1.70	0.045		0.066
c	0.49		0.70	0.019		0.027
D	15.25		15.75	0.60		0.620
E	10		10.40	0.393		0.409
e	2.40		2.70	0.094		0.106
e1	4.95		5.15	0.194		0.202
F	1.23		1.32	0.048		0.052
H1	6.20		6.60	0.244		0.256
J1	2.40		2.72	0.094		0.107
L	13		14	0.511		0.551
L1	3.50		3.93	0.137		0.154
L20		16.40			0.645	
L30		28.90			1.137	
øP	3.75		3.85	0.147		0.151
Q	2.65		2.95	0.104		0.116



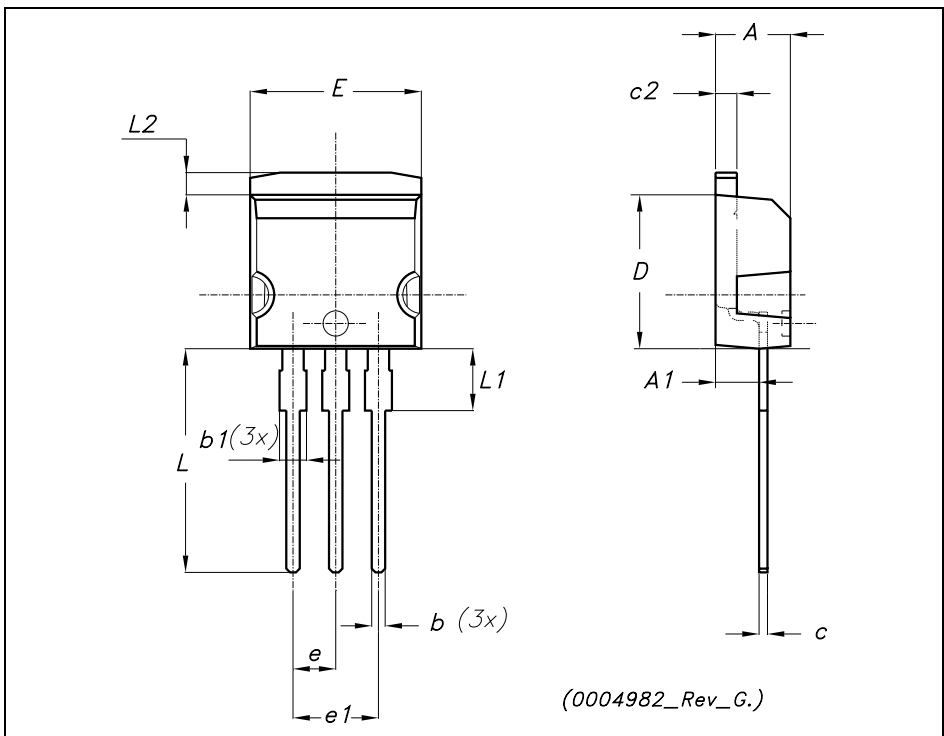
**D<sup>2</sup>PAK MECHANICAL DATA**

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A	4.4		4.6	0.173		0.181
A1	2.49		2.69	0.098		0.106
A2	0.03		0.23	0.001		0.009
B	0.7		0.93	0.027		0.036
B2	1.14		1.7	0.044		0.067
C	0.45		0.6	0.017		0.023
C2	1.23		1.36	0.048		0.053
D	8.95		9.35	0.352		0.368
D1		8			0.315	
E	10		10.4	0.393		
E1		8.5			0.334	
G	4.88		5.28	0.192		0.208
L	15		15.85	0.590		0.625
L2	1.27		1.4	0.050		0.055
L3	1.4		1.75	0.055		0.068
M	2.4		3.2	0.094		0.126
R		0.4			0.015	
V2	0°		4°			



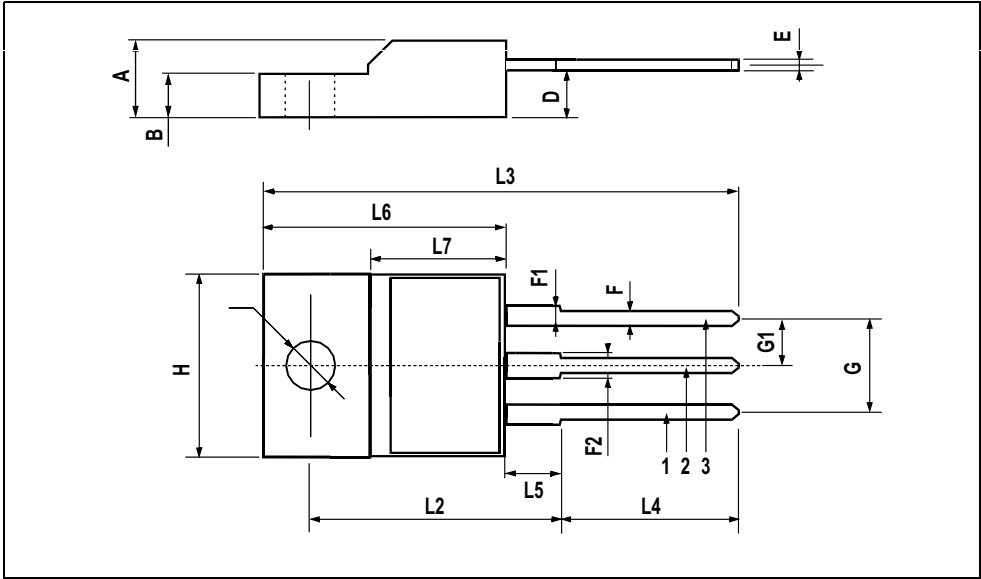
**TO-262 (I<sup>2</sup>PAK) MECHANICAL DATA**

DIM.	mm.			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.40		4.60	0.173		0.181
A1	2.40		2.72	0.094		0.107
b	0.61		0.88	0.024		0.034
b1	1.14		1.70	0.044		0.066
c	0.49		0.70	0.019		0.027
c2	1.23		1.32	0.048		0.052
D	8.95		9.35	0.352		0.368
e	2.40		2.70	0.094		0.106
e1	4.95		5.15	0.194		0.202
E	10		10.40	0.393		0.410
L	13		14	0.511		0.551
L1	3.50		3.93	0.137		0.154
L2	1.27		1.40	0.050		0.055



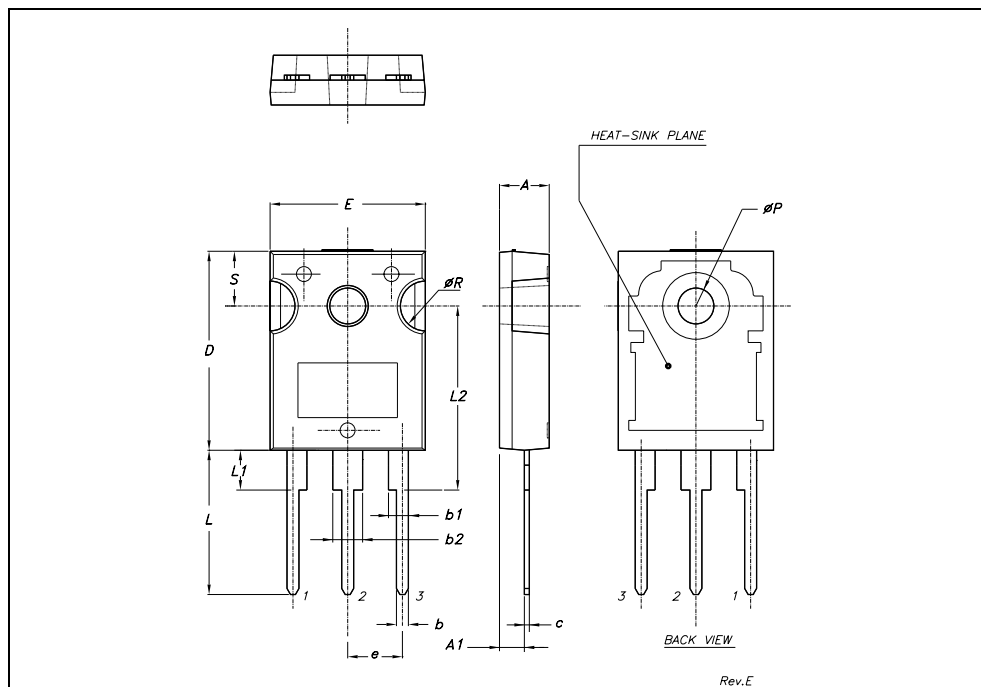
**TO-220FP MECHANICAL DATA**

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A	4.4		4.6	0.173		0.181
B	2.5		2.7	0.098		0.106
D	2.5		2.75	0.098		0.108
E	0.45		0.7	0.017		0.027
F	0.75		1	0.030		0.039
F1	1.15		1.7	0.045		0.067
F2	1.15		1.7	0.045		0.067
G	4.95		5.2	0.195		0.204
G1	2.4		2.7	0.094		0.106
H	10		10.4	0.393		0.409
L2		16			0.630	
L3	28.6		30.6	1.126		1.204
L4	9.8		10.6	.0385		0.417
L5	2.9		3.6	0.114		0.141
L6	15.9		16.4	0.626		0.645
L7	9		9.3	0.354		0.366
Ø	3		3.2	0.118		0.126



**TO-247 MECHANICAL DATA**

DIM.	mm.			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.85		5.15	0.19		0.20
A1	2.20		2.60	0.086		0.102
b	1.0		1.40	0.039		0.055
b1	2.0		2.40	0.079		0.094
b2	3.0		3.40	0.118		0.134
c	0.40		0.80	0.015		0.03
D	19.85		20.15	0.781		0.793
E	15.45		15.75	0.608		0.620
e		5.45			0.214	
L	14.20		14.80	0.560		0.582
L1	3.70		4.30	0.14		0.17
L2		18.50			0.728	
øP	3.55		3.65	0.140		0.143
øR	4.50		5.50	0.177		0.216
S		5.50			0.216	



# 5 Packaging mechanical data

## D<sup>2</sup>PAK FOOTPRINT



## TAPE AND REEL SHIPMENT

**TAPE MECHANICAL DATA**

DIM.	mm		inch	
	MIN.	MAX.	MIN.	MAX.
A0	10.5	10.7	0.413	0.421
B0	15.7	15.9	0.618	0.626
D	1.5	1.6	0.059	0.063
D1	1.59	1.61	0.062	0.063
E	1.65	1.85	0.065	0.073
F	11.4	11.6	0.449	0.456
K0	4.8	5.0	0.189	0.197
P0	3.9	4.1	0.153	0.161
P1	11.9	12.1	0.468	0.476
P2	1.9	2.1	0.075	0.082
R	50		1.574	
T	0.25	0.35	0.0098	0.0137
W	23.7	24.3	0.933	0.956

**REEL MECHANICAL DATA**

DIM.	mm		inch	
	MIN.	MAX.	MIN.	MAX.
A		330		12.992
B	1.5		0.059	
C	12.8	13.2	0.504	0.520
D	20.2		0.795	
G	24.4	26.4	0.960	1.039
N	100		3.937	
T		30.4		1.197

BASE QTY	BULK QTY
1000	1000

10 pitches cumulative tolerance on tape +/- 0.2 mm

Center line of cavity

User Direction of Feed

FEED DIRECTION

Bending radius R min.

\* on sales type



## 6 Revision history

**Table 8. Revision history**

Date	Revision	Changes
30-Nov-2004	1	First Release.
22-Mar-2005	2	Modified title
23-May-2005	3	Inserted some values in <a href="#">Table 6</a>
08-Jun-2005	4	Inserted new row in <a href="#">Table 5</a>
08-Sep-2005	5	New value for $C_{oss\ eq}$ in <a href="#">Table 5</a>
28-Sep-2005	6	Added curves
26-Oct-2005	7	Complete version
23-Jun-2006	8	New template, new value on <a href="#">Absolute maximum ratings</a>
25-Aug-2006	9	Wrong title on first page
14-Nov-2006	10	Modified <a href="#">Avalanche characteristics</a>
19-Jan-2007	11	Typo mistake on <a href="#">Table 6</a>

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