

## Silicon NPN Power Transistors

## 2SC2189

**DESCRIPTION**

- With TO-3 package
- Wide area of safe operation

**APPLICATIONS**

- For high speed switching and power amplifier applications

**PINNING(see Fig.2)**

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

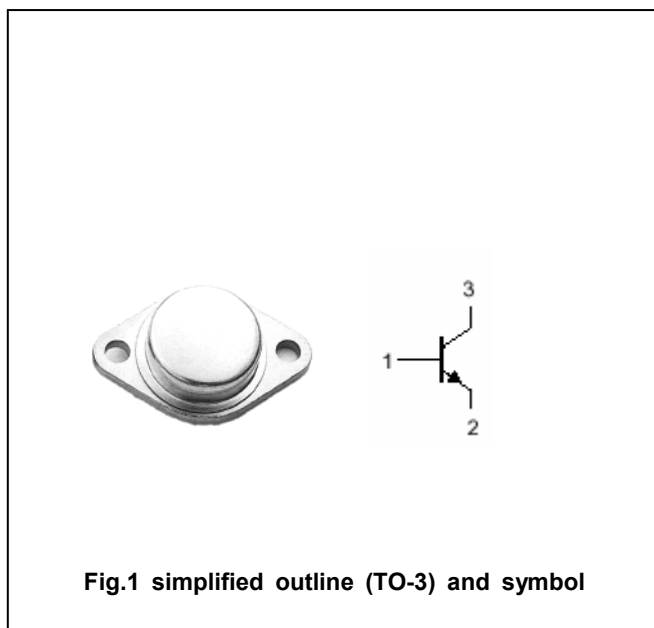


Fig.1 simplified outline (TO-3) and symbol

**Absolute maximum ratings(Ta=□)**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$V_{CBO}$	Collector-base voltage	Open emitter	150	V
$V_{CEO}$	Collector-emitter voltage	Open base	100	V
$V_{EBO}$	Emitter-base voltage	Open collector	5	V
$I_C$	Collector current		10	A
$P_C$	Collector power dissipation	$T_C=25^\circ$	80	W
$T_j$	Junction temperature		150	□
$T_{stg}$	Storage temperature		-55~150	□

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## CHARACTERISTICS

T<sub>j</sub>=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	I <sub>C</sub> =50mA ; I <sub>B</sub> =0	100			V
V <sub>(BR)CBO</sub>	Collector-base breakdown voltage	I <sub>C</sub> =1mA ; I <sub>E</sub> =0	150			V
V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage	I <sub>E</sub> =1mA ; I <sub>C</sub> =0	5			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =4A; I <sub>B</sub> =0.8A			0.8	V
V <sub>BEsat</sub>	Base-emitter saturation voltage	I <sub>C</sub> =4A; I <sub>B</sub> =0.8A			1.5	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =150V; I <sub>E</sub> =0			0.1	mA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =5V; I <sub>C</sub> =0			0.1	mA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> =4A ; V <sub>CE</sub> =5V	40			

PACKAGE OUTLINE



Fig.2 outline dimensions (unindicated tolerance:±0.1mm)