

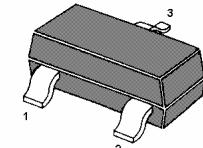
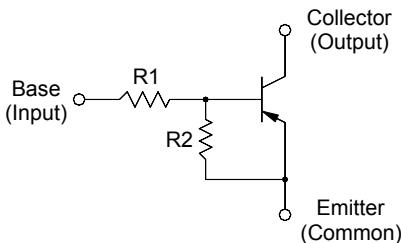
MMBTRA101SS...MMBTRA106SS

PNP Silicon Epitaxial Planar Transistor

for switching and interface circuit and drive circuit applications

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process



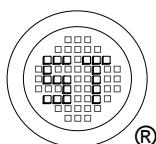
1. Base 2. Emitter 3. Collector
SOT-23 Plastic Package

Resistor Values

Type	R1 (KΩ)	R2 (KΩ)
MMBTRA101SS	4.7	4.7
MMBTRA102SS	10	10
MMBTRA103SS	22	22
MMBTRA104SS	47	47
MMBTRA105SS	2.2	47
MMBTRA106SS	4.7	47

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Value	Unit
Output Voltage	$-V_O$	50	V
Input Voltage	$-V_I$	20, -10	V
		30, -10	
		40, -10	
		40, -10	
		12, -5	
		20, -5	
Output Current	$-I_O$	100	mA
Total Power Dissipation	P_{tot}	200	mW
Junction Temperature	T_j	150	°C
Storage Temperature Range	T_s	- 55 to + 150	°C



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ISO/TS 16949 : 2002
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ISO 14001:2004
Certificate No. 7116



ISO 9001:2000
Certificate No. 0506098

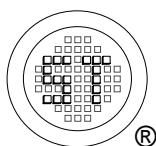
Dated : 08/12/2006

MMBTRA101SS...MMBTRA106SS

Characteristics at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Min.	Typ.	Max.	Unit
DC Current Gain at $-V_O = 5 \text{ V}$, $-I_O = 10 \text{ mA}$	G_I	30	-	-	-
		50	-	-	-
		70	-	-	-
		80	-	-	-
		80	-	-	-
		80	-	-	-
Output Cutoff Current at $-V_O = 50 \text{ V}$	$-I_{O(\text{OFF})}$	-	-	500	nA
Input Current at $-V_I = 5 \text{ V}$	$-I_I$	-	-	1.8	mA
		-	-	0.88	
		-	-	0.36	
		-	-	0.18	
		-	-	3.6	
		-	-	1.8	
Output Voltage at $-I_O = 10 \text{ mA}$, $-I_I = 0.5 \text{ mA}$	$-V_{O(\text{ON})}$	-	-	0.3	V
Input Voltage (ON) at $-V_O = 0.2 \text{ V}$, $-I_O = 5 \text{ mA}$	$-V_{I(\text{ON})}$	-	-	2	V
		-	-	2.4	
		-	-	3	
		-	-	5	
		-	-	1.1	
		-	-	1.3	
Input Voltage (OFF) at $-V_O = 5 \text{ V}$, $-I_O = 0.1 \text{ mA}$	$-V_{I(\text{OFF})}$	1	-	-	V
		0.5	-	-	
Transition Frequency at $-V_O = 10 \text{ V}$, $-I_O = 5 \text{ mA}$	$f_T^{(1)}$	-	200	-	MHz

¹⁾ Characteristic of transistor only.



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