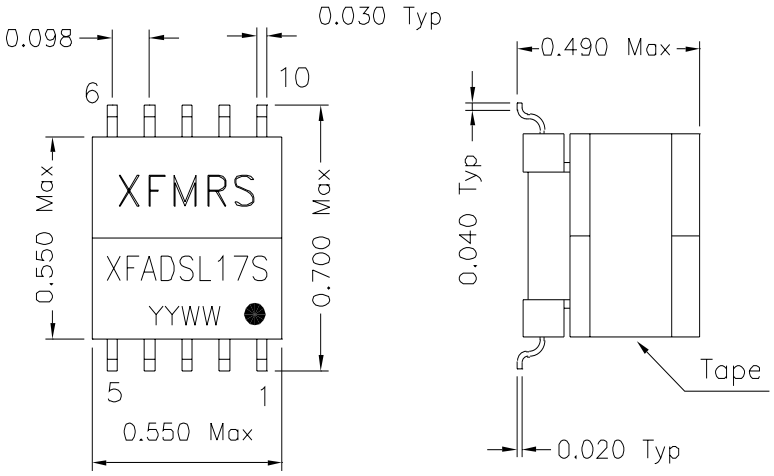
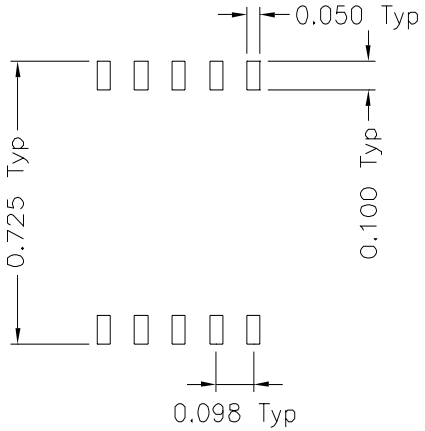
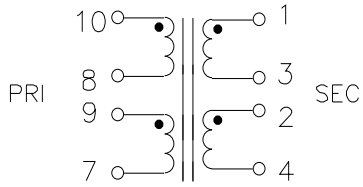
1. Dimensions:





Suggested PCB Layout

2.Schematic:



3.Electrical Specifications: @25°C

Turns Ratio: (P10-7):(P1-4) = 1.1:1 ±1% (TIE P3-2,P8-9)

PIN1-4 OCL: 2.0mH ±10% @20KHz 1.0V (TIE PIN3-2) Ls

PIN1-4 LL: 10uH Max @100KHz 0.1V
(CONNECT PIN3-2, PIN8-9,PIN10-7) Ls

CW/W: PIN1-10 40pF Max. @10KHz 1.0V (TIE P3-2,P8-9) Cs

DC Res.: P10-8,P9-7 0.890 Ohms ±15%
P1-3 0.675 Ohms ±15%
P2-4 0.950 Ohms ±15%

Impedance: Designed to reflect 100 Ohms on the PRI
with 83 Ohms load on SEC

Frequency Response: ±1.0dB 100KHz-1.1MHz, 300KHz ref.

Insertion Loss: 0.5dB Max @300KHz

THD: -80dB Max. @20KHz 4.4V Across PRI 83 Ohms load,
100 Ohms input, TIE P8-9,P3-2

Longitudinal Balance: 45dB Min.@10KHz-1.1MKHz Per ITU Method
(L->M), with terminal 4 grounded.

HIPOT: 2000VAC for 1 second between P10-1(Tie P8-9,P2-3)

Designed to meet UL1950 requirements for Supplementary Insulation with 250 working volts.

DOC. REV A/2

XFMRS Inc	Title: TRANSFORMER		
UNLESS OTHERWISE SPECIFIED TOLERANCES: .xxx ±0.010 Dimensions in INCH	P/N: XFADSL17S		REV. A
	DWN.	廣玉坤	Feb-29-00
	CHK.	李清 IL	Feb-29-00
SHEET 1 OF 1	APP.	Joe Huff	Feb-29-00