TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (Ultra-High-Speed U-MOSIII)

TPCA8021-H

High Efficiency DC / DC Converter Applications
Notebook PC Applications
Portable Equipment Applications

- Small footprint due to a small and thin package
- High speed switching
- Small gate charge: QSW = 6.9nC (typ.)
- Low drain-source ON-resistance: $RDS (ON) = 6.8 \text{ m}\Omega \text{ (typ.)}$
- High forward transfer admittance: $|Y_{fs}| = 46 \mathrm{S}$ (typ.)
- Low leakage current: $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 30 \text{ V)}$
- Enhancement mode: $V_{th} = 1.1 \text{ to } 2.3 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA)}$

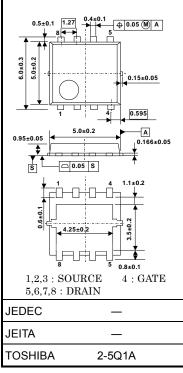
Maximum Ratings (Ta = 25°C)

Characte	eristic	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	30	V	
Drain-gate voltage (R	$k_{GS} = 20 \text{ k}\Omega$)	V_{DGR}	30	V	
Gate-source voltage		V_{GSS}	±20	V	
Drain current	DC (Note 1)	ΙD	27	Α	
Diam curicit	Pulsed (Note 1)	I_{DP}	81	Α .	
Drain power dissipati	on (Tc=25°C)	P_{D}	45	W	
Drain power dissipation	on (t = 10 s) (Note 2a)	P_{D}	2.8	W	
Drain power dissipation	on (t = 10 s) (Note 2b)	P _D	1.6	W	
Single-pulse avalance	he energy (Note 3)	E _{AS}	95	mJ	
Avalanche current		I _{AR}	27	Α	
Repetitive avalanche	energy c=25°C) (Note 4)	E _{AR}	2.7	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature	range	T _{stg}	−55 to 150	°C	

Note: For Notes 1 to 4, refer to the next page.

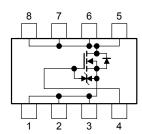
This transistor is an electrostatic-sensitive device. Handle with care.

Unit: mm



Weight: 0.068 g (typ.)

Circuit Configuration

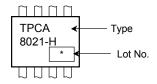




Thermal Characteristics

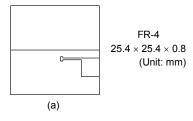
Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case (Tc=25°C)	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	44.6	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	78.1	°C/W

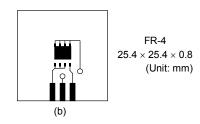
Marking (Note 5)



Note 1: The channel temperature should not exceed 150°C during use.

Note 2: (a) Device mounted on a glass-epoxy board (b) Device mounted on a glass-epoxy board (b)

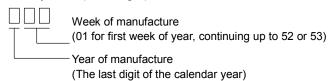




Note 3: VDD = 24 V, Tch = 25°C (initial), L = 0.1 mH, RG = 25 $\Omega,$ IAR = 27 A

Note 4: Repetitive rating: pulse width limited by max. channel temperature

Note 5: * Weekly code: (Three digits)





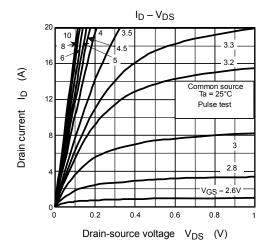
Electrical Characteristics (Ta = 25°C)

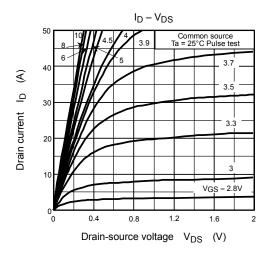
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА	
Drain cutoff curre	nt	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	_	_	10	μΑ	
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10$ mA, $V_{GS} = 0$ V	30	_	_	V	
Diain-source bre	akdown voltage	V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15 — — 1.1 — 2.3		V		
Gate threshold vo	oltage	V _{th}	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	1.1	_	2.3	V	
Drain source ON	into	D	V _{GS} = 4.5 V, I _D = 14 A	_	9.5	13	0	
Drain-source ON-resistance		R _{DS} (ON)	V _{GS} = 10 V, I _D = 14 A	_	6.8	9	mΩ	
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 14 A	23	46	_	S	
Input capacitance	;	C _{iss}			1395	_		
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	140	_	pF	
Output capacitance		C _{oss}		_	525	_		
Switching time	Rise time	t _r	V _{GS} 10 V I _D = 14A C _C C _C	_	5	_	-	
	Turn-on time	t _{on}		_	11	_		
	Fall time	t _f		_	10	_	ns	
	Turn-off time	t _{off}	$V_{DD} \simeq 15 \text{ V}$ Duty $\leq 1\%$, $t_w = 10 \mu\text{s}$	_	31	_		
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \simeq 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 27 \text{ A}$	_	23	_		
			$V_{DD} \simeq 24 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 27 \text{ A}$	_	13	_		
Gate-source charge 1		Q _{gs1}		_	4.5	_	nC	
Gate-drain ("Miller") charge		Q _{gd}	$V_{DD} \simeq 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 27 \text{ A}$	_	4.9	_		
Gate switch charge		Q _{SW}]	_	6.9	_		

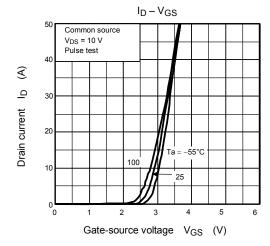
Source-Drain Ratings and Characteristics (Ta = 25°C)

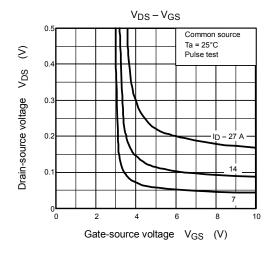
Character	istic		Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse	(Note 1)	I _{DRP}	_	_	_	81	Α
Forward voltage (diode)			V _{DSF}	I _{DR} = 27 A, V _{GS} = 0 V	_	_	-1.2	V

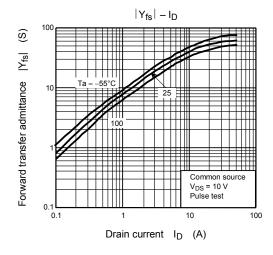
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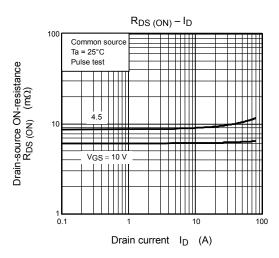




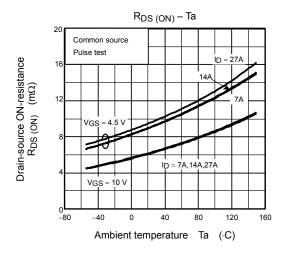


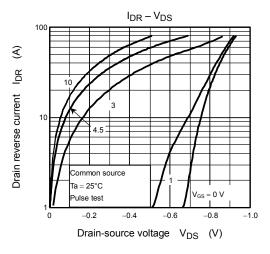


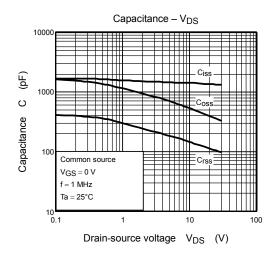


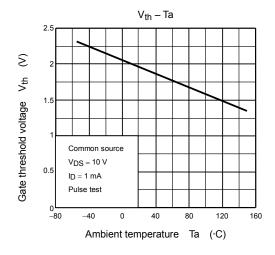


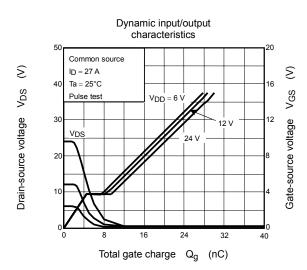
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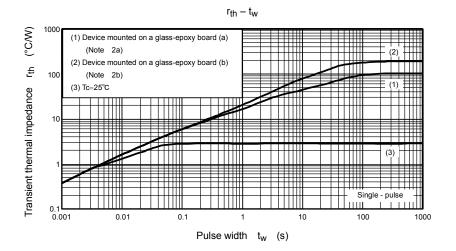


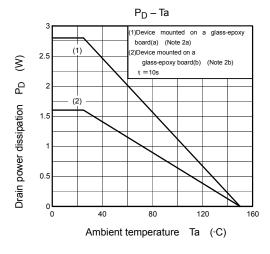


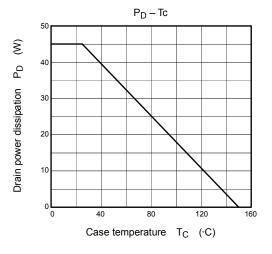


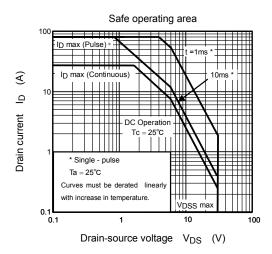












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