## Wireless Bipolar Power Transistor <br> 10W, 178-1.90 GHz

MA-COM Products

## Features

- Designed for cellular base station applications
- -30 dBc typ. 3rd IMD at 10 W PEP
- Common emitter configuration
- Internal input impedance matching
- Diffused emitter ballasting


## ABSOLUTE MAXIMUM RATING AT $25^{\circ} \mathrm{C}$

| Parameter | Symbol | Rating | Units |
| :--- | :---: | :---: | :---: |
| Collector-Base Voltage | $\mathrm{V}_{\mathrm{CBO}}$ | 65 | V |
| Collector-Emitter Voltage | $\mathrm{V}_{\mathrm{CES}}$ | 65 | V |
| Emitter-Base Voltage | $\mathrm{V}_{\text {EBO }}$ | 3.0 | V |
| Collector Current | $\mathrm{I}_{\mathrm{C}}$ | 3.0 | A |
| Power Dissipation | $\mathrm{P}_{\mathrm{D}}$ | 44 | W |
| Junction Temperature | $\mathrm{T}_{\mathrm{J}}$ | 200 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | $\mathrm{T}_{\text {STG }}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |
| Thermal Resistance | $\theta_{\mathrm{JC}}$ | 4.0 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

## Outline Drawing



ELECTRICAL SPECIFICATIONS AT $25^{\circ} \mathrm{C}$

| Parameter | Symbol | Min | Max | Units | Test Conditions |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Collector-Emitter Breakdown Voltage | BV ${ }_{\text {CES }}$ | 65 | - | V | $\mathrm{I}_{\mathrm{C}}=10 \mathrm{~mA}$ |
| Collector-Emitter Leakage Current | $\mathrm{I}_{\text {ces }}$ | - | 2.0 | mA | $\mathrm{V}_{\mathrm{CE}}=25 \mathrm{~V}$ |
| Collector-Emitter Breakdown Voltage | $B V_{\text {cEO }}$ | 20 | - | V | $\mathrm{I}_{\mathrm{C}}=10 \mathrm{~mA}$ |
| Collector-Emitter Breakdown Voltage | $B V_{\text {cer }}$ | 30 | - | V | $\mathrm{I}_{\mathrm{C}}=10 \mathrm{~mA}, \mathrm{R}_{\mathrm{BE}}=220$ ת |
| Emitter-Base Breakdown Voltage | $\mathrm{BV}_{\text {Ebo }}$ | 3.0 | - | V | $\mathrm{I}_{\mathrm{B}}=10 \mathrm{~mA}$ |
| DC Forward Current Gain | $\mathrm{h}_{\text {FE }}$ | 315 | 120 | - | $\mathrm{V}_{\text {CE }}=5 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=250 \mathrm{~mA}$ |
| Power Gain | $\mathrm{G}_{\mathrm{p}}$ | 9.0 | - | dB | $\mathrm{V}_{\mathrm{cc}}=25 \mathrm{~V}, \mathrm{I}_{\mathrm{cQ}}=100 \mathrm{~mA}, \mathrm{P}_{\text {out }}=10 \mathrm{~W}$ PEP, F $=1.78-1.90 \mathrm{GHz}$ |
| Collector Efficiency | ワC | 40 | - | \% | $\mathrm{V}_{\mathrm{cc}}=25 \mathrm{~V}, \mathrm{IcQ}=100 \mathrm{~mA}, \mathrm{P}_{\text {out }}=10 \mathrm{~W}$ PEP, F $=1.78-1.90 \mathrm{GHz}$ |
| Input Return Loss | RL | 10 | - | dB | $\mathrm{V}_{\mathrm{cc}}=25 \mathrm{~V}, \mathrm{I}_{\mathrm{cQ}}=100 \mathrm{~mA}, \mathrm{P}_{\text {out }}=10 \mathrm{~W}$ PEP, F $=1.78-1.90 \mathrm{GHz}$ |
| Load Mismatch Tolerance | VSWR | - | 3.0:1 | - | $\mathrm{V}_{\mathrm{cc}}=25 \mathrm{~V}, \mathrm{IcQ}=100 \mathrm{~mA}$, Pout $=10 \mathrm{~W}$ PEP, F $=1.78-1.90 \mathrm{GHz}$ |
| 3rd Order IMD | $\mathrm{IMD}_{3}$ | - | -28 | dBc | $\mathrm{V}_{\mathrm{cc}}=25 \mathrm{~V}, \mathrm{I}_{\mathrm{CQ}}=100 \mathrm{~mA}, \mathrm{P}_{\text {out }}=10 \mathrm{~W}$ PEP, F $=1.78-1.90 \mathrm{GHz}, \Delta \mathrm{F}=100 \mathrm{kHz}$ |

TYPICAL OPTIMUM DEVICE IMPEDANCES

| $\mathbf{F}(\mathbf{G H z})$ | $\mathbf{Z}_{\mathbf{I N}} \mathbf{( \Omega )}$ | $\mathbf{Z}_{\text {LOAD }}(\boldsymbol{\Omega})$ |
| :---: | :---: | :---: |
| 1.78 | $4.5+\mathrm{j} 7.0$ | $2.5+\mathrm{j} 0.2$ |
| 1.85 | $5.0+\mathrm{j} 7.3$ | $2.5+\mathrm{j} 0$ |
| 1.90 | $6.0+\mathrm{j} 6.1$ | $2.6+\mathrm{j} 0.2$ |



- North America Tel: 800.366 .2266 / Fax: 978.366 .2266
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- Asia/Pacific Tel: 81.44.844.8296 / Fax: 81.44.844.8298 Visit www.macomtech.com for additional data sheets and product information

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TEST FIXTURE ASSEMBLY


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## Typical Broadband Performance Curves




IMD vs $P_{\text {out }}$
$\mathrm{V}_{\mathrm{CC}}=25 \mathrm{~V} \mathrm{I}_{\mathrm{CQ}}=100 \mathrm{~mA}$ F1 $=1850.0 \mathrm{MHz}$ F2 $=1850.1 \mathrm{MHz}$


OUTPUT POWER vs COLLECTOR VOLTAGE
$\mathrm{I}_{\mathrm{CC}}=100 \mathrm{~mA} \quad \mathrm{~F}=1850 \mathrm{MHz}$


3RD ORDER IMD vs Pout



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