

DIM400DDM17-A000

Dual Switch IGBT Module

Replaces DS5549-4.1 June 2002

DS5549-5 June 2009 (LN26749)

FEATURES

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- Non Punch Through Silicon
- Isolated AlSiC Base with AlN Substrates
- Lead Free construction

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM400DDM17-A000 is a dual switch 1700V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM400DDM17-A000

Note: When ordering, please use the complete part number

KEY PARAMETERS

V _{CES}		1700V
V _{CE(sat)}	* (typ)	2.7 V
l _c	(max)	400A
I _{C(PK)}	(max)	800A

^{*} Measured at the power busbars, not the auxiliary terminals

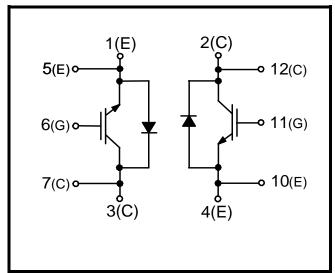


Fig. 1 Circuit configuration

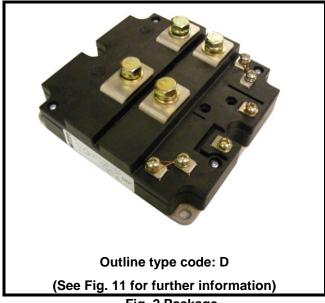


Fig. 2 Package



ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V _{CES}	Collector-emitter voltage	V _{GE} = 0V	1700	V
V_{GES}	Gate-emitter voltage		±20	V
I _C	Continuous collector current	$T_{case} = 75^{\circ}C$	400	Α
I _{C(PK)}	Peak collector current	1ms, T _{case} = 110°C	800	Α
P _{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 150^{\circ}C$	3470	W
l ² t	Diode I ² t value	$V_R = 0$, $t_p = 10$ ms, $T_j = 125$ °C	30	kA ² s
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	4000	V
Q_{PD}	Partial discharge – per module	IEC1287, V ₁ = 1800V, V ₂ = 1300V, 50Hz RMS	10	рC

THERMAL AND MECHANICAL RATINGS

Internal insulation material:

Baseplate material:

Creepage distance:

Clearance:

CTI (Comparative Tracking Index):

AIN

AISiC

20mm

10mm

350

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
R _{th(j-c)}	Thermal resistance – transistor (per switch)	Continuous dissipation - junction to case			36	°C/kW
R _{th(j-c)}	Thermal resistance – diode (per switch)	Continuous dissipation - junction to case			80	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)			8	°C/kW
T _j	Junction temperature	Transistor			150	°C
		Diode			125	°C
T_{stg}	Storage temperature range	-	-40		125	°C
	Screw torque	Mounting – M6			5	Nm
		Electrical connections – M4			2	Nm
		Electrical connections – M8			10	Nm



ELECTRICAL CHARACTERISTICS

 T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
	Collector cut-off current	$V_{GE} = 0V$, $V_{CE} = V_{CES}$			1	mA
I _{CES}		$V_{GE} = 0V$, $V_{CE} = V_{CES}$, $T_{case} = 125^{\circ}$	С		12	mA
I _{GES}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			2	μA
V _{GE(TH)}	Gate threshold voltage	$I_C = 20$ mA, $V_{GE} = V_{CE}$	4.5	5.5	6.5	V
\ \ \ †	Collector-emitter	$V_{GE} = 15V, I_{C} = 400A$		2.7	3.2	V
V _{CE(sat)} †	saturation voltage	$V_{GE} = 15V, I_C = 400A, T_j = 125^{\circ}C$		3.4	4.0	V
I _F	Diode forward current	DC			400	Α
I _{FM}	Diode maximum forward current	t _p = 1ms			800	Α
.,	Diode forward voltage	I _F = 400A		2.2	2.5	V
V_{F}		I _F = 400A, T _j = 125°C		2.3	2.6	V
C _{ies}	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		30		nF
Qg	Gate charge	±15V		4.5		μC
C _{res}	Reverse transfer capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		-		nF
L _M	Module inductance – per switch	-		20		nΗ
R _{INT}	Internal transistor resistance – per switch	-		270		μΩ
SC _{Data}	Short circuit current, I _{SC}	$T_j = 125^{\circ}\text{C}, V_{CC} = 1000\text{V}$ $t_p \le 10\mu\text{s}, V_{GE} \le 15\text{V}$	I ₁	1850		Α
		$V_{CE (max)} = V_{CES} - L^* x dI/dt$ IEC 60747-9		1600		А

Note:

 $[\]stackrel{\uparrow}{}$ Measured at the power busbars, not the auxiliary terminals L is the circuit inductance + L_{M}



ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	$I_{C} = 400A$ $V_{GF} = \pm 15V$		1150		ns
t _f	Fall time			100		ns
E _{OFF}	Turn-off energy loss	$V_{GE} = £13V$ $V_{CE} = 900V$		120		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = 4.7\Omega$ $R_{G(OFF)} = 4.7\Omega$ $L_{S} \sim 100 \text{nH}$		250		ns
t _r	Rise time			250		ns
E _{ON}	Turn-on energy loss			150		mJ
Q_{rr}	Diode reverse recovery charge	$I_F = 400A$ $V_{CE} = 900V$ $dI_F/dt = 3000A/\mu s$		100		μC
I _{rr}	Diode reverse recovery current			230		Α
E _{rec}	Diode reverse recovery energy			70		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time			1400		ns
t _f	Fall time	$I_C = 400A$ $V_{GF} = \pm 15V$		130		ns
E _{OFF}	Turn-off energy loss	$V_{CE} = 900V$		180		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = 4.7\Omega$ $R_{G(OFF)} = 4.7\Omega$ $L_{S} \sim 100 \text{nH}$		400		ns
t _r	Rise time			250		ns
E _{ON}	Turn-on energy loss			170		mJ
Q_{rr}	Diode reverse recovery charge	$I_F = 400A$ $V_{CE} = 900V$ $dI_F/dt = 2500A/\mu s$		170		μC
I _{rr}	Diode reverse recovery current			270		Α
E _{rec}	Diode reverse recovery energy			100		mJ



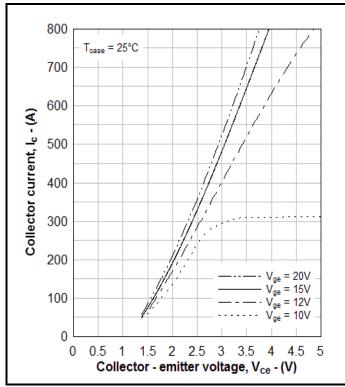


Fig. 3 Typical output characteristics

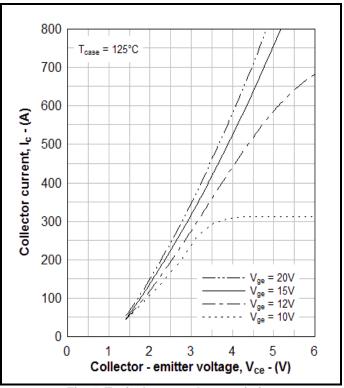


Fig. 4 Typical output characteristics

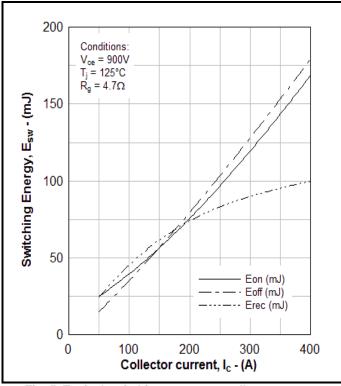


Fig. 5 Typical switching energy vs collector current

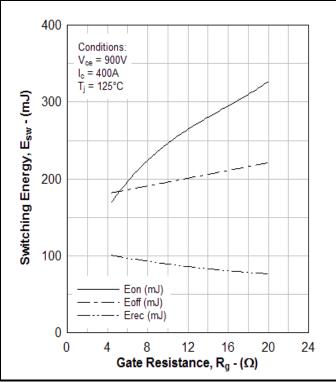


Fig. 6 Typical switching energy vs gate resistance



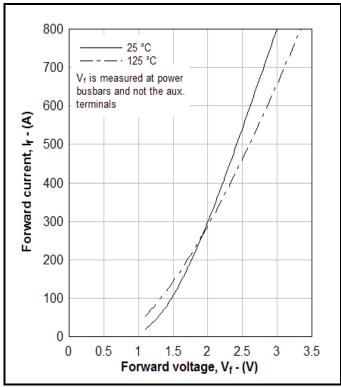


Fig. 7 Diode typical forward characteristics

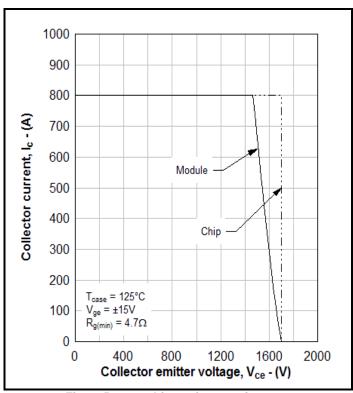


Fig. 8 Reverse bias safe operating area

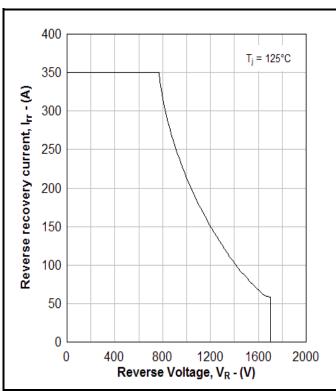


Fig. 9 Diode reverse bias safe operating area

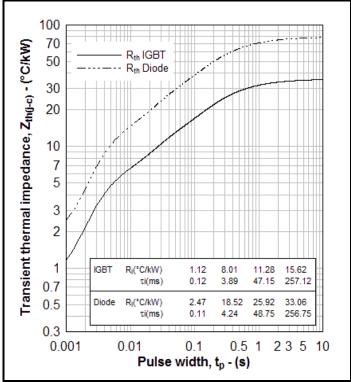


Fig. 10 Transient thermal impedance



PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

DO NOT SCALE.

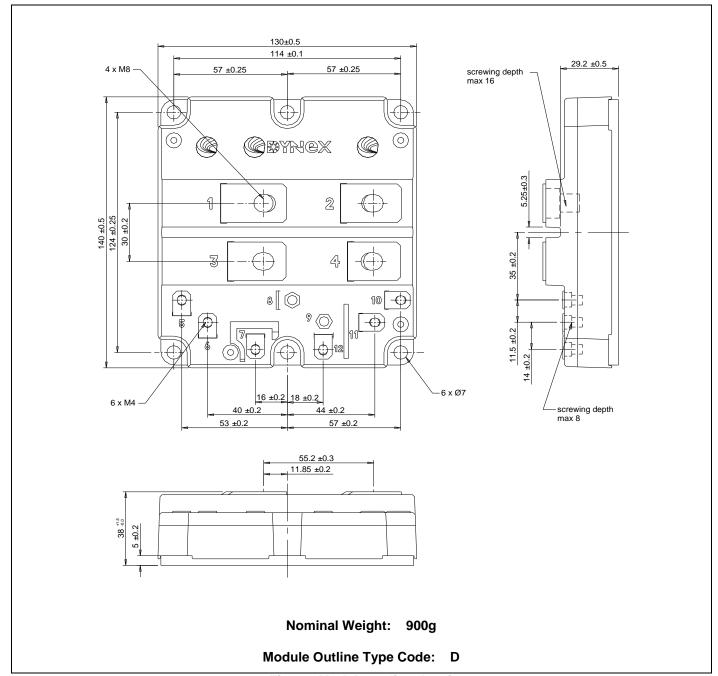


Fig. 11 Module outline drawing



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