

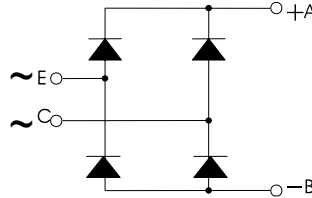
Single Phase Rectifier Bridges

PSB 61

I_{dAVM} = 65 A
 V_{RRM} = 800-1800 V

Preliminary Data Sheet

V_{RSM} V	V_{RRM} V	Type
800	800	PSB 61/08
1200	1200	PSB 61/12
1400	1400	PSB 61/14
1600	1600	PSB 61/16
1800	1800	PSB 61/18



Symbol	Test Conditions	Maximum Ratings
I_{dAVM}	$T_C = 100^\circ\text{C}$, module	65 A
I_{FSM}	$T_{VJ} = 45^\circ\text{C}$ $V_R = 0$ $t = 10$ ms (50 Hz), sine	1000 A
	$t = 8.3$ ms (60 Hz), sine	1100 A
	$T_{VJ} = T_{VJM}$ $V_R = 0$ $t = 10$ ms (50 Hz), sine	900 A
	$t = 8.3$ ms (60 Hz), sine	1000 A
$\int i^2 dt$	$T_{VJ} = 45^\circ\text{C}$ $V_R = 0$ $t = 10$ ms (50 Hz), sine	5000 A^2s
	$t = 8.3$ ms (60 Hz), sine	5050 A^2s
	$T_{VJ} = T_{VJM}$ $V_R = 0$ $t = 10$ ms (50 Hz), sine	4050 A^2s
	$t = 8.3$ ms (60 Hz), sine	4170 A^2s
T_{VJ}		-40 ... + 150 $^\circ\text{C}$
T_{VJM}		150 $^\circ\text{C}$
T_{stg}		-40 ... + 125 $^\circ\text{C}$
V_{ISOL}	50/60 HZ, RMS $t = 1$ min	2500 V ~
	$I_{ISOL} \leq 1$ mA $t = 1$ s	3000 V ~
M_d	Mounting torque (M5)	2-2.5 Nm
Weight	typ.	100 g

Features

- Package with fast-on terminals
- Isolation voltage 3000 V~
- Planar glasspassivated chips
- Blocking voltage up to 1800 V
- Low forward voltage drop
- UL registered E 148688

Applications

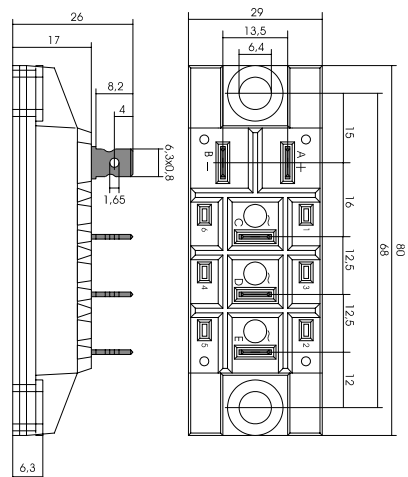
- Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
- Field supply for DC motors

Advantages

- Easy to mount with two screws
- Space and weight savings
- Improved temperature and power cycling capability

Package, style and outline

Dimensions in mm (1mm = 0.0394")



Symbol	Test Conditions	Characteristic Value
I_R	$V_R = V_{RRM}$ $T_{VJ} = 25^\circ\text{C}$	≤ 0.12 mA
	$V_R = V_{RRM}$ $T_{VJ} = T_{VJM}$	≤ 3 mA
V_F	$I_F = 150$ A $T_{VJ} = 25^\circ\text{C}$	≤ 1.45 V
V_{TO}	For power-loss calculations only	0.8 V
r_T	$T_{VJ} = T_{VJM}$	5 $\text{m}\Omega$
R_{thJC}	per diode; DC current	1.12 K/W
	per module	0.28 K/W
R_{thJK}	per diode; DC current	1.49 K/W
	per module	0.37 K/W
d_S	Creeping distance on surface	21 mm
d_A	Creeping distance in air	12.4 mm
a	Max. allowable acceleration	50 m/s^2

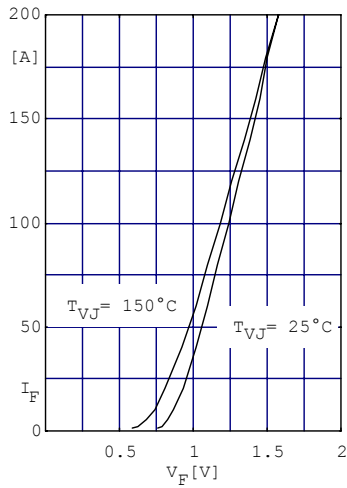


Fig. 1 Forward current versus voltage drop per diode

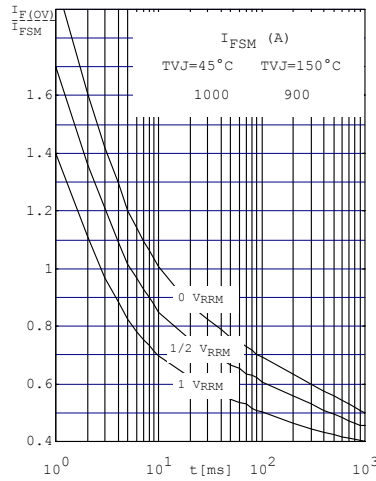


Fig. 2 Surge overload current per diode I_{FSM} : Crest value. t : duration

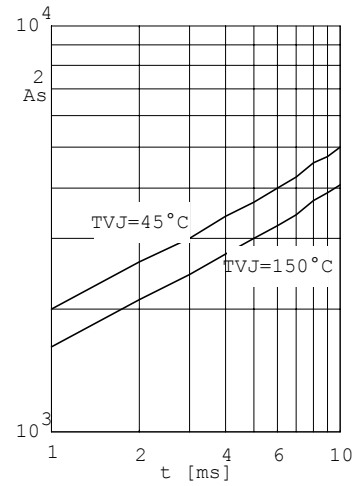


Fig. 3 $\int i^2 dt$ versus time (1-10ms) per diode (or thyristor)

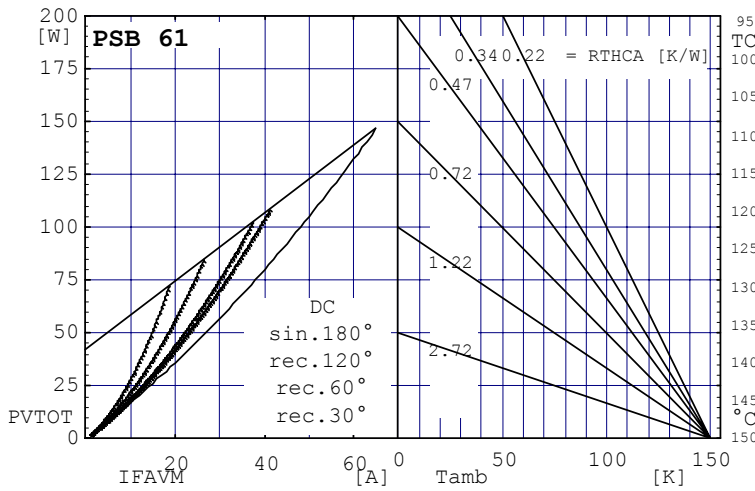


Fig. 4 Power dissipation versus direct output current and ambient temperature

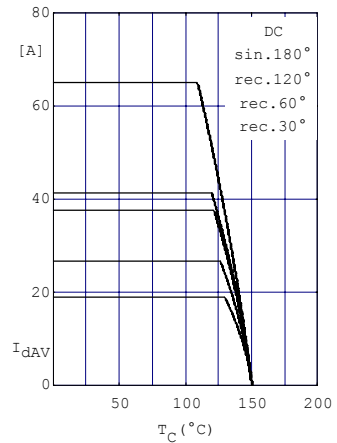


Fig.5 Maximum forward current at case temperature

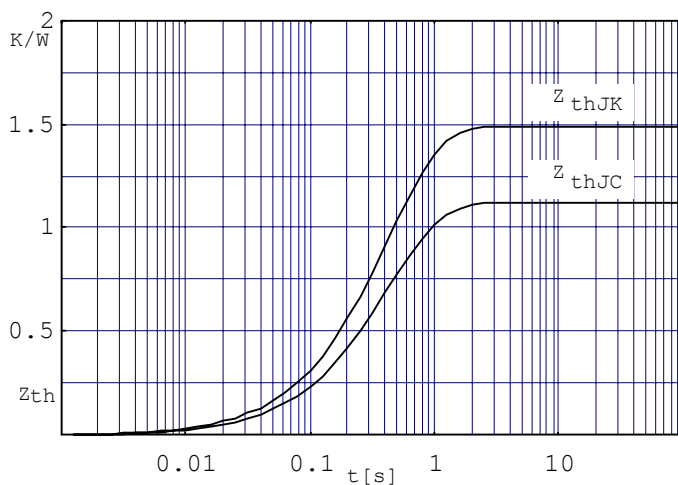


Fig. 6 Transient thermal impedance per diode (or thyristor), calculated