30V Complementary PowerTrench MOSFET
KI4542DY

■ Features

● N-Channel

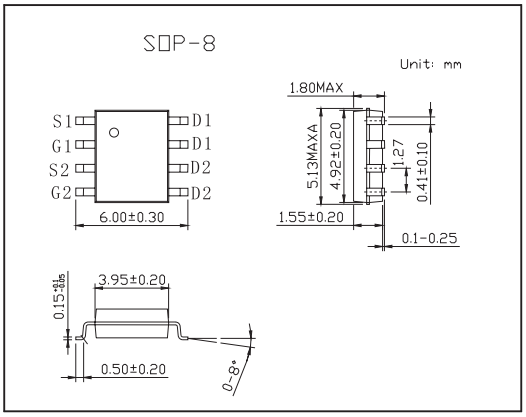
6 A, 30 V $R_{DS(ON)}$ = 28mΩ @ V_{GS} = 10V

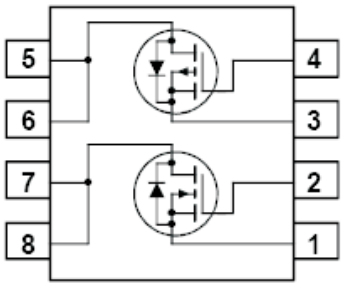
$R_{DS(ON)}$ = 35mΩ @ V_{GS} =4.5V

● P-Channel

-6 A, -30 V $R_{DS(ON)}$ = 32mΩ @ V_{GS} =- 10 V

$R_{DS(ON)}$ = 45mΩ @ V_{GS} =-4.5V





■ Absolute Maximum Ratings Ta = 25℃

Parameter	Symbol	N-Channel	P- Channel	Unit
Drain to Source Voltage	V_{DSS}	30	-30	V
Gate to Source Voltage	V_{GS}	±20	±20	V
Drain Current Continuous (Note 1a)	I_D	6	-6	A
Drain Current Pulsed		20	-20	A
Power Dissipation for Single Operation	P_D	2		W
Power Dissipation for Single Operation (Note 1a)	P_D	1.6		W
(Note 1b)		1.2		
(Note 1c)		1		
Operating and Storage Temperature	T_J, T_{STG}	-55 to 175		℃
Thermal Resistance Junction to Ambient (Note 1a)	$R_{\theta JA}$	78		℃/W
Thermal Resistance Junction to Case (Note 1)	$R_{\theta JC}$	40		℃/W

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■ Electrical Characteristics Ta = 25℃

Parameter	Symbol	Testconditons		Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	B_{VDSS}	V_{GS} = 0 V, I_D = 250 μA	N-Ch	30			V
		V_{GS} = 0 V, I_D = -250 μA	P-Ch	-30			
Breakdown Voltage Temperature Coefficient	$\frac{\Delta BV_{DSS}}{\Delta T_J}$	I_D = 250 μA, Referenced to 25℃	N-Ch		23		mV/℃
		I_D = -250 μA, Referenced to 25℃	P-Ch		-21		
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 24V, V_{GS} = 0 V	N-Ch			1	μA
		V_{DS} = -24 V, V_{GS} = 0 V	P-Ch			-1	
Gate-Body Leakage	I_{GSS}	V_{GS} = ±20V, V_{DS} = 0 V	N-Ch			±100	nA
		V_{GS} = ±20 V, V_{DS} = 0 V	P-Ch			±100	
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 μA	N-Ch	1	1.5	3	V
		V_{DS} = V_{GS}, I_D = -250 μA	P-Ch	-1	-1.7	-3	
Gate Threshold Voltage Temperature Coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	I_D = 250 μA, Referenced to 25℃	N-Ch		-4		mV/℃
		I_D = -250 μA, Referenced to 25℃	P-Ch		4		
Static Drain-Source On-Resistance	$R_{DS(on)}$	V_{GS} = 10 V, I_D =6A	N-Ch		19	28	mΩ
		V_{GS} = 10 V, I_D =6A,T_J=125℃			32	48	
		V_{GS} = 4.5 V, I_D = 5A			25	35	
Static Drain-Source On-Resistance	$R_{DS(on)}$	V_{GS} = -10 V, I_D =-6 A	P-Ch		21	32	
		V_{GS} = -10 V, I_D =-5 A,T_J=125℃			29	51	
		V_{GS} = -4.5 V, I_D =-5A			30	45	
On-State Drain Current	$I_{D(on)}$	V_{GS} = 10 V, V_{DS} = 5V	N-Ch	20			A
		V_{GS} = -10 V, V_{DS} = -5V	P-Ch	-20			
Forward Transconductance	g_{FS}	V_{DS} = 15V, I_D = 6A	N-Ch		18		S
		V_{DS} = -10V, I_D = -6A	P-Ch		16		
Input Capacitance	C_{iss}	N-Channel	N-Ch		830		pF
		V_{DS} = 15 V, V_{GS} = 0 V,f = 1.0 MHz	P-Ch		1540		
Output Capacitance	C_{oss}		N-Ch		185		pF
		P-Channel	P-Ch		400		
Reverse Transfer Capacitance	C_{rss}	V_{DS} = -15 V, V_{GS} = 0 V,f = 1.0 MHz	N-Ch		80		pF
			P-Ch		170		
Turn-On Delay Time	$t_{d(on)}$	N-Channel	N-Ch		6	12	ns
		V_{DD} = 15 V, I_D = 1 A,	P-Ch		13	24	
Turn-On Rise Time	tr	V_{GS} = 10 V, R_{GEN} = 6 Ω (Note 2)	N-Ch		10	18	ns
			P-Ch		22	35	
Turn-Off Delay Time	$t_{d(off)}$	P-Channel	N-Ch		18	29	ns
		V_{DD} = -15 V, I_D = -1 A,	P-Ch		47	75	
Turn-Off Fall Time	t_f	V_{GS} = -10 V, R_{GEN} = 6 Ω (Note 2)	N-Ch		5	12	ns
			P-Ch		18	30	
Total Gate Charge	Q_g	N-Channel	N-Ch		9	13	nC
		V_{DS} =15V,I_D=7.5A,V_{GS}=5V(Note 2)	P-Ch		15	20	
Gate-Source Charge	Q_{gs}		N-Ch		2.8		nC
		P-Channel	P-Ch		4		
Gate-Drain Charge	Q_{gd}	V_{DS}=-10V,I_D=-6A,V_{GS}=-5V(Note 2)	N-Ch		3.1		nC
			P-Ch		5		

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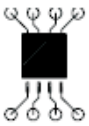
Parameter	Symbol	Testconditons		Min	Typ	Max	Unit
Maximum Continuous Drain-Source Diode Forward Current	Is		N-Ch			1.3	A
			P-Ch			-1.3	
Drain-Source Diode Forward Voltage	VSD	VGS = 0 V, Is = 1.3A (Not 2)	N-Ch		0.7	1.2	V
		VGS = 0 V, Is = -1.3A (Not 2)	P-Ch		-0.7	-1.2	

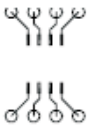
Notes:

1. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design while $R_{\theta CA}$ is determined by the user's board design.

a) 78°C/W when mounted on a 0.5 in^2 pad of 2 oz copper



b) 125°C/W when mounted on a .02 in^2 pad of 2 oz copper



c) 135°C/W when mounted on a minimum pad.

Scale 1 : 1 on letter size paper

2. Pulse Test: Pulse Width < 300μs, Duty Cycle < 2.0%