

SD4931

RF power transistors HF/VHF/UHF N-channel MOSFETs

Features

- Improved ruggedness V_{(BR)DSS} > 200 V
- Excellent thermal stability
- 20:1 all phases load mismatch capability
- P_{OUT} = 150 W min.
 with 14.8 dB gain @ 175 MHz
- In compliance with the 2002/95/EC european directive

Description

The SD4931 is a N-channel MOS field-effect RF power transistor. It is intended for use in 50 V DC large signal applications up to 250 MHz.

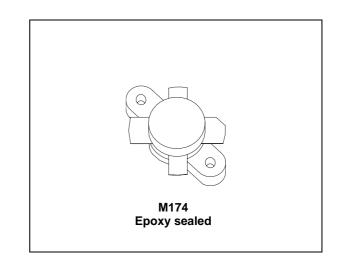
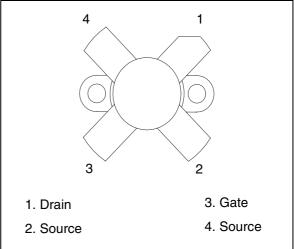


Figure 1. Pin connection



1 Electrical data

1.1 Maximum ratings

Absolute maximum ratings (TCASE = 25 C)					
Symbol	Parameter	Value	Unit		
V _{(BR)DSS}	Drain source voltage	200	V		
V _{DGR}	Drain-gate voltage ($R_{GS} = 1 M\Omega$)	200	V		
V _{GS}	Gate-source voltage	±20	V		
Ι _D	Drain current	20	Α		
P _{DISS}	Power dissipation	389	W		
TJ	Max. operating junction temperature	200	°C		
T _{STG}	Storage temperature	-65 to +150	°C		

Table 2. Absolute maximum ratings ($T_{CASE} = 25^{\circ}C$)

1.2 Thermal data

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R _{thJC}	Junction - case thermal resistance	0.45	°C/W



2 Electrical characteristics

 $T_{CASE} = +25 \ ^{\circ}C$

2.1 Static

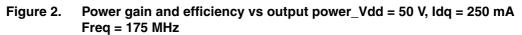
Table 4.	Static						
Symbol		Test conditions		Min	Тур	Max	Unit
V _{(BR)DSS}	$V_{GS} = 0 V$	I _{DS} = 100 mA		200			V
I _{DSS}	V _{GS} = 0 V	V _{DS} = 100 V				1	mA
I _{GSS}	V _{GS} = 20 V	$V_{DS} = 0 V$				250	nA
V _{TH}	I _D = 250 V			1.5	2.5	4.0	V
V _{DS(ON)}	V _{GS} = 10 V	I _D = 10 A			3.5	5.0	V
G _{FS}	V _{DS} = 10 V	I _D = 2.5 A		2.5	4.0		S
C _{ISS}	V _{GS} = 0 V	V _{DS} = 50 V	f = 1 MHz		500		pF
C _{OSS}	V _{GS} = 0 V	$V_{DS} = 50 V$	f = 1 MHz		200		pF
C _{RSS}	$V_{GS} = 0 V$	$V_{DS} = 50 V$	f = 1 MHz		8		pF

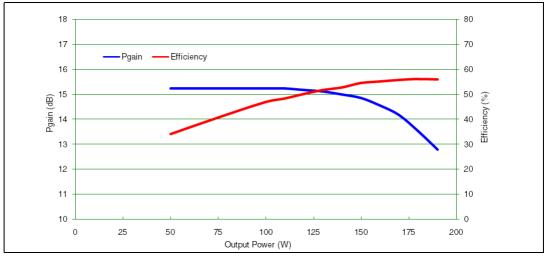
2.2 Dynamic

Symbol	Test conditions	Min	Тур	Max	Unit
P _{1dB}	$V_{DD} = 50 \text{ V}$ $I_{DQ} = 250 \text{ mA}$ $f = 175 \text{ MHz}$	150	175		W
G _{PS}	$V_{DD} = 50 \text{ V} \text{ I}_{DQ} = 250 \text{ mA} \text{ P}_{OUT} = 150 \text{ W} \text{ f} = 175 \text{ MHz}$	13	14.8		dB
n _D	$V_{DD} = 50 \text{ V}$ $I_{DQ} = 250 \text{ mA}$ $P_{OUT} = 150 \text{ W} \text{ f} = 175 \text{ MHz}$	50	56		%
	$V_{DD} = 50 \text{ V}$ I _{DQ} = 250 mA P _{OUT} = 150 W f = 175 MHz All phase angles	10:1	20:1		VSWR



3 Typical performance





Marking	Min.	Max.
DD	1.5	1.6
EE	1.6	1.7
FF	1.7	1.8
A	1.8	1.9
В	1.9	2
С	2	2.1
D	2.1	2.2
E	2.2	2.3
F	2.3	2.4
G	2.4	2.5
Н	2.5	2.6
I	2.6	2.7
J	2.7	2.8
К	2.8	2.9
L	2.9	3
М	3	3.1
N	3.1	3.2
0	3.2	3.3
Р	3.3	3.4



Table 0. Vgs Soft (@250 mA) (continued)					
Marking	Min.	Max.			
Q	3.4	3.5			
R	3.5	3.6			
S	3.6	3.7			
Т	3.7	3.8			
U	3.8	3.9			
V	3.9	4			

Table 6.Vgs sort (@250 mA) (continued)



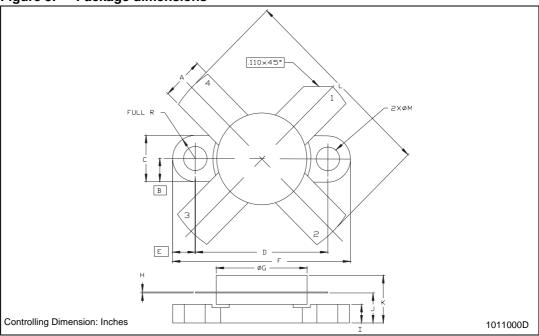
4 Package mechanical data

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Dim		mm.			Inch			
Dim.	Min	Тур	Max	Min	Тур	Max		
А	5.56		5.584	0.219		0.230		
В		3.18			0.125			
С	6.22		6.48	0.245		0.255		
D	18.28		18.54	0.720		0.730		
E		3.18			0.125			
F	24.64		24.89	0.970		0.980		
G	12.57		12.83	0.495		0.505		
Н	0.08		0.18	0.003		0.007		
Ι	2.11		3.00	0.083		0.118		
J	3.81		4.45	0.150		0.175		
К			7.11			0.280		
L	25.53		26.67	1.005		1.050		
М	3.05		3.30	0.120		0.130		

 Table 7.
 M174 (0.500 DIA 4/L N/HERM W/FLG) mechanical data









5 Revision history

Table 8.Document revision history

Date	Revision	Changes	
17-Mar-2008	1	Initial release.	
14-Jan-2010	2	Updated test conditions in Table 5: Dynamic.	



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