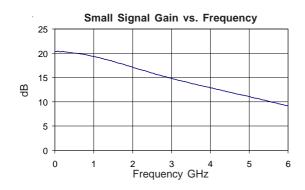


### **Product Description**

The SGA-0363 is a high performance SiGe HBT MMIC Amplifier. A Darlington configuration featuring 1 micron emitters provides high FT and excellent thermal perfomance. The heterojunction increases breakdown voltage and minimizes leakage current between junctions. Cancellation of emitter junction non-linearities results in higher suppression of intermodulation products. Only 2 DC-blocking capacitors, a bias resistor and an optional RF choke are required for operation.

The matte tin finish on Sirenza's lead-free package utilizes a post annealing process to mitigate tin whisker formation and is RoHS compliant per EU Directive 2002/95. This package is also manufactured with green molding compounds that contain no antimony trioxide nor halogenated fire retardants.



# **SGA-0363**

**SGA-0363Z** 



DC-5000 MHz, Silicon Germanium Cascadeable Gain Block



#### **Product Features**

- Now available in Lead Free, RoHS Compliant, & Green Packaging
- DC-5000 MHz Operation
- Single Voltage Supply
- Low Current Draw: 11mA at 2.5V typ.
- · High Output Intercept: 14 dBm typ. at 1950MHz

## **Applications**

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- IF Amplifier
- Wireless Data, Satellite

Symbol	Parameter	Frequency	Units	Min.	Тур.	Max.
$P_{1dB}$	Output Power at 1dB Compression	850 MHz 1950 MHz 2400 MHz	dBm dBm dBm		2.3 2.3 1.6	
IP <sub>3</sub>	Third Order Intercept Point	850 MHz 1950 MHz 2400 MHz	dBm dBm dBm		14.2 14.0 13.1	
S <sub>21</sub>	Small Signal Gain	850 MHz 1950 MHz 2400 MHz	dB dB dB		19.6 17.2 16.2	
BW <sub>3dB</sub>	3dB Bandwidth		MHz		5000	
VSWR <sub>IN</sub>	Input VSWR	DC - 4500MHz	-		1.8:1	
VSWR <sub>OUT</sub>	Output VSWR	DC - 4500MHz	-		1.7:1	
S <sub>12</sub>	Reverse Isolation	850 MHz 1950 MHz 2400 MHz	dB dB dB		24.0 22.8 22.1	
NF	Noise Figure	1950 MHz	dB		3.0	
V <sub>D</sub>	Device Operating Voltage		V		2.5	
I <sub>D</sub>	Device Operating Current		mA	9	11	13
R <sub>TH</sub> , j-l	Thermal Resistance (junction - lead)		°C/W		255	

**Test Conditions:** 

 $V_s = 5 V$  $R_{BIAS} = 220 Ohms$   $I_D = 11 \text{ mA Typ.}$   $T_L = 25^{\circ}\text{C}$ 

 $\mathrm{OIP_3}$  Tone Spacing = 1 MHz, Pout per tone = -12 dBm  $\mathrm{Z_s}$  =  $\mathrm{Z_i}$  = 50 Ohms

The information provided herein is believed to be reliable at press time. Sirenza Microdevices assumes no responsibility for inaccuracies or omissions. Sirenza Microdevices assumes no responsibility for the use of this information, and all such information shall be entirely at the user's own risk. Prices and specifications are subject to change without notice. No patent rights or licenses to any of the circuits described herein are implied or granted to any third party. Sirenza Microdevices does not authorize or warrant any Sirenza Microdevices product for use in life-support devices and/or systems. Copyright 2001 Sirenza Microdevices, Inc.. All worldwide rights reserved.



### Key parameters, at typical operating frequencies:

	Typical		Test Condition
Parameter	25ºC	Unit	(I <sub>n</sub> = 11mA, unless otherwise noted)
100 MHz			
Gain	20.4	dB	
Output IP3	14.8	dBm	Tone spacing = 1 MHz, Pout per tone = -12dBm
Output P1dB	3.2	dBm	
Input Return Loss	9.3	dB	
Reverse Isolation	23.9	dB	
Noise Figure	2.9	dB	$Z_s = 50 \text{ Ohms}$
500 MHz			
Gain	20.0	dB	
Output IP3	14.5	dBm	Tone spacing = 1 MHz, Pout per tone = -12dBm
Output P1dB	2.9	dBm	
Input Return Loss	9.4	dB	
Reverse Isolation	23.9	dB	
Noise Figure	2.8	dB	$Z_s = 50 \text{ Ohms}$
850 MHz			
Gain	19.6	dB	
Output IP3	14.2	dBm	Tone spacing = 1 MHz, Pout per tone = -12dBm
Output P1dB	2.3	dBm	
Input Return Loss	9.4	dB	
Reverse Isolation	24.0	dB	
Noise Figure	3.0	dB	$Z_s = 50 \text{ Ohms}$
1950 MHz			
Gain	17.2	dB	
Output IP3	14.0	dBm	Tone spacing = 1 MHz, Pout per tone = -12dBm
Output P1dB	2.3	dBm	
Input Return Loss	10.4	dB	
Reverse Isolation	22.8	dB	
Noise Figure	3.0	dB	$Z_s = 50 \text{ Ohms}$
2400 MHz			
Gain	16.2	dB	
Output IP3	13.1	dBm	Tone spacing = 1 MHz, Pout per tone = -12dBm
Output P1dB	1.6	dBm	
Input Return Loss	10.8	dB	
Reverse Isolation	22.1	dB	
3500 MHz			
Gain	13.8	dB	
Output IP3	11.5	dBm	Tone spacing = 1 MHz, Pout per tone = -12dBm
Output P1dB	0.8	dBm	
Input Return Loss	11.3	dB	
Reverse Isolation	20.1	dB	

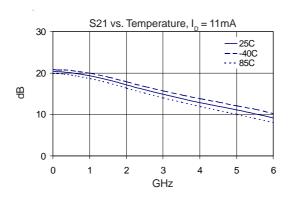
### **Absolute Maximum Ratings**

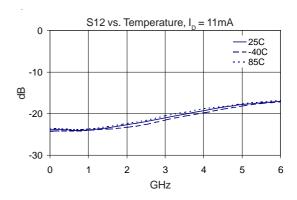
Parameter	Absolute Limit
Max. Device Current (I <sub>D</sub> )	22 mA
Max. Device Voltage (V <sub>D</sub> )	6 V
Max. RF Input Power	-5 dBm
Max. Junction Temp. (T <sub>J</sub> )	+150°C
Operating Temp. Range (T <sub>L</sub> )	-40°C to +85°C
Max. Storage Temp.	+150°C

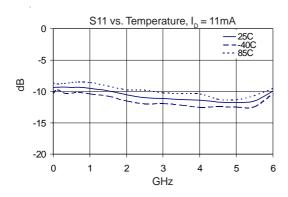
Operation of this device beyond any one of these limits may cause permanent damage. For reliable continous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one.

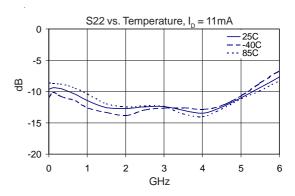
Bias conditions should also satisfy the following expression:  $I_D V_D < (T_J - T_L) \ / \ R_{TH}, \ j\text{-}I$ 

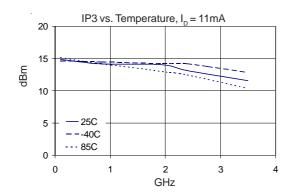


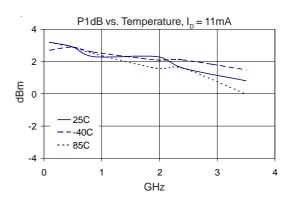






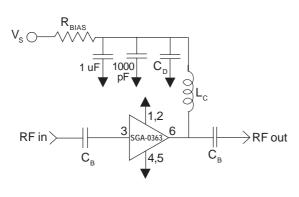


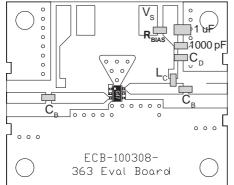




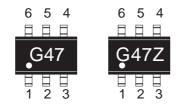


### **Basic Application Circuit**





### Part Identification Marking





### **Application Circuit Element Values**

Reference		Fr	equency (N	(lhz)	
Designator	500	850	1950	2400	3500
C <sub>B</sub>	220 pF	100 pF	68 pF	56 pF	39 pF
C <sub>D</sub>	100 pF	68 pF	22 pF	22 pF	15 pF
L <sub>c</sub>	68 nH	33 nH	22 nH	18 nH	15 nH

Recommended Bias Resistor Values for $I_p$ =11mA $R_{BIAS}$ =( $V_s$ - $V_p$ ) / $I_p$				
Supply Voltage(V <sub>S</sub> )	5 V	7.5 V	9 V	12 V
R <sub>BIAS</sub>	220 Ω	470 Ω	620 Ω	910 Ω
Note: P provides DC bias stability over temperature				

#### **Mounting Instructions**

- 1. Use a large ground pad area near device pins 1, 2, 4, and 5 with many plated through-holes as shown.
- 2. We recommend 1 or 2 ounce copper. Measurements for this data sheet were made on a 31 mil thick FR-4 board with 1 ounce copper on both sides.

Pin #	Function	Description
3	RF IN	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.
1, 2, 4, 5	GND	Connection to ground. Use via holes for best performance to reduce lead inductance as close to ground leads as possible.
6	RF OUT/ BIAS	RF output and bias pin. DC voltage is present on this pin, therefore a DC blocking capacitor is necessary for proper operation.

Part Number	Reel Size	Devices/Reel
SGA-0363	7"	3000
SGA-0363Z	7"	3000

board thicknesses and dielectric contants.

3. We recommend 1 or 2 ounce copper. Measurements for this data sheet were made on a 31 mil thick Getek with 1 ounce copper on both sides.



#### **SOT-363 PCB Pad Layout** Dimensions in inches [millimeters] 0.056 [1.42] (2X) 0.018 [0.46] (2X) Ø0.018 [Ø0.46] Ground Via (6X) 0.018 [0.46] (2X) -0.044 [1.10] (2X) 0.178 [4.52] RF 0.015 [0.38] (2X) OUT 0.051 [1.30] П $\Box$ RF -0.059 [1.50] (2X) IN 0.097 [2.46] (2X) 0.064 [1.61] (2X) 0.027 [0.69] 0.037 [0.94] (2X) 0.025 [0.62] (4X) **DEVICE SHOWN** 0.052 [1.31] (2X) -FOR REFERENCE ONLY 0.020 [0.51] (2X) Notes: 1. Provide a large ground pad area under device 0.017 [0.43] (2X) pins 1, 2, 4, & 5 with many plated via holes as 0.093 [2.36] (2X) shown. 2. Dimensions given for 50 Ohm RF I/O lines are for 31 mil thick Getek. Scale accordingly for different

### **SOT-363 Nominal Package Dimensions**

Dimensions in inches [millimeters]

A link to the SOT-363 package outline drawing with full dimensions and tolerances may be found on the product web page at www.sirenza.com.

