REVISIONS			
LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
A	Changes in accordance with NOR 5962-R252-94.	94-07-25	K. A. Cottongim
B	Add case outlines T, U, Y, and Z.	98-02-23	K. A. Cottongim
C	Added device type 02. Updated table I for the addition of RadHard limits for the device type 02. Updated paragraph 4.3.5 to add the RadHard requirements. Redrew entire document. -sld.	02-06-21	Raymond Monnin

REV																				
SHEET																				
REV	C	C	C																	
SHEET	15	16	17																	

REV STATUS	REV	C	C	C	C	C	C	C	C	C	C	C	C	C	C
OF SHEETS	SHEET	1	2	3	4	5	6	7	8	9	10	11	12	13	14

PMIC N/A

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DRAWING APPROVAL DATE
94-05-27

REVISION LEVEL
C

STANDARD MICROCIRCUIT DRAWING

THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE

AMSC N/A

DEFENSE SUPPLY CENTER COLUMBUS
POST OFFICE BOX 3990
COLUMBUS, OHIO 43216-5000
http://www.dscc.dla.mil

MICROCIRCUIT, HYBRID, LINEAR, 15 VOLT, DUAL CHANNEL, DC-DC CONVERTER

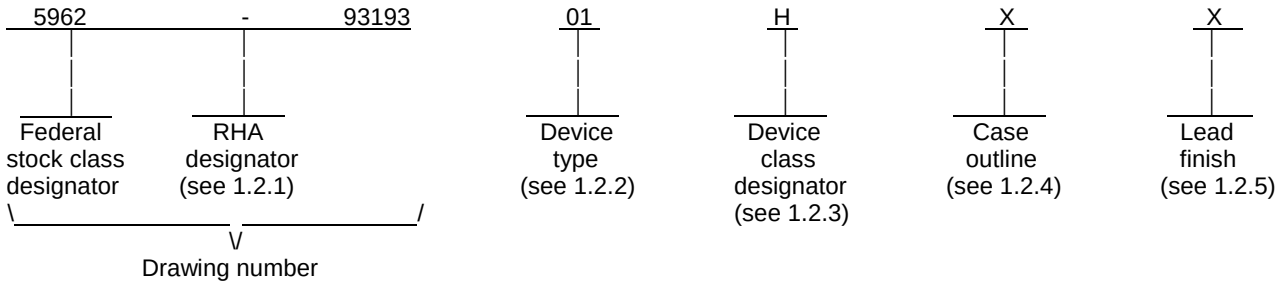
SIZE	CAGE CODE	
A	**67268**	**5962-93193**

SHEET 1 OF 17

1. SCOPE

1.1 Scope. This drawing documents five product assurance classes as defined in paragraph 1.2.3 and MIL-PRF-38534. A choice of case outlines and lead finishes which are available and are reflected in the Part or Identifying Number (PIN). When available, a choice of radiation hardness assurance levels are reflected in the PIN.

1.2 PIN. The PIN shall be as shown in the following example:



1.2.1 Radiation hardness assurance (RHA) designator. RHA marked devices shall meet the MIL-PRF-38534 specified RHA levels and shall be marked with the appropriate RHA designator. A dash (-) indicates a non-RHA device.

1.2.2 Device type(s). The device type(s) identify the circuit function as follows:

Device type	Generic number	Circuit function
01	MFL2815D	DC-DC converter, 65 W, ±15 V outputs
02	SMFL2815D	DC-DC converter, 65 W, ±15 V outputs

1.2.3 Device class designator. This device class designator shall be a single letter identifying the product assurance level. All levels are defined by the requirements of MIL-PRF-38534 and require QML Certification as well as qualification (Class H, K, and E) or QML Listing (Class G and D). The product assurance levels are as follows:

Device class	Device performance documentation
K	Highest reliability class available. This level is intended for use in space applications.
H	Standard military quality class level. This level is intended for use in applications where non-space high reliability devices are required.
G	Reduced testing version of the standard military quality class. This level uses the Class H screening and In-Process Inspections with a possible limited temperature range, manufacturer specified incoming flow, and the manufacturer guarantees (but may not test) periodic and conformance inspections (Group A, B, C and D).
E	Designates devices which are based upon one of the other classes (K, H, or G) with exception(s) taken to the requirements of that class. These exception(s) must be specified in the device acquisition document; therefore the acquisition document should be reviewed to ensure that the exception(s) taken will not adversely affect system performance.
D	Manufacturer specified quality class. Quality level is defined by the manufacturers internal, QML certified flow. This product may have a limited temperature range.

1.2.4 Case outline(s). The case outline(s) are as designated in MIL-STD-1835 and as follows:

Outline letter	Descriptive designator	Terminals	Package style
T	See figure 1	12	Tabbed flange mount, lead formed up
U	See figure 1	12	Flange mount, lead formed down
X	See figure 1	12	Flange mount, short lead
Y	See figure 1	12	Tabbed flange mount, short lead
Z	See figure 1	12	Tabbed flange mount, lead formed down

1.2.5 Lead finish. The lead finish shall be as specified in MIL-PRF-38534.

1.3 Absolute maximum ratings. 1/

Input voltage range (V_{IN})	-0.5 V dc to +50 V dc
Power dissipation (P_D):	
Device types 01and 02 (non-RHA)	16 W
Device type 02 (RHA level R)	18 W
Lead soldering temperature (10 seconds)	+300°C
Storage temperature range	-65°C to +150°C

1.4 Recommended operating conditions.

Input voltage range (V_{IN})	+16 V dc to +40 V dc
Output power	≤ 65 W
Case operating temperature range (T_C)	-55°C to +125°C

2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbooks. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those listed in the issue of the Department of Defense Index of Specifications and Standards (DoDISS) and supplement thereto, cited in the solicitation.

SPECIFICATION

DEPARTMENT OF DEFENSE

MIL-PRF-38534 - Hybrid Microcircuits, General Specification for.

STANDARDS

DEPARTMENT OF DEFENSE

MIL-STD-883 - Test Method Standard Microcircuits.
MIL-STD-1835 - Interface Standard for Electronic Component Case Outlines.

HANDBOOKS

1/ Stresses above the absolute maximum ratings may cause permanent damage to the device. Extended operation at the maximum levels may degrade performance and affect reliability.

DEPARTMENT OF DEFENSE

MIL-HDBK-103 - List of Standard Microcircuit Drawings.
MIL-HDBK-780 - Standard Microcircuit Drawings.

(Unless otherwise indicated, copies of the specification, standards, and handbooks are available from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 Item requirements. The individual item performance requirements for device classes D, E, G, H, and K shall be in accordance with MIL-PRF-38534. Compliance with MIL-PRF-38534 may include the performance of all tests herein or as designated in the device manufacturer's Quality Management (QM) plan or as designated for the applicable device class. Therefore, the tests and inspections herein may not be performed for the applicable device class (see MIL-PRF-38534). Furthermore, the manufacturer may take exceptions or use alternate methods to the tests and inspections herein and not perform them. However, the performance requirements as defined in MIL-PRF-38534 shall be met for the applicable device class.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38534 and herein.

3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.4 herein and figure 1.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 2.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full specified operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are defined in table I.

3.5 Marking of device(s). Marking of device(s) shall be in accordance with MIL-PRF-38534. The device shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's vendor similar PIN may also be marked in MIL-HDBK-103 and QML-38534.

3.6 Data. In addition to the general performance requirements of MIL-PRF-38534, the manufacturer of the device described herein shall maintain the electrical test data (variables format) from the initial quality conformance inspection group A lot sample, for each device type listed herein. Also, the data should include a summary of all parameters manually tested, and for those which, if any, are guaranteed. This data shall be maintained under document revision level control by the manufacturer and be made available to the preparing activity (DSCC-VA) upon request.

3.7 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to supply to this drawing. The certificate of compliance (original copy) submitted to DSCC-VA shall affirm that the manufacturer's product meets the performance requirements of MIL-PRF-38534 and herein.

3.8 Certificate of conformance. A certificate of conformance as required in MIL-PRF-38534 shall be provided with each lot of microcircuits delivered to this drawing.

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-PRF-38534 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions 1/ $-55°C \le T_C \le +125°C$ V_{IN} = 28 V dc ±0.5 V dc no external sync, C_L = 0 unless otherwise specified		Group A subgroups	Device types	Limits		Unit
						Min	Max	
Output voltage	V_{OUT}	I_{OUT} = ±2.17 A dc, (main)		1	01,02	+14.85	+15.15	V
				2,3		+14.55	+15.45	
			R	1,2,3	02	+14.10	+15.90	
		I_{OUT} = ±2.17 A dc, (dual)		1	01,02	-14.77	-15.23	
				2,3		-14.47	-15.53	
			R	1,2,3	02	-14.02	-15.90	
Output current 2/	I_{OUT}	V_{IN} = 16 V, 28 V, and 40 V dc, sum of both outputs		1,2,3	01,02	0.0	4.34	A
			R	1,2,3	02	0.0	4.34	
V_{OUT} ripple voltage	V_{RIP}	I_{OUT} = ±2.17 A, (main) B.W. = 10 kHz to 2 MHz		1	01,02		100	mVp-p
				2,3			150	
			R	1,2,3	02		175	
		I_{OUT} = ±2.17 A, (dual) B.W. = 10 kHz to 2 MHz		1	01,02		100	
				2,3			150	
			R	1,2,3	02		175	
V_{OUT} line regulation	VR_{LINE}	I_{OUT} = ±2.17 A, (main) V_{IN} =16 V dc to 40 V dc		1,2,3	01,02		50	mV
			R	1,2,3	02		100	
		I_{OUT} = ±2.17 A, (dual) V_{IN} =16 V dc to 40 V dc		1,2,3	01,02		100	
			R	1,2,3	02		150	

See footnotes at end of table.

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions 1/ $-55°C \le T_C \le +125°C$ V_{IN} = 28 V dc ±0.5 V dc no external sync, C_L = 0 unless otherwise specified		Group A subgroups	Device types	Limits		Unit
						Min	Max	
V_{OUT} load regulation	VR_{LOAD}	I_{OUT} = 0 to ±2.17 A, (main)		1,2,3	01,02		50	mV
			R	1,2,3	02		100	
		I_{OUT} = 0 to ±2.17 A, (dual)		1,2,3	01,02		150	
			R	1,2,3	02		250	
Input current	I_{IN}	I_{OUT} = 0 A, Inhibit 1 (pin 4) = 0		1,2,3	01,02		14	mA
			R	1,2,3	02		17	
		I_{OUT} = 0 A, Inhibit 2 (pin12) = 0		1,2,3	01,02		70	
			R	1,2,3	02		90	
		I_{OUT} = 0 A, Inhibit 1 (pin 4) and inhibit 2 (pin 12) = open		1,2,3	01,02		100	
			R	1,2,3	02		130	
I_{IN} ripple current	I_{RIP}	I_{OUT} = ±2.17 A, B.W. = 10 kHz to 10 MHz		1	01,02		45	mAp-p
				2,3			50	
			R	1,2,3	02		75	
Efficiency	Eff	I_{OUT} = ±2.17 A		1	01	84		%
					02	82		
				2,3	01	82		
					02	80		
			R	1,2,3	02	79		

See footnotes at end of table.

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions 1/ $-55°C \le T_C \le +125°C$ V_{IN} = 28 V dc ±0.5 V dc no external sync, C_L = 0 unless otherwise specified	Group A subgroups	Device types	Limits		Unit
					Min	Max	
Isolation	ISO	Input to output or any pin to case at 500 V dc, T_C = +25°C	1	01,02	100		MΩ
		R	1	02	100		
Capacitive load 3/ 4/ (each output)	C_L	No effect on dc performance, T_C = +25°C	4	01,02		500	µF
		R	4	02		500	
Power dissipation load fault	P_D	Short circuit	1	01,02		14	W
			2,3			16	
		R	1,2,3	02		18	
Switching frquency	F_S	I_{OUT} = ±2.17 A	4,5,6	01,02	525	675	kHz
		R	4,5,6	02	500	700	
External sync range 5/	F_{SYNC}	I_{OUT} = ±2.17 A, TTL level to pin 6	4,5,6	01,02	525	675	kHz
		R	4,5,6	02	525	675	
V_{OUT} step load transient 6/	V_{TLOAD}	50 percent load to/from 100 percent load	4,5,6	01,02	-600	+600	mV pk
		R	4,5,6	02	-900	+900	
V_{OUT} step load transient recovery 4/ 6/ 7/	TT_{LOAD}	50 percent load to/from 100 percent load	4,5,6	01,02		3.0	ms
		R	4,5,6	02		4.0	
V_{OUT} step line transient 4/ 8/	V_{TLINE}	I_{OUT} = ±2.17 A, Input step from 16 V dc to 40 V dc	4,5,6	01,02	-400	+400	mV pk
		R	4,5,6	02	-500	+500	
		I_{OUT} = ±2.17 A, Input step from 40 V dc to 16 V dc	4,5,6	01,02	-400	+400	
		R	4,5,6	02	-500	+500	

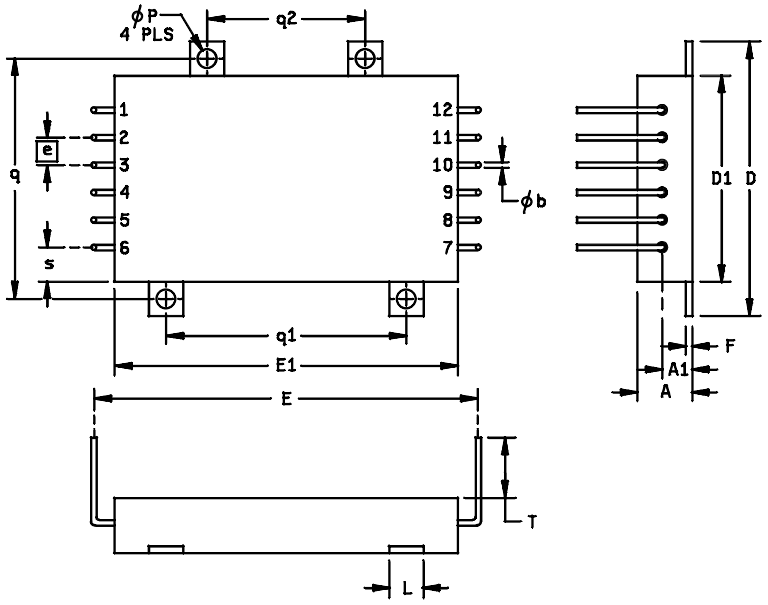
See footnotes at end of table.

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions 1/ $-55°C \le T_C \le +125°C$ V_{IN} = 28 V dc ±0.5 V dc no external sync, C_L = 0 unless otherwise specified		Group A subgroups	Device types	Limits		Unit
						Min	Max	
V_{OUT} step line transient recovery 4/ 7/	TT_{LINE}	I_{OUT} = ±2.17 A, Input step from 16 V dc to 40 V dc		4,5,6	01,02	-300	+300	µs
			R	4,5,6	02	-400	+400	
		I_{OUT} = ±2.17 A, Input step from 40 V dc to 16 V dc		4,5,6	01,02	-300	+300	
			R	4,5,6	02	-400	+400	
Start up overshoot 4/	$Vton_{OS}$	I_{OUT} = ±2.17 A, V_{IN} = 0 to 28 V dc		4,5,6	01,02		±50	mV pk
			R	4,5,6	02		±75	
Start up delay 9/	Ton_D	I_{OUT} = ±2.17 A, V_{IN} = 0 to 28 V dc		4,5,6	01,02		6	ms
			R	4,5,6	02		12	
Load fault recovery 4/	Tr_{LF}	I_{OUT} = ±2.17 A		4,5,6	01,02		4	ms
			R	4,5,6	02		8	

1/ Post irradiation testing shall be in accordance with 4.3.5 herein.
2/ The output power available from either output is limited to 45.5 watts (i.e. 70 percent of the total output power).
3/ Capacitive load may be any value from 0 to the maximum limit without compromising dc performance.
4/ Parameter shall be tested as part of design characterization and after design or process changes; therefore, the parameter shall be guaranteed to limits specified in table I.
5/ A TTL level waveform (V_{IH} = 4.5 V minimum, V_{IL} = 0.8 V maximum) with a 50 percent ±10 percent duty cycle applied to the sync input pin (pin 6) within the the sync range frequency shall cause the converter's switching frequency to become synchronous with the frequency applied to the sync input pin (pin 6).
6/ Load step transition time is 10 microseconds minimum.
7/ Recovery time is measured from the initiation of the transient until V_{OUT} has returned to within ±1 percent of its final value.
8/ Input step transition time greater than 10 microseconds.
9/ Turn-on delay time measurement is either for a step application of power at the input or the removal of a ground signal from the inhibit 1 pin (pin 4) or inhibit 2 pin (pin 12) while power is applied to the input.

Case outline T.

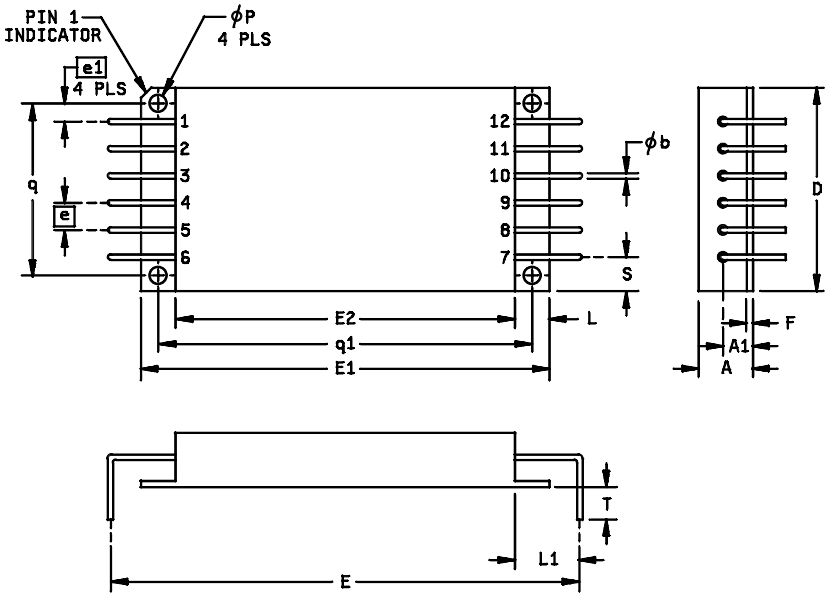


Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A		10.16		.400
A1	5.33	5.84	.210	.230
ϕb	0.89	1.14	.035	.045
D	50.55	51.05	1.990	2.010
D1	37.85	38.35	1.490	1.510
e	5.08 BSC		.200 BSC	
E	69.85	72.39	2.750	2.850
E1	63.25	63.75	2.490	2.510
F	1.14	1.40	.045	.055
L	6.10	6.60	.240	.260
ϕP	3.43	3.68	.135	.145
q/q1	44.20	44.70	1.740	1.760
q2	28.96	29.46	1.140	1.160
s	6.10	6.60	.240	.260
T	10.92	11.43	.430	.450

NOTES:
1. The U.S. government preferred system of measurement is the metric SI. This item was designed using inch-pound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
2. Device weight: 86 grams maximum.

FIGURE 1. Case outline(s).

Case outline U.

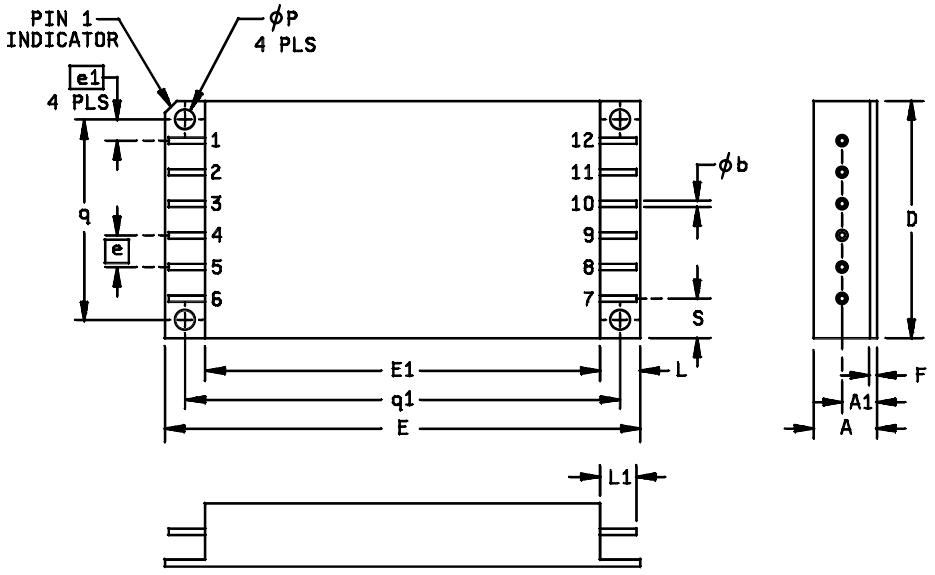


Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A		10.16		.400
A1	5.46	5.72	.215	.225
ϕb	0.89	1.14	.035	.045
D	37.97	38.23	1.495	1.505
e	5.08 BSC		.200 BSC	
e1	3.30 BSC		.130 BSC	
E	87.38	87.88	3.440	3.460
E1	75.95	76.45	2.990	3.010
E2	63.37	63.63	2.495	2.505
F	1.14	1.40	.045	.055
L	6.10	6.60	.240	.260
L1	11.94	12.19	.470	.480
ϕP	3.12	3.38	.123	.133
q	31.88	32.13	1.255	1.265
q1	69.85	70.36	2.750	2.770
S	6.22	6.48	.245	.255
T	5.84	6.86	.230	.270

NOTES:
1. The U.S. government preferred system of measurement is the metric SI. This item was designed using inch-pound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
2. Device weight: 86 grams maximum.

FIGURE 1. Case outline(s) - Continued.

Case outline X.

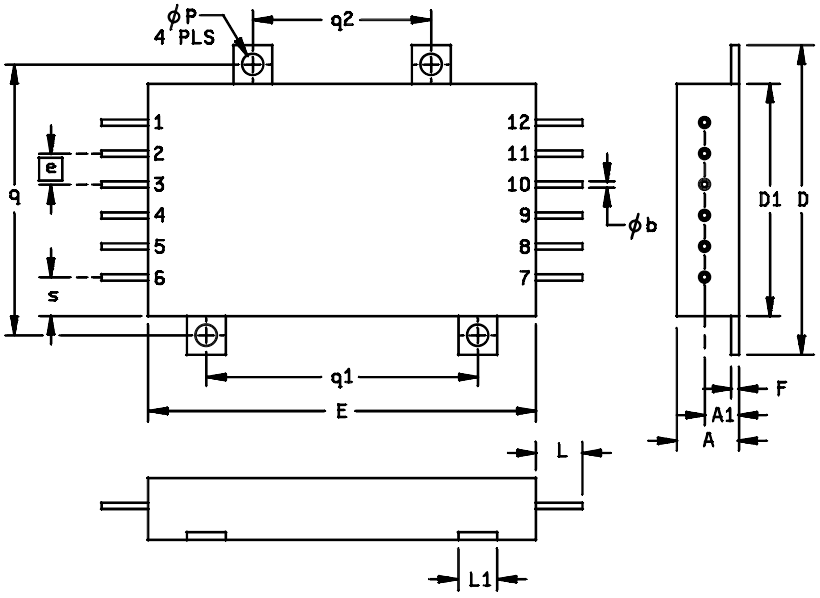


Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A		10.16		.400
A1	5.46	5.72	.215	.225
ϕb	0.89	1.14	.035	.045
D	37.97	38.23	1.495	1.505
e	5.08 BSC		.200 BSC	
e1	3.30 BSC		.130 BSC	
E	75.95	76.45	2.990	3.010
E1	63.37	63.63	2.495	2.505
F	1.14	1.40	.045	.055
L	6.10	6.60	.240	.260
L1	5.58	6.10	.220	.240
ϕP	3.12	3.38	.123	.133
q	31.88	32.13	1.255	1.265
q1	69.85	70.36	2.750	2.770
S	6.22	6.48	.245	.255

NOTES:
1. The U.S. government preferred system of measurement is the metric SI. This item was designed using inch-pound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
2. Device weight: 86 grams maximum.

FIGURE 1. Case outline(s) - Continued.

Case outline Y.

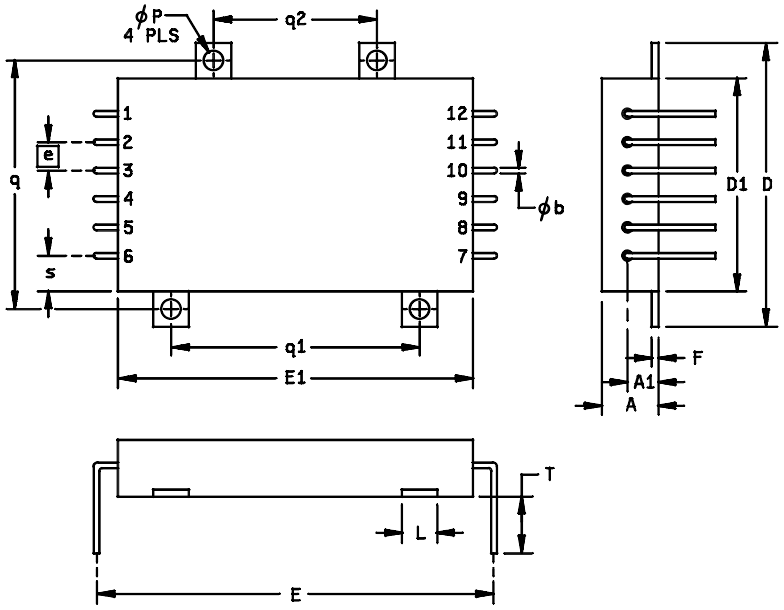


Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A		10.16		.400
A1	5.33	5.84	.210	.230
ϕb	0.89	1.14	.035	.045
D	50.55	51.05	1.990	2.010
D1	37.85	38.35	1.490	1.510
e	5.08 BSC		.200 BSC	
E	63.25	63.75	2.490	2.510
F	1.14	1.40	.045	.055
L	6.99	8.26	.275	.325
L1	6.10	6.60	.240	.260
ϕP	3.43	3.68	.135	.145
q/q1	44.20	44.70	1.740	1.760
q2	28.96	29.46	1.140	1.160
s	6.10	6.60	.240	.260

NOTES:
1. The U.S. government preferred system of measurement is the metric SI. This item was designed using inch-pound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
2. Device weight: 86 grams maximum.

FIGURE 1. Case outline(s) - Continued.

Case outline Z.



Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A		10.16		.400
A1	5.33	5.84	.210	.230
ϕb	0.89	1.14	.035	.045
D	50.55	51.05	1.990	2.010
D1	37.85	38.35	1.490	1.510
e	5.08 BSC		.200 BSC	
E	69.85	72.39	2.750	2.850
E1	63.25	63.75	2.490	2.510
F	1.14	1.40	.045	.055
L	6.10	6.60	.240	.260
ϕP	3.43	3.68	.135	.145
q/q1	44.20	44.70	1.740	1.760
q2	28.96	29.46	1.140	1.160
s	6.10	6.60	.240	.260
T	8.64	9.65	.340	.380

NOTES:
1. The U.S. government preferred system of measurement is the metric SI. This item was designed using inch-pound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
2. Device weight: 86 grams maximum.

FIGURE 1. Case outline(s) - Continued.

Device types	01 and 02
Case outlines	T, U, X, Y, and Z
Terminal number	Terminal symbol
1	Input
2	Input common
3	Tri
4	Inhibit 1
5	Sync output
6	Sync input
7	Positive output
8	Output common
9	Negative output
10	No connection
11	Slave to master
12	Master to slave/ Inhibit 2

NOTES:

1. Multiple devices may be used in parallel to drive a common load. When using this mode of operation the load current is shared by two or three devices. In the current sharing mode, one device is designated as the master. The slave to master pin (pin 11) of the master device is not connected and the master to slave/inhibit 2 pin (pin 12) of the master is connected to the slave to master pin (pin 11) of the slave device(s). The device(s) designated as slave(s) have the master to slave/inhibit 2 pin (pin 11) connected to the output common pin (pin 8).

2. A second slave device may be placed in parallel with a master and slave device, this requires the Tri pin (pin 3) of the master device to be connected to the output common pin (pin 8). When paralleled, 95 percent of the sum of the power of the devices is available at the load. This means that 185 watts at 15 volts is available for three devices in parallel.

3. The device has a sync input pin (pin 6) and a sync output pin (pin 5) which allows multiple devices, whether they're in a single unit or master/slave configurations to be synchronized to a system clock or each other. Two or more devices may be synchronized to each other by connecting the sync output pin (pin 5) of one to the sync input pin (pin 6) of another.

4. The device has two inhibit options, one is ground referenced to the input common and the other is referenced to the output common. The output referred inhibit pin uses the master to slave/inhibit 2 pin (pin 12). This pin is normally used to parallel devices, and a TTL compatiable open collector low will inhibit the device when applied to this pin.

FIGURE 2. Terminal connections.

TABLE II. Electrical test requirements.

MIL-PRF-38534 test requirements	Subgroups (in accordance with MIL-PRF-38534, group A test table)
Interim electrical parameters	- - -
Final electrical parameters	1*, 2, 3, 4, 5, 6
Group A test requirements	1, 2, 3, 4, 5, 6
Group C end-point electrical parameters	1
End-point electrical parameters for radiation hardness assurance (RHA) devices	1, 2, 3, 4, 5, 6

* PDA applies to subgroup 1.

4.2 Screening. Screening shall be in accordance with MIL-PRF-38534. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DSCC-VA or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.

(2) T_A as specified in accordance with table I of method 1015 of MIL-STD-883.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Conformance and periodic inspections. Conformance inspection (CI) and periodic inspection (PI) shall be in accordance with MIL-PRF-38534 and as specified herein.

4.3.1 Group A inspection (CI). Group A inspection shall be in accordance with MIL-PRF-38534 and as follows:

a. Tests shall be as specified in table II herein.

b. Subgroups 7, 8, 9, 10, and 11 shall be omitted.

4.3.2 Group B inspection (PI). Group B inspection shall be in accordance with MIL-PRF-38534.

4.3.3 Group C inspection (PI). Group C inspection shall be in accordance with MIL-PRF-38534 and as follows:

a. End-point electrical parameters shall be as specified in table II herein.

b. Steady-state life test, method 1005 of MIL-STD-883.

(1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DSCC-VA or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.

(2) T_A as specified in accordance with table I of method 1005 of MIL-STD-883.

(3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

4.3.4 Group D inspection (PI). Group D inspection shall be in accordance with MIL-PRF-38534.

4.3.5. Radiation hardness assurance (RHA). RHA qualification is required only for those devices with the RHA designator as specified herein.

	RHA level R	Units
Total ionizing dose tolerance level	100	kRad (Si)
Single event upset survival level (LET)	40	MeV

a. Radiation dose rate is in accordance with condition C of method 1019 of MIL-STD-883. Unless otherwise specified, components are tested at a rate of 9 rad(Si)/s, in accordance with method 1019 of MIL-STD-750 or MIL-STD-883, as applicable.

b. The manufacturer shall perform a worst-case and radiation susceptibility analysis on the device. This analysis shall show that the minimum performance requirements of each component has adequate design margin under worst-case operating conditions (extremes of line voltage, temperatures, load, frequency, radiation environment, etc.). This analysis guarantees the post-irradiation parameter limits specified in table I.

c. RHA testing shall be performed at the component level for initial device qualification, and after design changes that may affect the RHA performance of the device. As an alternative to testing, components may be procured to manufacturer radiation guarantees that meet the minimum performance requirements. Component radiation performance guarantees shall be established in compliance with MIL-PRF-19500, Group D or MIL-PRF-38535, Group E, as applicable. For components with less than adequate performance margin, component lot radiation acceptance screening shall be performed.

d. The manufacturer shall establish procedures controlling component radiation testing, and shall establish radiation test plans used to implement component lot qualification during procurement. Test plans and test reports shall be filed and controlled in accordance with the manufacturer's configuration management system.

e. The device manufacturer shall designate a RHA program manager to oversee component lot qualification, and to monitor design changes for continued compliance to RHA requirements.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38534.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-PRF-38534.

6.4 Record of users. Military and industrial users shall inform Defense Supply Center Columbus when a system application requires configuration control and the applicable SMD. DSCC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DSCC-VA, telephone (614) 692-0544.

6.5 Comments. Comments on this drawing should be directed to DSCC-VA, Post Office Box 3990, Columbus, Ohio 43216-5000, or telephone (614) 692-1081.

6.6 Sources of supply. Sources of supply are listed in MIL-HDBK-103 and QML-38534. The vendors listed in MIL-HDBK-103 and QML-38534 have submitted a certificate of compliance (see 3.7 herein) to DSCC-VA and have agreed to this drawing.

STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 02-06-21

Approved sources of supply for SMD 5962-93193 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38534 during the next revisions. MIL-HDBK-103 and QML-38534 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DSCC-VA. This bulletin is superseded by the next dated revisions of MIL-HDBK-103 and QML-38534.

Standard microcircuit drawing PIN 1/	Vendor CAGE number	Vendor similar PIN 2/
5962-9319301HTA	50821	MFL2815DW/883
5962-9319301HTC	50821	MFL2815DW/883
5962-9319302HTA	50821	SMFL2815DW/HO
5962-9319302HTC	50821	SMFL2815DW/HO
5962R9319302HTA	50821	SMFL2815DW/HR
5962R9319302HTC	50821	SMFL2815DW/HR
5962R9319302KTA	50821	SMFL2815DW/KR
5962R9319302KTC	50821	SMFL2815DW/KR
5962-9319301HUA	50821	MFL2815DV/883
5962-9319301HUC	50821	MFL2815DV/883
5962-9319302HUA	50821	SMFL2815DV/HO
5962-9319302HUC	50821	SMFL2815DV/HO
5962R9319302HUA	50821	SMFL2815DV/HR
5962R9319302HUC	50821	SMFL2815DV/HR
5962R9319302KUA	50821	SMFL2815DV/KR
5962R9319302KUC	50821	SMFL2815DV/KR
5962-9319301HXA	50821	MFL2815D/883
5962-9319301HXC	50821	MFL2815D/883
5962-9319302HXA	50821	SMFL2815D/HO
5962-9319302HXC	50821	SMFL2815D/HO
5962R9319302HXA	50821	SMFL2815D/HR
5962R9319302HXC	50821	SMFL2815D/HR
5962R9319302KXA	50821	SMFL2815D/KR
5962R9319302KXC	50821	SMFL2815D/KR
5962-9319301HYA	50821	MFL2815DY/883
5962-9319301HYC	50821	MFL2815DY/883
5962-9319302HYA	50821	SMFL2815DY/HO
5962-9319302HYC	50821	SMFL2815DY/HO
5962R9319302HYA	50821	SMFL2815DY/HR
5962R9319302HYC	50821	SMFL2815DY/HR
5962R9319302KYA	50821	SMFL2815DY/KR
5962R9319302KYC	50821	SMFL2815DY/KR
5962-9319301HZA	50821	MFL2815DZ/883
5962-9319301HZC	50821	MFL2815DZ/883
5962-9319302HZA	50821	SMFL2815DZ/HO
5962-9319302HZC	50821	SMFL2815DZ/HO
5962R9319302HZA	50821	SMFL2815DZ/HR
5962R9319302HZC	50821	SMFL2815DZ/HR
5962R9319302KZA	50821	SMFL2815DZ/KR
5962R9319302KZC	50821	SMFL2815DZ/KR

1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the Vendor to determine its availability.

2/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number	Vendor name and address
50821	Interpoint Corporation 10301 Willows Road Redmond, WA 98073-9705

The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in the information bulletin.