January 2001

FAIRCHILD SEMICONDUCTOR

Sig424DY Single P-Channel 2.5V Specified PowerTrench MOSFET

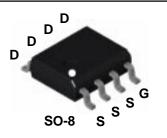
General Description

This P-Channel 2.5V specified MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench process that has been especially tailored to minimize on-state resistance and yet maintain superior switching performance.

These devices are well suited for low voltage and battery powered applications where low in-line power loss and fast switching are required.

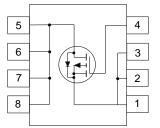
Applications

- DC/DC converter
- Load switch
- Battery Protection



Features

- -8.0 A, -20 V. $R_{DS(on)} = 0.024 \ \Omega \ @ V_{GS} = -4.5 \ V$ $R_{DS(on)} = 0.032 \ \Omega \ @ V_{GS} = -2.5 \ V.$
- Low gate charge (23nC typical).
- Fast switching speed.
- High performance trench technology for extremely low R_{DS(ON)}.
- High power and current handling capability.



Absolute Maximum Ratings T_A = 25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V _{DSS}	Drain-Source Voltage		-20	V
/ _{GSS}	Gate-Source Voltage		± 10	V
D	Drain Current - Continuous	(Note 1a)	-8.0	А
	- Pulsed		-50	
PD	Power Dissipation for Single Operation	(Note 1a)	2.5	W
		(Note 1b)	1.2	
		(Note 1c)	1	
TJ, Tstg	Operating and Storage Junction Temperature Range		-55 to +150	°C

Thermal Characteristics

R _{θJC} Thermal Resistance, Junction-to-Case (Note 1) 25 °C/W	R _{θJA}	Thermal Resistance, Junction-to-Ambient	(Note 1a)	50	∘C/W
	R _{θJC}	Thermal Resistance, Junction-to-Case	(Note 1)		∘C/W

Package Outlines and Ordering Information

Device Marking	Device	Reel Size	Tape Width	Quantity
9424	Si9424DY	13"	12mm	2500 units

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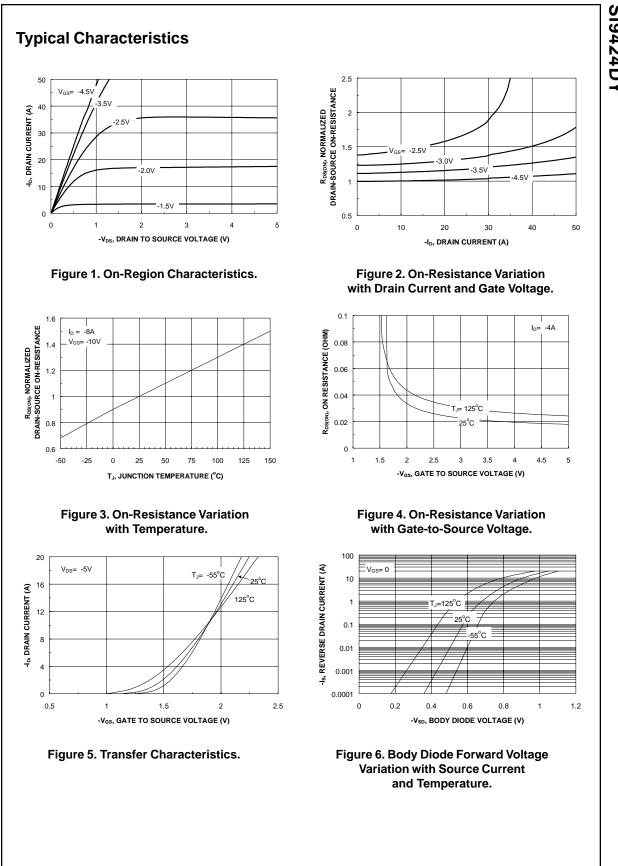
Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Char	acteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 V, I_D = -250 \mu A$	-20			V
ABVDSS ∆TJ	Breakdown Voltage Temperature Coefficient	$I_D = -250 \ \mu\text{A}$, Referenced to 25°C		-24		mV/∘C
DSS	Zero Gate Voltage Drain Current	$V_{DS} = -16 V, V_{GS} = 0 V$			-1	μA
GSSF	Gate-Body Leakage Current, Forward	$V_{GS}=10~V,~V_{DS}=0~V$			100	nA
GSSR	Gate-Body Leakage Current, Reverse	$V_{GS} = -10 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA
)n Char	acteristics (Note 2)					
GS(th)	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-0.4	-0.8	-1.5	V
VGS(th) ATJ	Gate Threshold Voltage Temperature Coefficient	I_D = -250 μ A, Referenced to 25°C		5		mV/∘C
R _{DS(on)}	Static Drain-Source On-Resistance	$ \begin{array}{l} V_{GS}=-4.5 \ V, \ I_{D}=-8 \ A \\ V_{GS}=-4.5 \ V, \ I_{D}=-8 \ A, \ T_{J}{=}125^{\circ}C \\ V_{GS}=-2.5 \ V, \ I_{D}=-7 \ A \end{array} $		0.019 0.026 0.027	0.024 0.039 0.032	Ω
D(on)	On-State Drain Current	V_{GS} = -4.5 V, V_{DS} = -5.0 V	-50			А
FS	Forward Transconductance	$V_{DS} = -5 V, I_D = -8 A$		28		s
Ovnamio	c Characteristics					
iss	Input Capacitance	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V},$		2260		pF
oss	Output Capacitance	f = 1.0 MHz		500		pF
Srss	Reverse Transfer Capacitance			205		pF
Switchir	ng Characteristics (Note 2)					
d(on)	Turn-On Delay Time	$V_{DD} = -10 V, I_D = -1 A,$		8	16	ns
	Turn-On Rise Time	V_{GS} = -4.5 V, R_{GEN} = 6 Ω		15	27	ns
d(off)	Turn-Off Delay Time			98	135	ns
	Turn-Off Fall Time			35	55	ns
ک ^و	Total Gate Charge	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -8 \text{ A},$		23	33	nC
) _{gs}	Gate-Source Charge	$V_{GS} = -5 V,$		5.5		nC
⊋ _{gd}	Gate-Drain Charge			4		nC
)rain-Sc	ource Diode Characteristics an	d Maximum Ratings				
	Maximum Continuous Drain-Source Die	-			-2.1	А
3						

Scale 1 : 1 on letter size paper

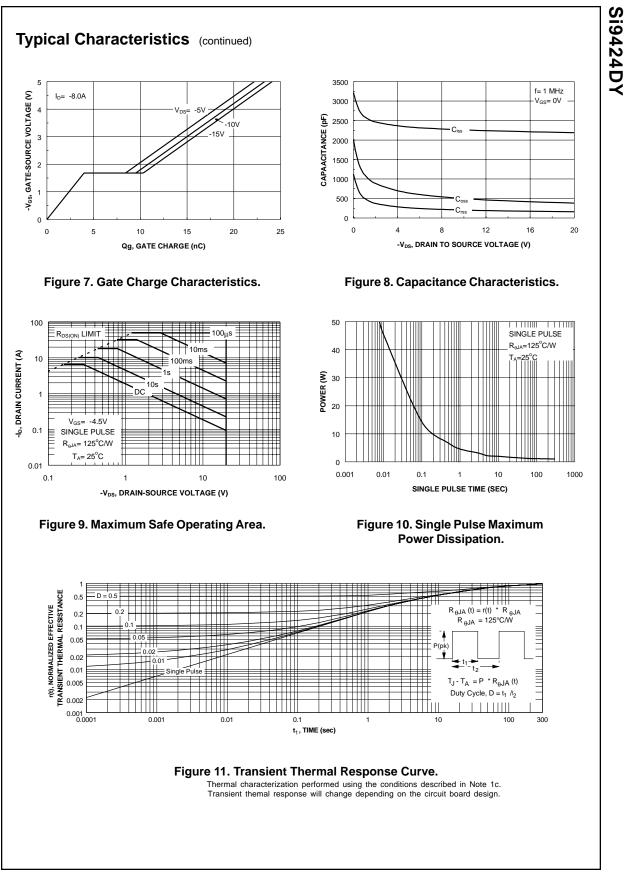
2: Pulse Test: Pulse Width $\leq 300~\mu s,$ Duty Cycle $\leq 2.0\%$

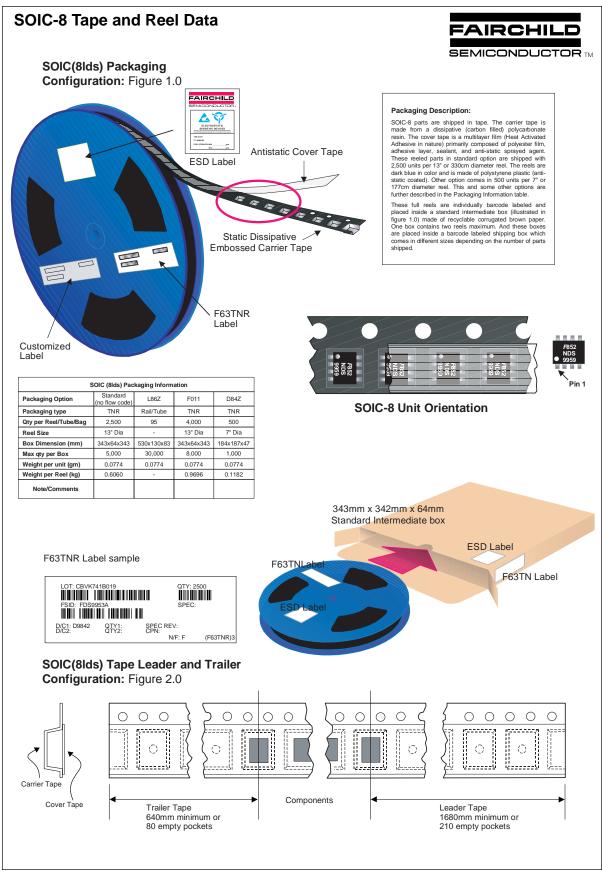
Si9424DY

Si9424DY Rev.A



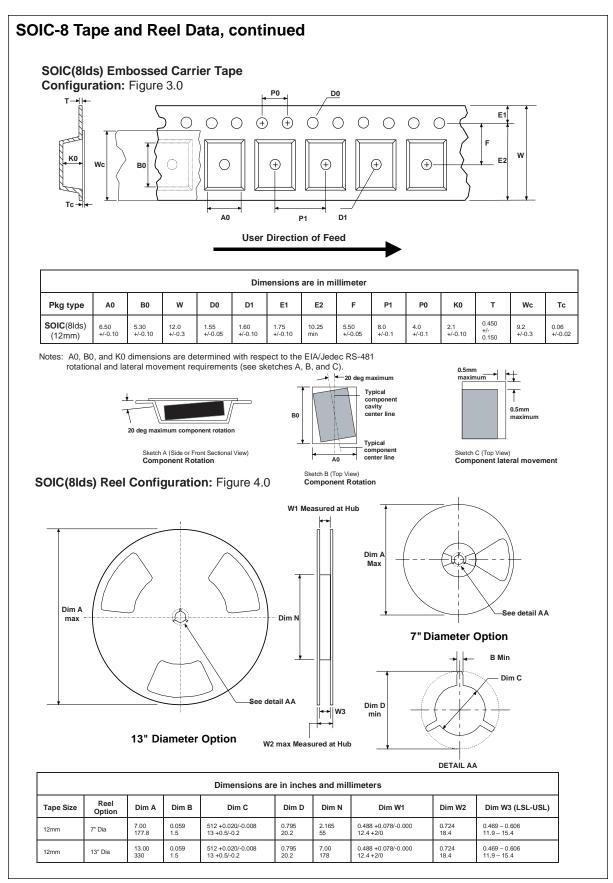
Si9424DY

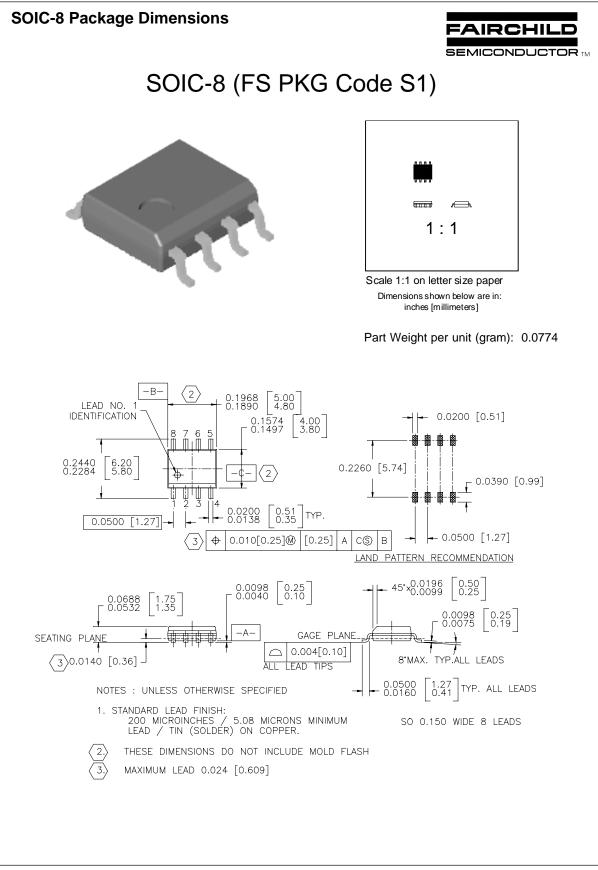




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