



PRELIMINARY

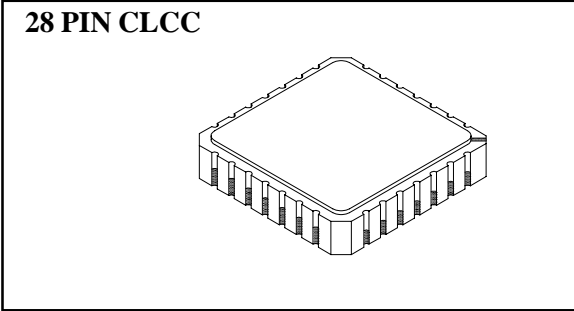
# SOLID STATE DEVICES, INC.

14005 Stage Road \* Santa Fe Springs, Ca 90670  
Phone: (562) 404-4474 \* Fax: (562) 404-1773

## DESIGNER'S DATA SHEET

# SFF120-28Q

**9.2 AMPS  
100 VOLTS  
0.35Ω  
QUAD N-CHANNEL  
POWER MOSFET**



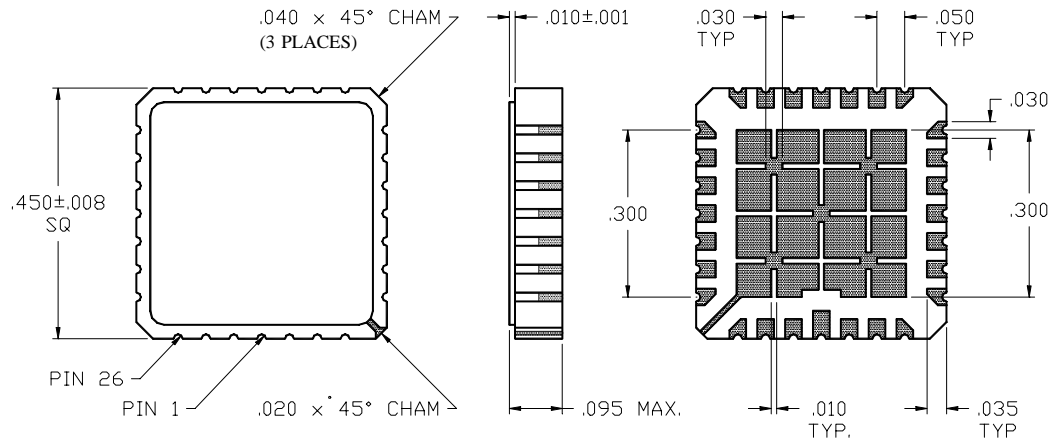
- FEATURES:**
- Rugged construction with poly silicon gate
  - Low RDS (on) and high transconductance
  - Excellent high temperature stability
  - Very fast switching speed
  - Fast recovery and superior dv/dt performance
  - Increased reverse energy capability
  - Low input and transfer capacitance for easy paralleling
  - Hermetically sealed surface mount package
  - TX, TXV and Space Level screening available
  - Replaces 4x IRF120 Types in One Package

## MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	VALUE	UNIT
Drain to Source Voltage	V <sub>DS</sub>	100	Volts
Gate to Source Voltage	V <sub>GS</sub>	±20	Volts
Continuous Drain Current	I <sub>D</sub>	9.2	Amps
Operating and Storage Temperature	T <sub>op</sub> & T <sub>stg</sub>	-55 to +150	°C
Thermal Resistance, Junction to Case (All Four)	R <sub>θJC</sub>	10	°C/W
Total Device Dissipation	@ TC = 25°C	12.5	Watts
	@ TC = 70°C	9.5	

## PACKAGE OUTLINE: 28

- PIN OUT:**
- MOSFET 1**  
DRAIN: 5, 6, 7  
GATE: 1  
SOURCE: 2, 3, 4
- MOSFET 2**  
DRAIN: 9, 10, 11  
GATE: 8  
SOURCE: 12, 13, 14
- MOSFET 3**  
DRAIN: 19, 20, 21  
GATE: 15  
SOURCE: 16, 17, 18
- MOSFET 4**  
DRAIN: 23, 24, 25  
GATE: 22  
SOURCE: 26, 27, 28



**NOTE:** All drain/source pins must be connected on the PC board in order to maximize current carrying capability and to minimize RDS (on)

NOTE: All specifications are subject to change without notification. SCDs for these devices should be reviewed by SSDI prior to release.

**DATA SHEET #: F00225B**

# SFF120-28Q

PRELIMINARY



**SOLID STATE DEVICES, INC.**

14005 Stage Road \* Santa Fe Springs, Ca 90670  
Phone: (562) 404-4474 \* Fax: (562) 404-1773

## ELECTRICAL CHARACTERISTICS @ T<sub>J</sub> = 25°C (Unless Otherwise Specified)

RATING		SYMBOL	MIN	TYP	MAX	UNIT
<b>Drain to Source Breakdown Voltage</b> (V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250μA)		<b>BV<sub>DSS</sub></b>	100	-	-	<b>V</b>
<b>Drain to Source ON State Resistance</b> (V <sub>GS</sub> = 10 V, 60% of Rated ID)		<b>R<sub>DS(on)</sub></b>	-	-	0.35	<b>Ω</b>
<b>ON State Drain Current</b> (V <sub>DS</sub> > I <sub>D(on)</sub> x R <sub>DS(on)</sub> Max, V <sub>GS</sub> = 10 V)		<b>I<sub>D(on)</sub></b>	9.2	-	-	<b>A</b>
<b>Gate Threshold Voltage</b> (V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA)		<b>V<sub>GS(th)</sub></b>	2.0	-	4.0	<b>V</b>
<b>Forward Transconductance</b> (V <sub>DS</sub> > I <sub>D(on)</sub> x R <sub>DS(on)</sub> Max, I <sub>DS</sub> = 60% rated ID)		<b>g<sub>fS</sub></b>	2.7	4.1	-	<b>S(Ω)</b>
<b>Zero Gate Voltage Drain Current</b> (V <sub>DS</sub> = max rated Voltage, V <sub>GS</sub> = 0V) (V <sub>DS</sub> = 80% rated V <sub>DS</sub> , V <sub>GS</sub> = 0V, T <sub>A</sub> = 125°C)		<b>I<sub>DSS</sub></b>	-	-	25 250	<b>μA</b>
<b>Gate to Source Leakage Forward</b> <b>Gate to Source Leakage Reverse</b>	At rated V <sub>GS</sub>	<b>I<sub>GSS</sub></b>	-	-	+100 -100	<b>nA</b>
<b>Total Gate Charge</b> <b>Gate to Source Charge</b> <b>Gate to Drain Charge</b>	V <sub>GS</sub> = 10 V 80% rated V <sub>DS</sub> 60% rated ID	<b>Q<sub>g</sub></b> <b>Q<sub>gs</sub></b> <b>Q<sub>gd</sub></b>	-	10.7 2.9 5.1	16 4.4 7.7	<b>nC</b>
<b>Turn on Delay Time</b> <b>Rise Time</b> <b>Turn off DELAY Time</b> <b>Fall Time</b>	V <sub>DD</sub> = 50% rated V <sub>DS</sub> 50% rated ID R <sub>G</sub> = 18 Ω	<b>t<sub>d(on)</sub></b> <b>t<sub>r</sub></b> <b>t<sub>d(off)</sub></b> <b>t<sub>f</sub></b>	-	13 30 19 20	20 45 29 30	<b>nsec</b>
<b>Diode Forward Voltage</b> (I <sub>S</sub> = rated I <sub>D</sub> , V <sub>GS</sub> = 0V, T <sub>J</sub> = 25°C)		<b>V<sub>SD</sub></b>	-	-	2.5	<b>V</b>
<b>Diode Reverse Recovery Time</b> <b>Reverse Recovery Charge</b>	T <sub>J</sub> = 25°C I <sub>F</sub> = rated ID di/dt = 100A/μsec	<b>t<sub>rr</sub></b> <b>Q<sub>RR</sub></b>	55 0.25	140 0.65	260 1.3	<b>nsec</b> <b>μC</b>
<b>Input Capacitance</b> <b>Output Capacitance</b> <b>Reverse Transfer Capacitance</b>	V <sub>GS</sub> = 0 Volts V <sub>DS</sub> = 25 Volts f = 1 MHz	<b>C<sub>iss</sub></b> <b>C<sub>oss</sub></b> <b>C<sub>rss</sub></b>	-	350 130 36	- - -	<b>pF</b>

For thermal derating curves and other characteristic curves please contact SSDI Marketing Department.

NOTES: