PART NUMBER: VESD2-SIP **DESCRIPTION:** dc-dc converter

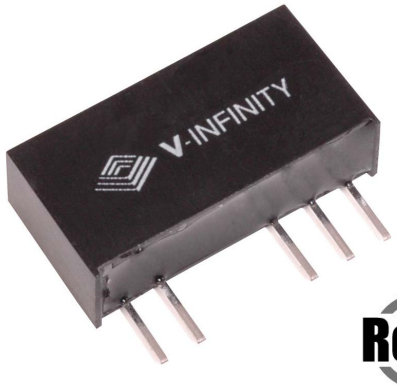
description

Designed to convert fixed voltages into an isolated voltage, the VESD2-SIP series is well suited for providing board-mount local supplies in a wide range of applications, including mixed analog/digital circuits, test & measurement equip., process/machine controls, datacom/telecom fields, etc...

The semi-regulated output can be followed by 3-terminal regulators to provide output protection, in addition to output regulation.

features

- **Isolated 2 W output**
- **Temperature range: -40°C~+85°C**
- **Unregulated**
- **High efficiency to 82%**
- **Dual voltage output**
- **Small footprint**
- **SIP package style**
- **Industry standard pinout**
- **UL94-V0 package**
- **No heatsink required**
- **3K Vdc isolation**
- **Power density 1.42 W/cm³**
- **No external component required**
- **Low cost**



RoHS

model number	input voltage nominal	input voltage range	output voltage	output current max.	output current min.	efficiency	package style
VESD2-S5-D5-SIP	5 Vdc	4.5~5.5 Vdc	±5 Vdc	±200 mA	±20 mA	82%	SIP
VESD2-S5-D9-SIP	5 Vdc	4.5~5.5 Vdc	±9 Vdc	±111 mA	±12 mA	83%	SIP
VESD2-S5-D12-SIP	5 Vdc	4.5~5.5 Vdc	±12 Vdc	±83 mA	±9 mA	85%	SIP
VESD2-S5-D15-SIP	5 Vdc	4.5~5.5 Vdc	±15 Vdc	±67 mA	±7 mA	85%	SIP
VESD2-S12-D5-SIP	12 Vdc	10.8~13.2 Vdc	±5 Vdc	±200 mA	±20 mA	83%	SIP
VESD2-S12-D9-SIP	12 Vdc	10.8~13.2 Vdc	±9 Vdc	±111 mA	±12 mA	84%	SIP
VESD2-S12-D12-SIP	12 Vdc	10.8~13.2 Vdc	±12 Vdc	±83 mA	±9 mA	86%	SIP
VESD2-S12-D15-SIP	12 Vdc	10.8~13.2 Vdc	±15 Vdc	±67 mA	±7 mA	86%	SIP
VESD2-S24-D5-SIP	24 Vdc	21.6~26.4 Vdc	±5 Vdc	±200 mA	±20 mA	84%	SIP
VESD2-S24-D9-SIP	24 Vdc	21.6~26.4 Vdc	±9 Vdc	±111 mA	±12 mA	85%	SIP
VESD2-S24-D12-SIP	24 Vdc	21.6~26.4 Vdc	±12 Vdc	±83 mA	±9 mA	87%	SIP
VESD2-S24-D15-SIP	24 Vdc	21.6~26.4 Vdc	±15 Vdc	±67 mA	±7 mA	87%	SIP

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OUTPUT SPECIFICATIONS

item	test conditions	min.	typ.	max.	units
output power		0.2		1	W
line regulation	for Vin change of 1%			1.2	%
load regulation	10% to 100% full load		10	15	%
output voltage accuracy	see tolerance envelope graph				
temperature drift	@ 100% load			0.03	%/°C
output ripple/noise	20 MHz bandwidth		50	75	mVp-p
switching frequency	full load, nominal input (5 V/12 V)		75		KHz

GENERAL SPECIFICATIONS

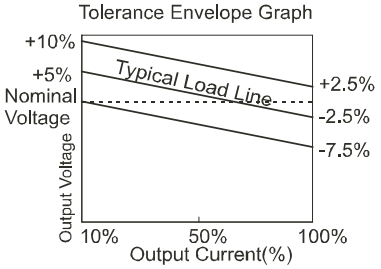
short circuit protection	<1 second
temperature rise at full load	25°C Max, 15°C typ.
cooling	free air convection
operating temperature range	-40°C to +85°C
storage temperature range	-55°C to +125°C
soldering temperature	300°C (1.5mm from case for 10 sec.)
storage humidity range	<95%
case material	plastic (UL94-V0)
MTBF	>3,500,000 hrs.
burn-in	full load at +85°C, for 4 hours at no-load and 4 hours at full load.

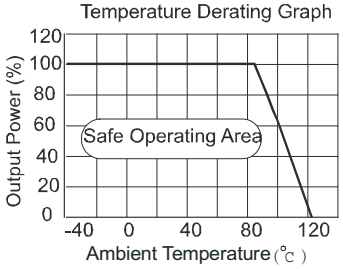
ISOLATION SPECIFICATIONS

item	test conditions	min.	typ.	max	units
isolation voltage	tested for 1 min.	3,000			V dc
insulation resistance	test at 500 Vdc	1,000			MΩ

NOTE: 1. All specifications measured at TA=25°C, humidity <75%, nominal input voltage and rated output load unless otherwise specified.

TYPICAL CHARACTERISTICS

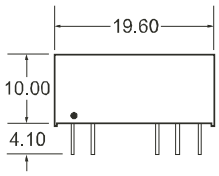




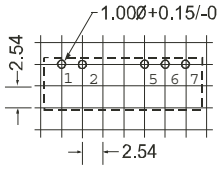
PART NUMBER: VESD2-SIP **DESCRIPTION:** dc-dc converter

DIMENSIONS (mm)

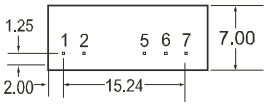
Side View



Layout



Bottom View



Pin	Function
1	+Vin
2	-Vin
5	-Vout
6	COM
7	+Vout

Note: All Pins on a 2.54mm pitch; all pin diameters are 0.50mm; all dimensions in mm.

PART NUMBER: VESD2-SIP

DESCRIPTION: dc-dc converter

APPLICATION NOTES:

- Input filtering

To reduce the reflected ripple current and minimize EMI, especially when the converter input is more than 2" away from the DC source, it is recommended to connect a low ESR electrolytic capacitor between Vin and Gnd. The values suggested are as shown in Table 1. If additional filtering is required, the capacitance may be increased, or expanded to an LC network as shown in Figure 1.

TABLE 1

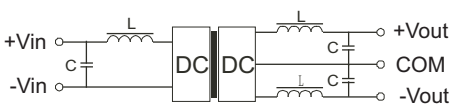
Input Voltage	External Input Capacitance
5 V	4.7 µF
12 V	2.2 µF
24 V	1.0 µF

- Output filtering

An output capacitor is needed to meet output ripple requirements as shown in Table 2. Output capacitance may be increased for additional filtering, but should not exeed 10 µF or expanded to an LC network as in Figure 1.

TABLE 2

Vout	External Ouput Capacitance
5 V	4.7 µF
9 V	2.2 µF
12 V	1.0 µF
15 V	0.47 µF



<Figure 1>

- Minimum loading

During operation, the minimum output load should not be less than 10% of the full load, and this product should never be operated under no load. If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load.

- Input Regulation

This converter accepts a narrow input range. With a semi-regulated design, the converter's output voltage will also change proportionally with the input voltage. To accept a wider input range and to improve line regulation, an input regulator can be added as shown in Fig. 2

- Protection

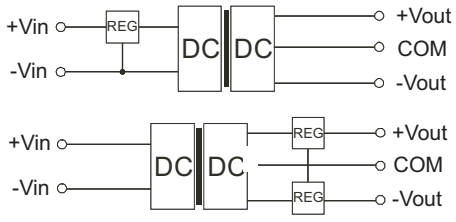
The converter has minimal protection against input over-voltage or output over-load, and may be permanently damaged if exposed to these conditions. An input clamping device can be used for input voltage limiting. An input fuse or an output fuse also be used to protect against over-loading.

- Dual outputs used as a single output

The +Vout and -Vout can be used to obtain a single output that is the sum of the two outputs. In this case, the COM pin shouldn't be used.

- Output Regulation

An external 3-terminal regulator can be connected to the output of the converter to achieve full regulation. Make sure the converter's output voltage provides sufficient head room for the regulator. An additional benefit is that the built-in protection features in the regulator, such as OCP, OTP, etc, will protect the converter also. In a complimentory supply, a negative output regulator must be used to achieve the negative regulated output.



<Figure 2>