

CPH5617 — N-Channel Silicon MOSFET General-Purpose Switching Device Applications

Features

- Low ON-resistance
- Ultrahigh-speed switching
- 1.5V drive
- Composite type with 2 MOSFETs contained in the one package, improving the mounting efficiency greatly

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		30	V
Gate-to-Source Voltage	V _{GSS}		±10	V
Drain Current (DC)	I _D		150	mA
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycles≤1%	600	mA
Allowable Power Dissipation	P _D		0.25	W
Channel Temperature	T _{ch}		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

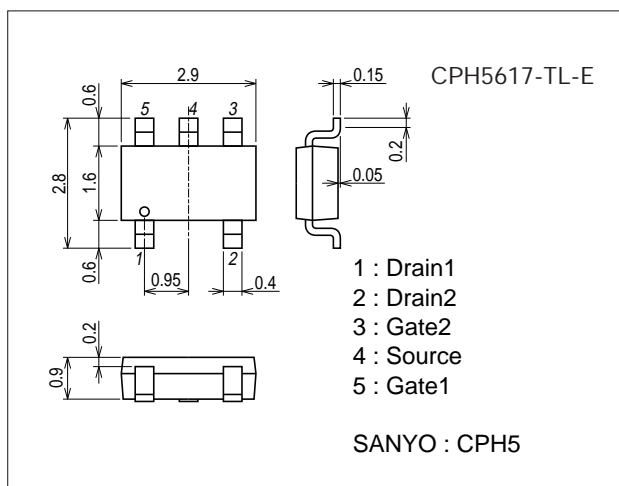
This product is designed to "ESD immunity < 200V**", so please take care when handling.

* Machine Model

Package Dimensions

unit : mm (typ)

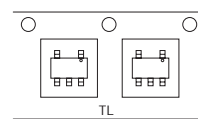
7017A-004



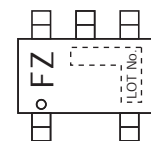
Product & Package Information

- Package : CPH5
- JEITA, JEDEC : SC-74A, SOT-25
- Minimum Packing Quantity : 3,000 pcs./reel

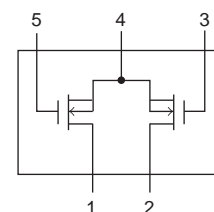
Packing Type : TL



Marking



Electrical Connection

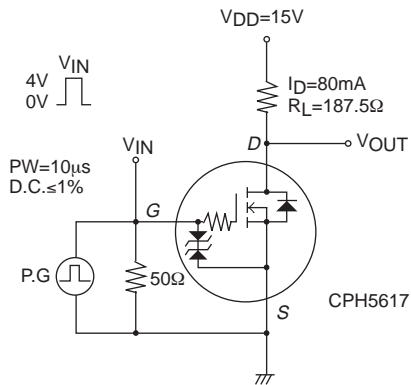


CPH5617

Electrical Characteristics at Ta=25°C

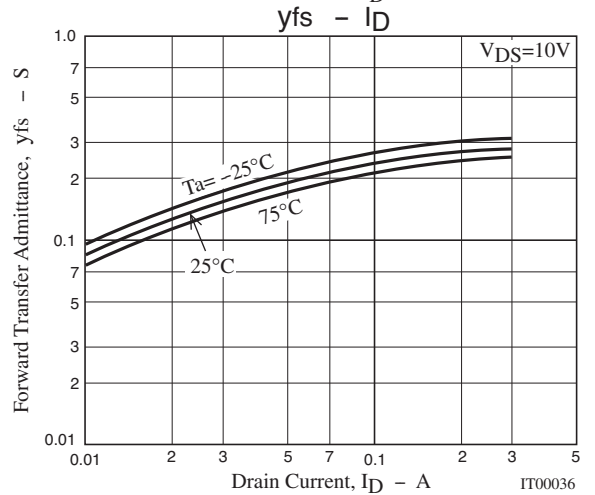
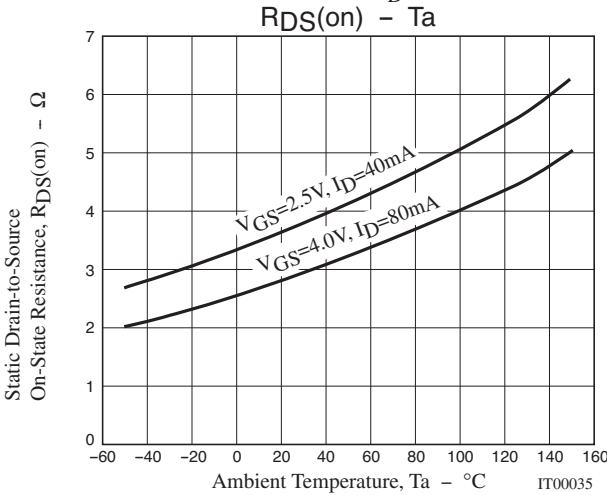
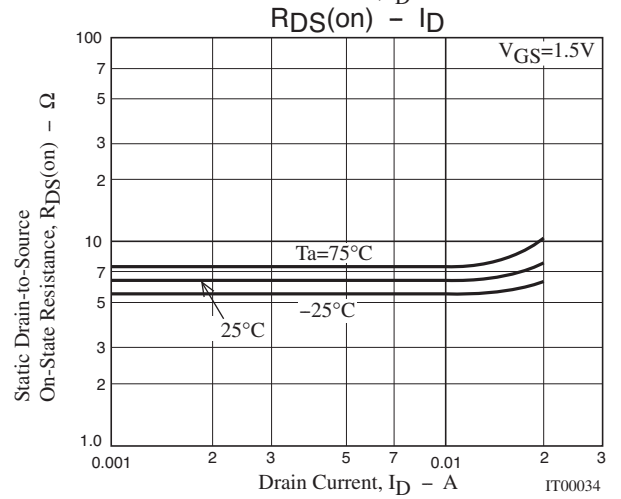
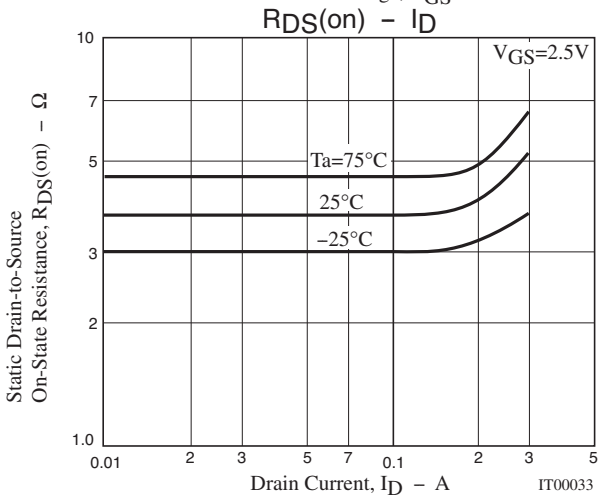
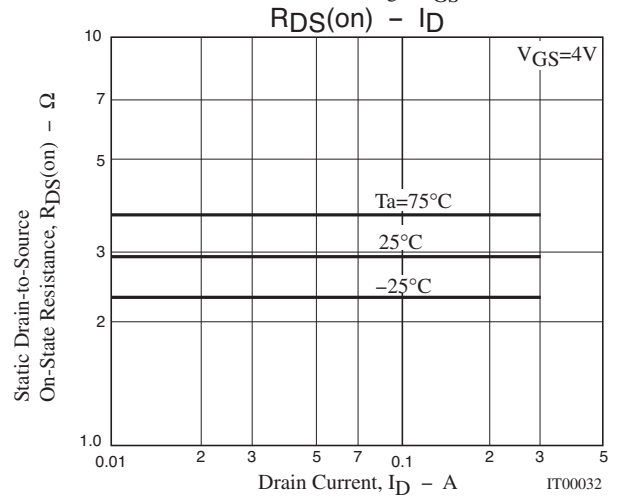
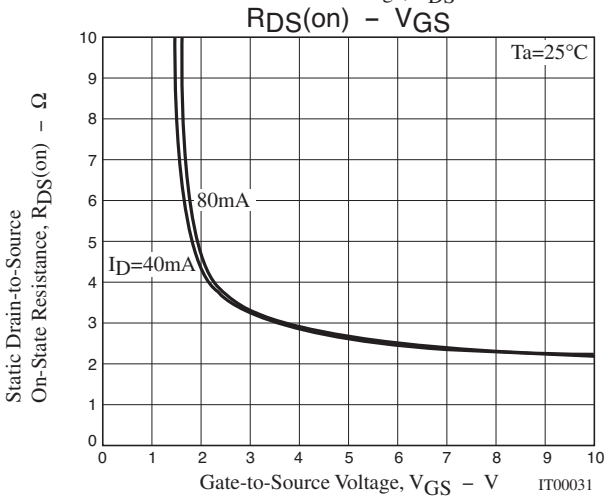
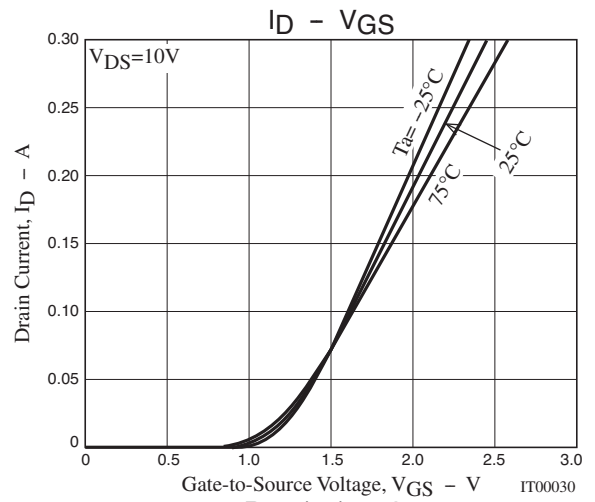
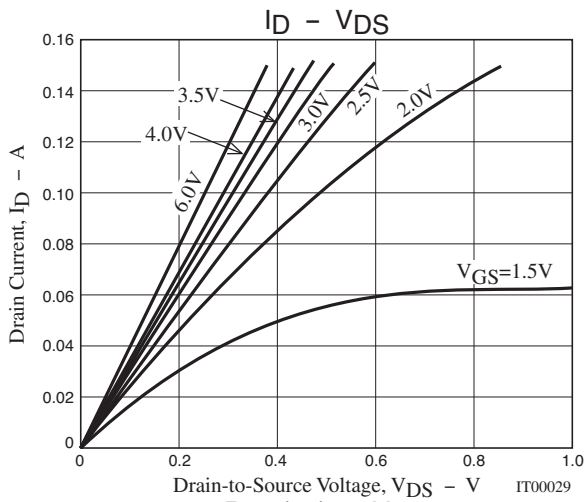
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =1mA, V _{GS} =0V	30			V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V			10	μA
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} =±8V, V _{DS} =0V			±10	μA
Cutoff Voltage	V _{GS(off)}	V _{DS} =10V, I _D =100μA	0.4		1.3	V
Forward Transfer Admittance	y _{fs}	V _{DS} =10V, I _D =80mA	0.15	0.22		S
Static Drain-to-Source On-State Resistance	R _{DS(on)1}	I _D =80mA, V _{GS} =4V		2.9	3.7	Ω
	R _{DS(on)2}	I _D =40mA, V _{GS} =2.5V		3.7	5.2	Ω
	R _{DS(on)3}	I _D =10mA, V _{GS} =1.5V		6.4	12.8	Ω
Input Capacitance	C _{iss}	V _{DS} =10V, f=1MHz		7.0		pF
Output Capacitance	C _{oss}			5.9		pF
Reverse Transfer Capacitance	C _{rss}			2.3		pF
Turn-ON Delay Time	t _{d(on)}		See specified Test Circuit.		19	
Rise Time	t _r			65		ns
Turn-OFF Delay Time	t _{d(off)}			155		ns
Fall Time	t _f			120		ns
Total Gate Charge	Q _g	V _{DS} =10V, V _{GS} =10V, I _D =150mA			1.58	
Gate-to-Source Charge	Q _{gs}			0.26		nC
Gate-to-Drain "Miller" Charge	Q _{gd}			0.31		nC
Diode Forward Voltage	V _{SD}	I _S =150mA, V _{GS} =0V		0.87	1.2	V

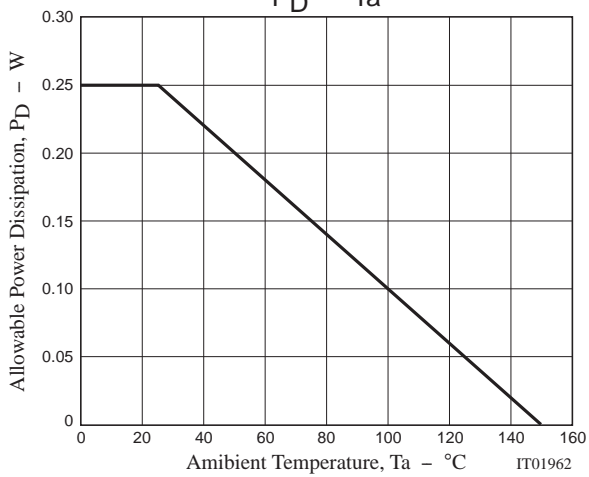
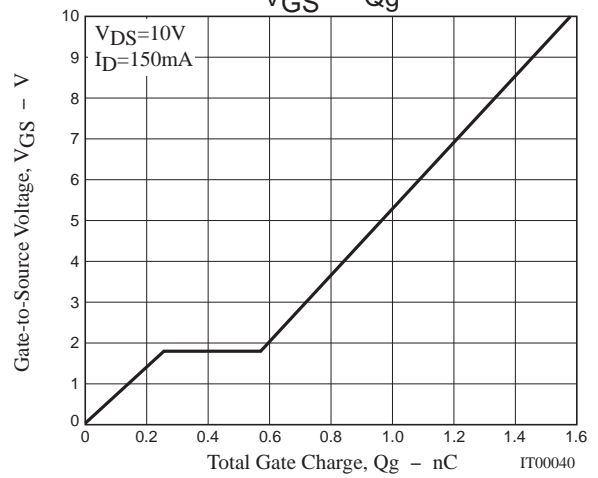
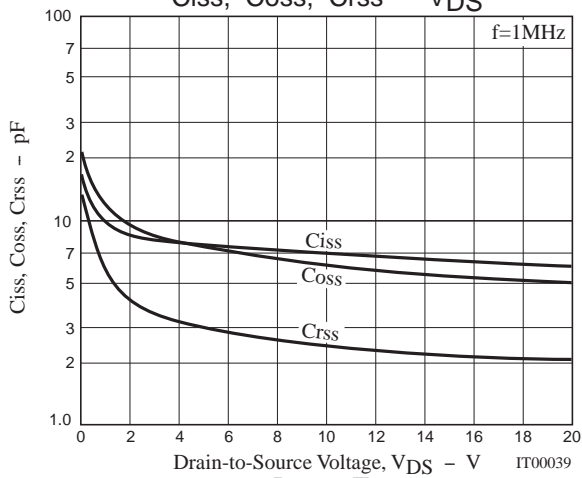
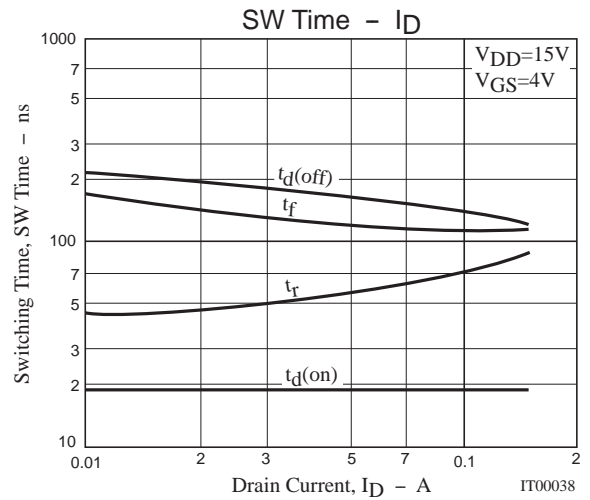
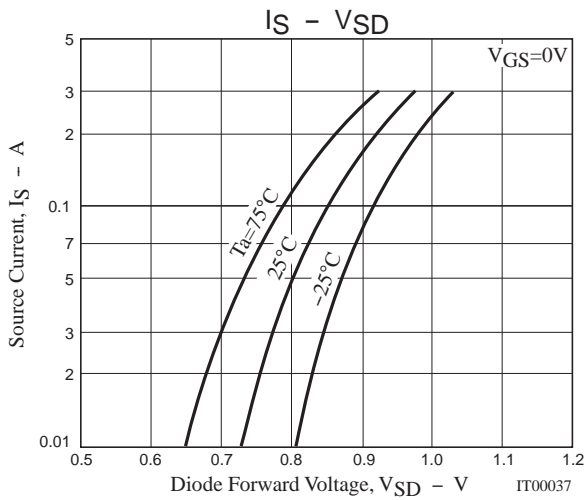
Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
CPH5617-TL-E	CPH5	3,000pcs./reel	Pb Free





Embossed Taping Specification

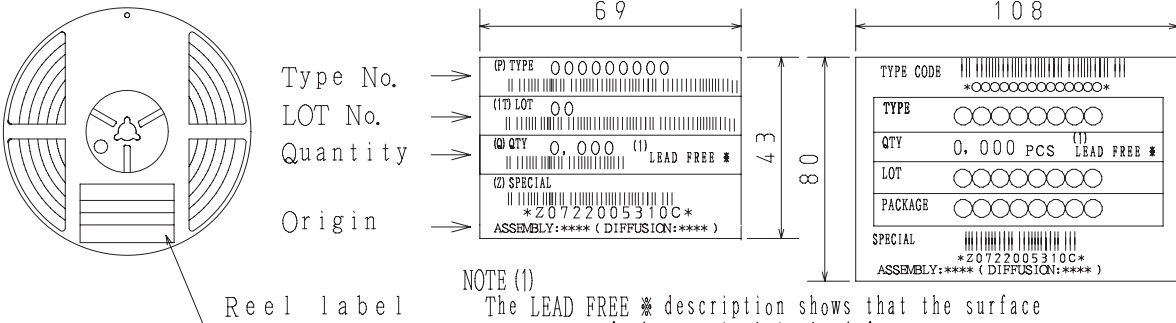
CPH5617-TL-E

1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
CPH5	CPH6	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Packing method

Reel label, Inner box label (unit:mm) Outer box label
 [It is a label at the time of factory shipments. The form of a label may change in physical distribution process.]

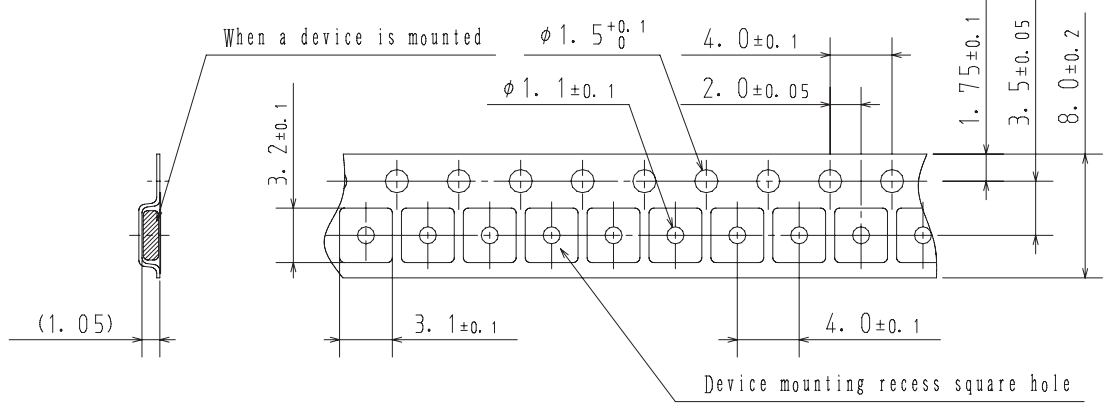


NOTE (1)
 The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

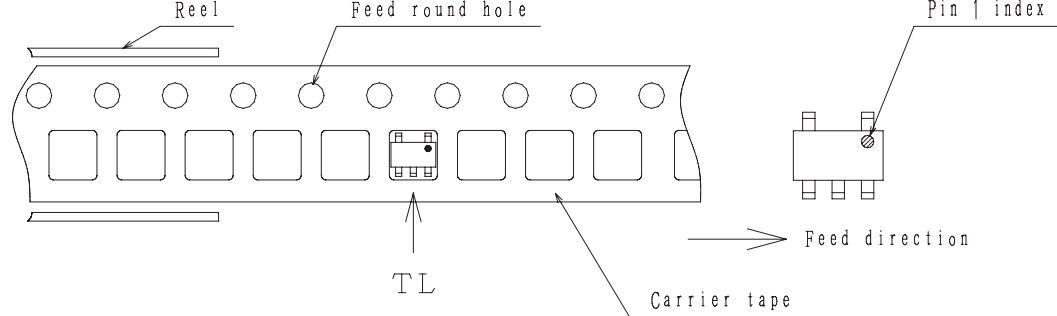
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)

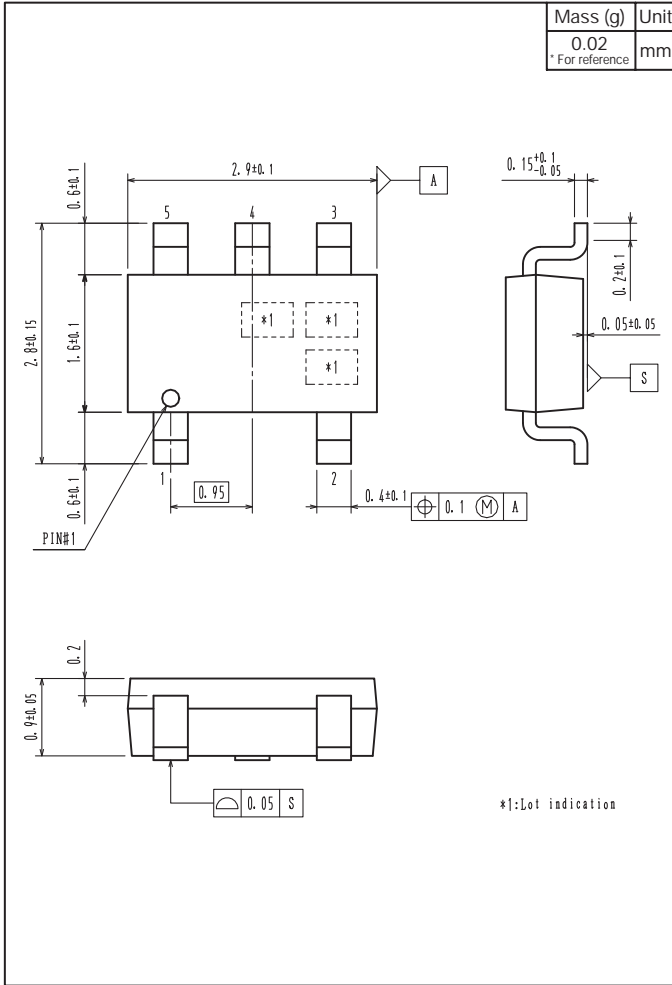


2-2. Device placement direction

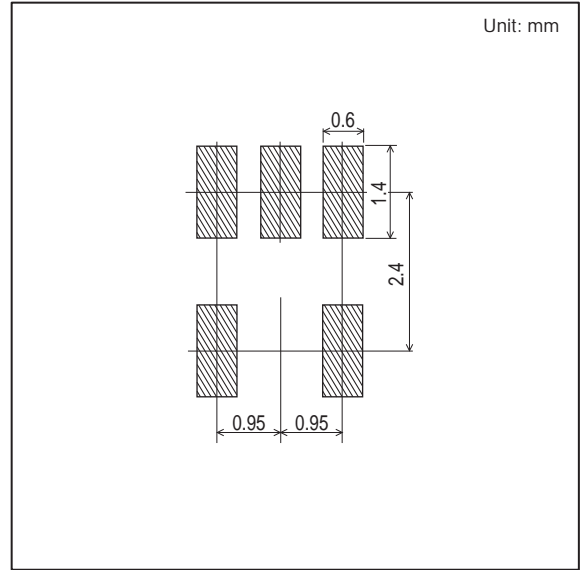


Those with pin 1 index on the feed hole side.....TL

Outline Drawing
CPH5617-TL-E



Land Pattern Example



Note on usage : Since the CPH5617 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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