

# SFM21-S THRU SFM28-S

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# SFM21-S THRU SFM28-S

## 2.0A Surface Mount Super Fast Rectifiers-50-600V

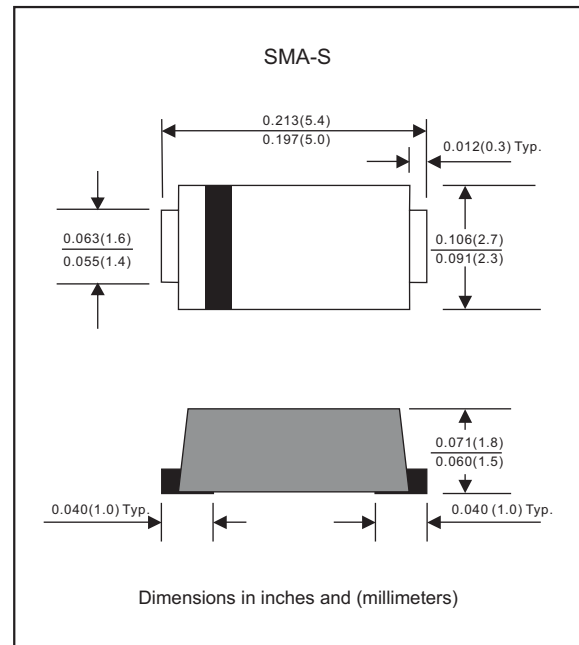
### Package outline

### Features

- Batch process design, excellent power dissipation offers better reverse leakage current and thermal resistance.
- Low profile surface mounted application in order to optimize board space.
- High current capability.
- Superfast recovery tim for switching mode application.
- High surge current capability.
- Glass passivated chip junction.
- Lead-free parts meet RoHS requirments.
- Suffix "-H" indicates Halogen free parts, ex. SFM21-S-H.

### Mechanical data

- Epoxy:UL94-V0 rated flame retardant
- Case : Molded plastic, DO-214AC / SMA-S
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026
- Polarity : Indicated by cathode band
- Mounting Position : Any
- Weight : Approximated 0.05 gram



### Maximum ratings and Electrical Characteristics (AT $T_A=25^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
Forward rectified current	Ambient temperature = $50^{\circ}\text{C}$	$I_o$			2.0	A
Forward surge current	8.3ms single half sine-wave superimposed on rate load (JEDEC methode)	$I_{FSM}$			50	A
Reverse current	$V_R = V_{RRM}$ $T_J = 25^{\circ}\text{C}$	$I_R$			5.0	uA
	$V_R = V_{RRM}$ $T_J = 125^{\circ}\text{C}$				100	
Diode junction capacitance	f=1MHz and applied 4V DC reverse voltage	$C_j$		25		pF
Storage temperature		$T_{STG}$	-65		+175	$^{\circ}\text{C}$

SYMBOLS	$V_{RRM}^{*1}$ (V)	$V_{RMS}^{*2}$ (V)	$V_R^{*3}$ (V)	$V_F^{*4}$ (V)	$t_{rr}^{*5}$ (ns)	Operating temperature $T_J, (^{\circ}\text{C})$
SFM21-S	50	35	50	0.95	35	-55 to +150
SFM22-S	100	70	100			
SFM23-S	150	105	150			
SFM24-S	200	140	200			
SFM25-S	300	210	300	1.25	35	-55 to +150
SFM26-S	400	280	400			
SFM27-S	500	350	500	1.70	35	-55 to +150
SFM28-S	600	420	600			

- \*1 Repetitive peak reverse voltage
- \*2 RMS voltage
- \*3 Continuous reverse voltage
- \*4 Maximum forward voltage@ $I_F=2.0A$
- \*5 Reverse recovery time, note 1

Note 1. Reverse recovery time test condition,  $I_F=0.5A$ ,  $I_R=1.0A$ ,  $I_{RR}=0.25A$

## Rating and characteristic curves (SFM21-S THRU SFM28-S)

FIG.1-TYPICAL FORWARD CHARACTERISTICS

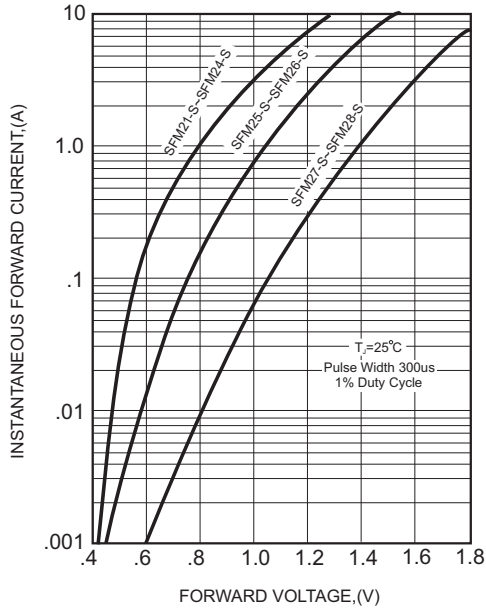


FIG.2-TYPICAL FORWARD CURRENT DERATING CURVE

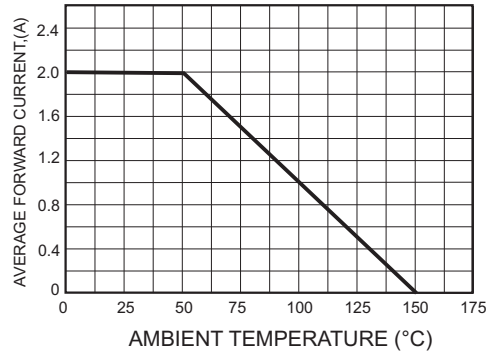


FIG.4-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

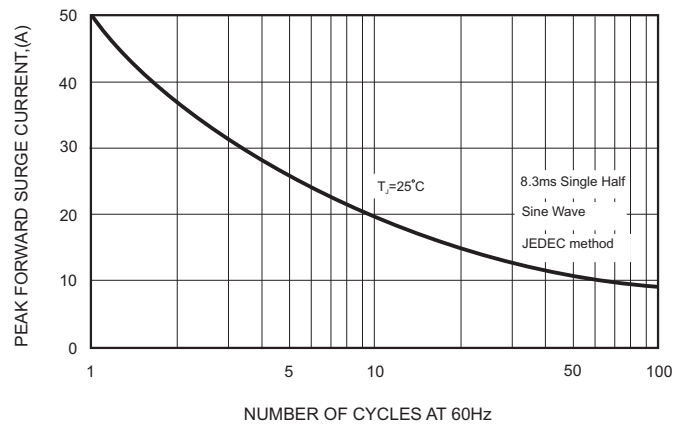
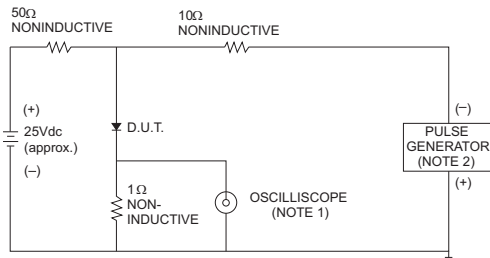


FIG.3- TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTICS



- NOTES: 1. Rise Time = 7ns max., Input Impedance = 1 megohm, 22pF.  
2. Rise Time = 10ns max., Source Impedance = 50 ohms.

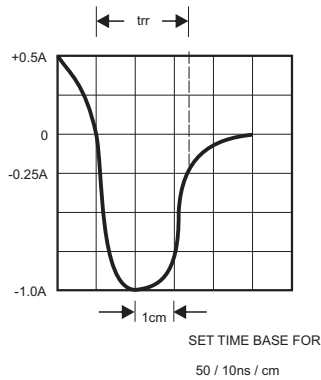
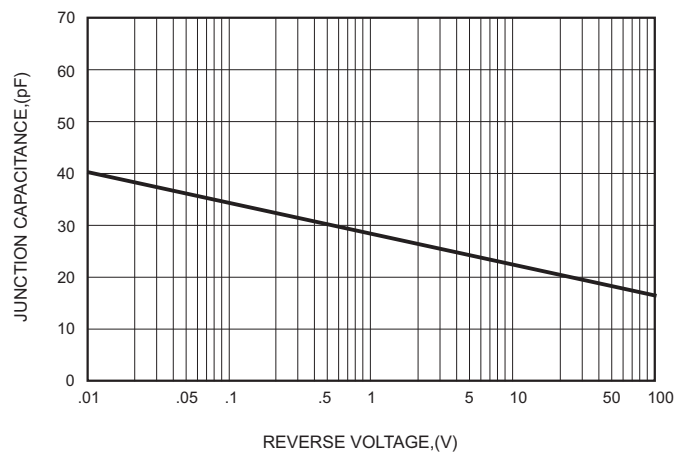




FIG.5-TYPICAL JUNCTION CAPACITANCE



# SFM21-S THRU SFM28-S

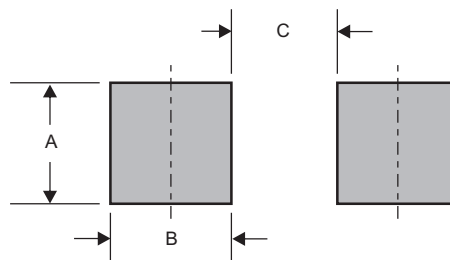
## Pinning information

Pin	Simplified outline	Symbol
Pin1 cathode Pin2 anode		

## Marking

Type number	Marking code
SFM21-S	S21
SFM22-S	S22
SFM23-S	S23
SFM24-S	S24
SFM25-S	S25
SFM26-S	S26
SFM27-S	S27
SFM28-S	S28

## Suggested solder pad layout

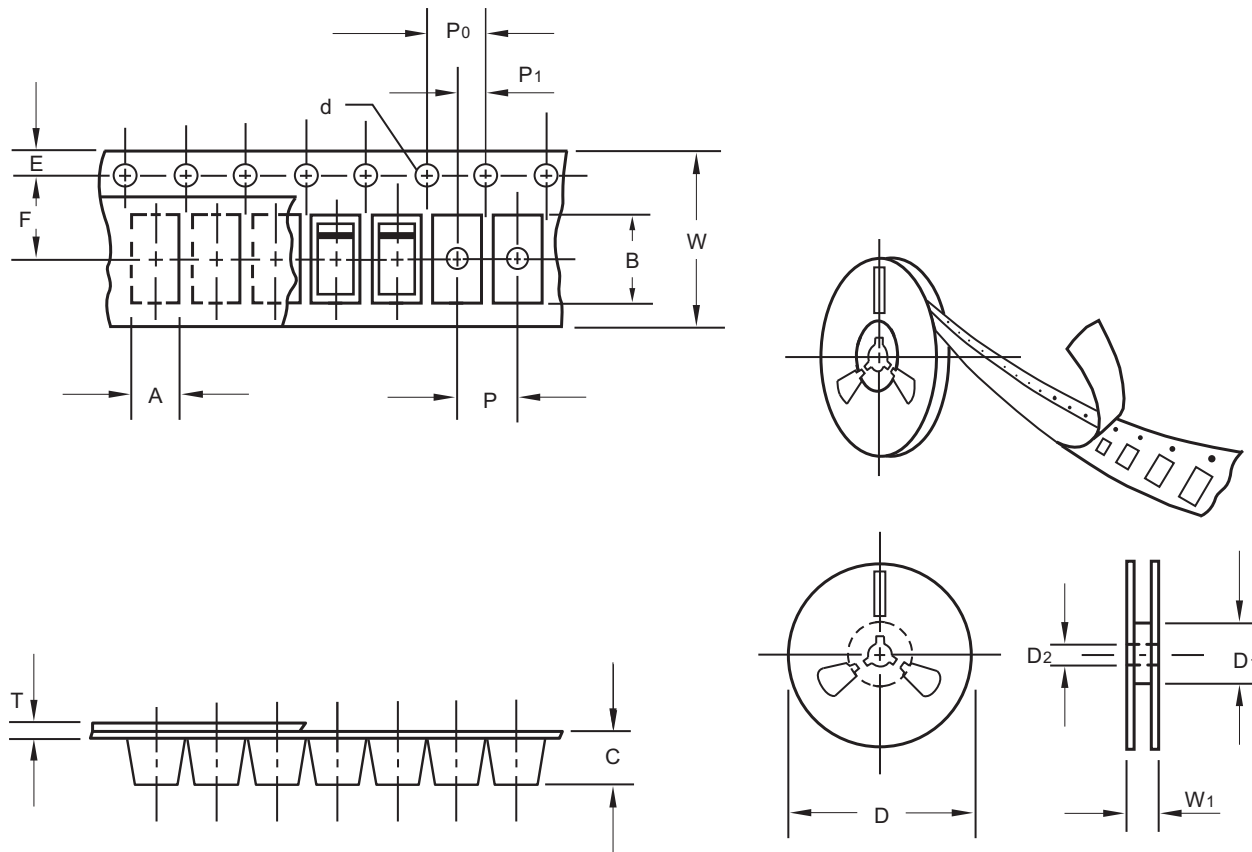


Dimensions in inches and (millimeters)

PACKAGE	A	B	C
SMA-S	0.063 (1.60)	0.059 (1.50)	0.110 (2.80)

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## Packing information



unit:mm

Item	Symbol	Tolerance	SMA-S
Carrier width	A	0.1	2.90
Carrier length	B	0.1	5.50
Carrier depth	C	0.1	2.10
Sprocket hole	d	0.1	1.50
13" Reel outside diameter	D	2.0	330.00
13" Reel inner diameter	D1	min	50.00
7" Reel outside diameter	D	2.0	178.00
7" Reel inner diameter	D1	min	62.00
Feed hole diameter	D2	0.5	13.00
Sprocket hole position	E	0.1	1.75
Punch hole position	F	0.1	5.50
Punch hole pitch	P	0.1	4.00
Sprocket hole pitch	P0	0.1	4.00
Embossment center	P1	0.1	2.00
Overall tape thickness	T	0.1	0.23
Tape width	W	0.3	12.00
Reel width	W1	1.0	18.00

Note: Devices are packed in accordance with EIA standard RS-481-A and specifications listed above.

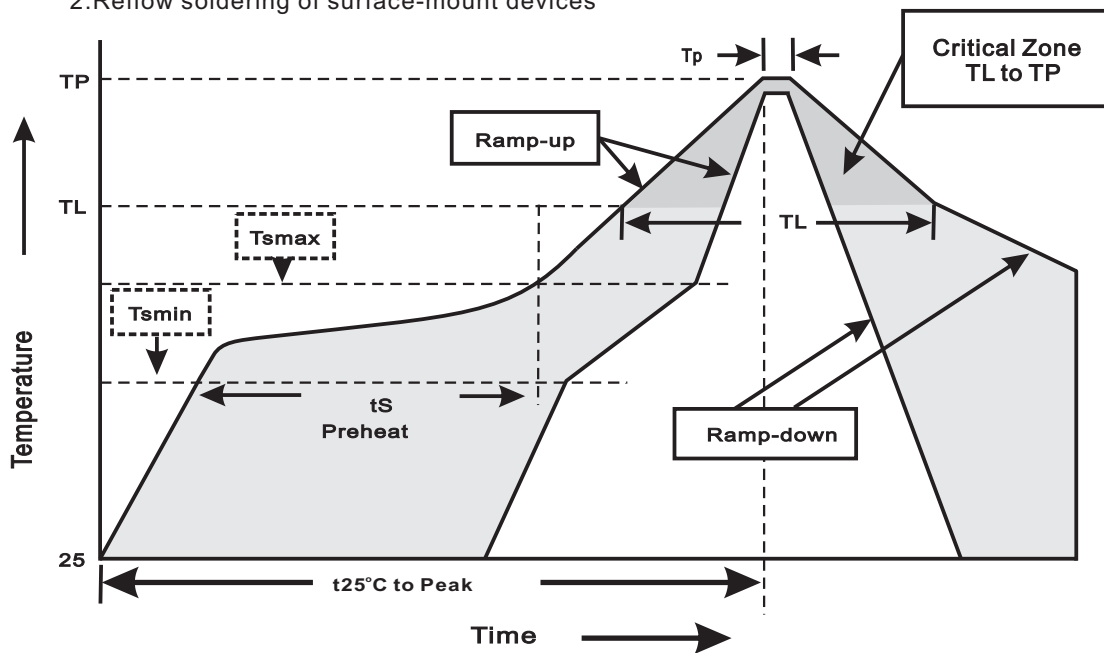
# SFM21-S THRU SFM28-S

## Reel packing

PACKAGE	REEL SIZE	REEL (pcs)	COMPONENT SPACING (m/m)	BOX (pcs)	INNER BOX (m/m)	REEL DIA, (m/m)	CARTON SIZE (m/m)	CARTON (pcs)	APPROX. GROSS WEIGHT (kg)
SMA-S	7"	2,000	4.0	20,000	183*170*183	178	382*356*387	160,000	15.5
	13"	7,500	4.0	15,000	337*337*37	330	350*330*360	120,000	14.2

## Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
- 2.Reflow soldering of surface-mount devices



### 3.Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(TL to TP)	<3°C/sec
Preheat -Temperature Min(Tsmin) -Temperature Max(Tsmax) -Time(min to max)(ts)	150°C 200°C 60~120sec
Tsmax to TL -Ramp-upRate	<3°C/sec
Time maintained above: -Temperature(TL) -Time(tL)	217°C 60~260sec
Peak Temperature(TP)	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(tp)	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes

**SFM21-S THRU SFM28-S****High reliability test capabilities**

Item Test	Conditions	Reference
1. Solder Resistance	at $260\pm 5^{\circ}\text{C}$ for $10\pm 2\text{sec.}$ immerse body into solder $1/16''\pm 1/32''$	MIL-STD-750D METHOD-2031
2. Solderability	at $245\pm 5^{\circ}\text{C}$ for 5 sec.	MIL-STD-202F METHOD-208
3. High Temperature Reverse Bias	$V_R=80\%$ rate at $T_J=150^{\circ}\text{C}$ for 168 hrs.	MIL-STD-750D METHOD-1038
4. Forward Operation Life	Rated average rectifier current at $T_A=25^{\circ}\text{C}$ for 500hrs.	MIL-STD-750D METHOD-1027
5. Intermittent Operation Life	$T_A = 25^{\circ}\text{C}$ , $I_F = I_O$ On state: power on for 5 min. off state: power off for 5 min. on and off for 500 cycles.	MIL-STD-750D METHOD-1036
6. Pressure Cooker	$15P_{SIG}$ at $T_A=121^{\circ}\text{C}$ for 4 hrs.	JESD22-A102
7. Temperature Cycling	$-55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$ dwelled for 30 min. and transferred for 5min. total 10 cycles.	MIL-STD-750D METHOD-1051
8. Thermal Shock	$0^{\circ}\text{C}$ for 5 min. rise to $100^{\circ}\text{C}$ for 5 min. total 10 cycles.	MIL-STD-750D METHOD-1056
9. Forward Surge	8.3ms single half sine-wave superimposed on rated load, one surge.	MIL-STD-750D METHOD-4066-2
10. Humidity	at $T_A=85^{\circ}\text{C}$ , RH=85% for 1000hrs.	MIL-STD-750D METHOD-1021
11. High Temperature Storage Life	at $175^{\circ}\text{C}$ for 1000 hrs.	MIL-STD-750D METHOD-1031