

GP4226

N-CHANNEL ENHANCEMENT MODE POWER MOSFET

BVDSS	30V
RDS(ON)	18mΩ
ID	8.2A

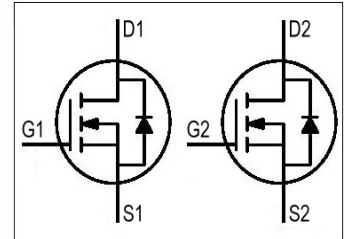
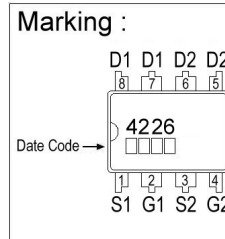
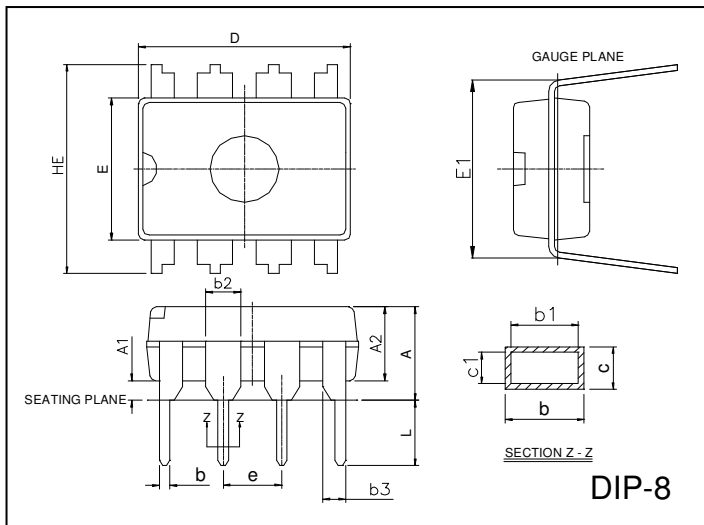
Description

The GP4226 provides the designer with the best combination of fast switching, ruggedized device design, ultra low on-resistance and cost-effectiveness.

Features

- *Low On-Resistance
- *Single Drive Requirement

Package Dimensions



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	-	0.5334	c1	0.203	0.279
A1	0.381	-	D	9.017	10.16
A2	2.921	4.953	E	6.096	7.112
b	0.356	0.559	E1	7.620	8.255
b1	0.356	0.508	e	2.540 BSC	
b2	1.143	1.778	HE	-	10.92
b3	0.762	1.143	L	2.921	3.810
c	0.203	0.356			

Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ³	$I_D @ T_A=25^\circ C$	8.2	A
Continuous Drain Current ³	$I_D @ T_A=70^\circ C$	6.7	A
Pulsed Drain Current ¹	I_{DM}	30	A
Total Power Dissipation	$P_D @ T_A=25^\circ C$	2	W
Linear Derating Factor		0.016	W/ $^\circ C$
Operating Junction and Storage Temperature Range	T_j, T_{stg}	-55 ~ +150	$^\circ C$

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance Junction-ambient ³ Max.	R_{thj-a}	62.5	$^\circ C/W$

Electrical Characteristics(T_j = 25°C Unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	BV_{DSS}	30	-	-	V	$V_{GS}=0, I_D=250\mu A$
Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS} / \Delta T_j$	-	0.03	-	V/°C	Reference to 25°C, $I_D=1mA$
Gate Threshold Voltage	$V_{GS(th)}$	1.0	-	3.0	V	$V_{DS}=V_{GS}, I_D=250\mu A$
Forward Transconductance	g_{fs}	-	15	-	S	$V_{DS}=10V, I_D=6A$
Gate-Source Leakage Current	I_{GSS}	-	-	±100	nA	$V_{GS}= \pm 20V$
Drain-Source Leakage Current(T _j =25°C)	I_{DSS}	-	-	1	uA	$V_{DS}=30V, V_{GS}=0$
Drain-Source Leakage Current(T _j =70°C)		-	-	25	uA	$V_{DS}=24V, V_{GS}=0$
Static Drain-Source On-Resistance ²	$R_{DS(ON)}$	-	-	18	mΩ	$V_{GS}=10V, I_D=8A$
		-	-	28		$V_{GS}=4.5V, I_D=6A$
Total Gate Charge ²	Q_g	-	20	30	nC	$I_D=8A$ $V_{DS}=24V$ $V_{GS}=4.5V$
Gate-Source Charge	Q_{gs}	-	5	-		
Gate-Drain ("Miller") Change	Q_{gd}	-	12	-		
Turn-on Delay Time ²	$T_{d(on)}$	-	12	-	ns	$V_{DS}=15V$ $I_D=1A$ $V_{GS}=10V$ $R_G=3.3\Omega$ $R_D=15\Omega$
Rise Time	T_r	-	8	-		
Turn-off Delay Time	$T_{d(off)}$	-	31	-		
Fall Time	T_f	-	12	-		
Input Capacitance	C_{iss}	-	1450	2320	pF	$V_{GS}=0V$ $V_{DS}=25V$ $f=1.0MHz$
Output Capacitance	C_{oss}	-	320	-		
Reverse Transfer Capacitance	C_{rss}	-	230	-		

Source-Drain Diode

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Forward On Voltage ²	V_{SD}	-	-	1.2	V	$I_S=1.7A, V_{GS}=0V$
Reverse Recovery Time ²	T_{rr}	-	27	-	ns	$I_S=8A, V_{GS}=0V$ $di/dt=100A/\mu s$
Reverse Recovery Charge	Q_{rr}	-	18	-	nC	

Notes: 1. Pulse width limited by Max. junction temperature.

2. Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

3. Mounted on 1 in² copper pad of FR4 board; 90°C/W when mounted on Min. copper pad.

Characteristics Curve

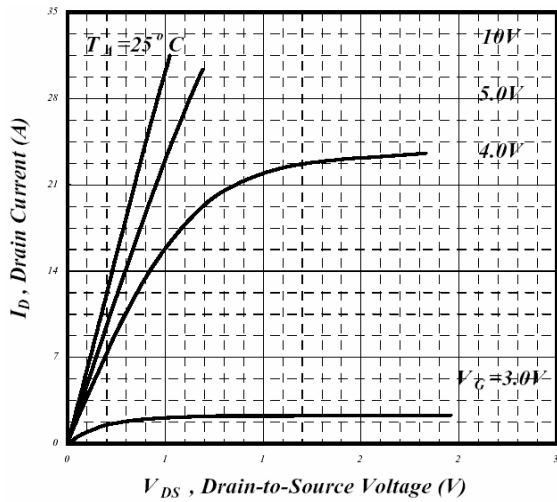


Fig 1. Typical Output Characteristics

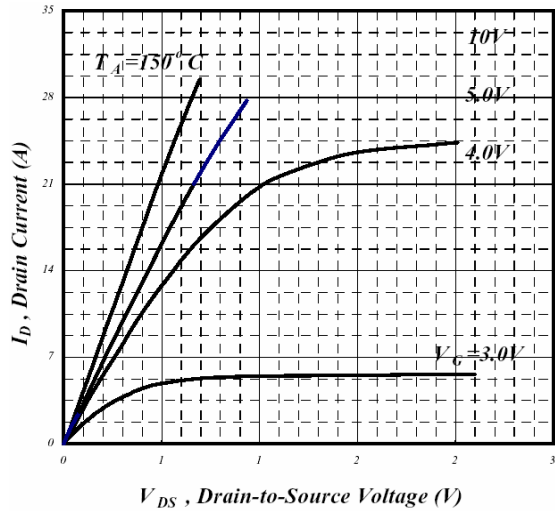


Fig 2. Typical Output Characteristics

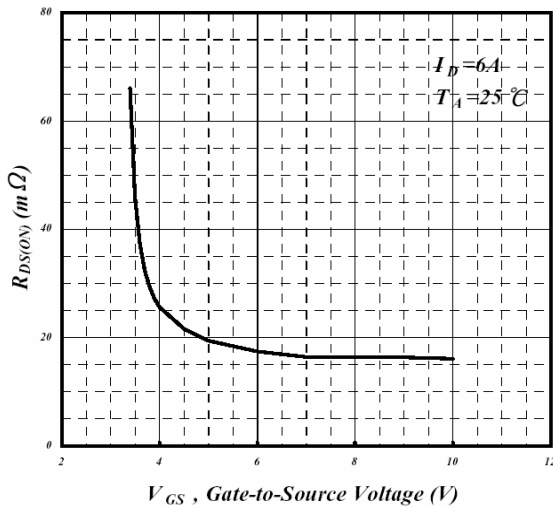


Fig 3. On-Resistance v.s. Gate Voltage

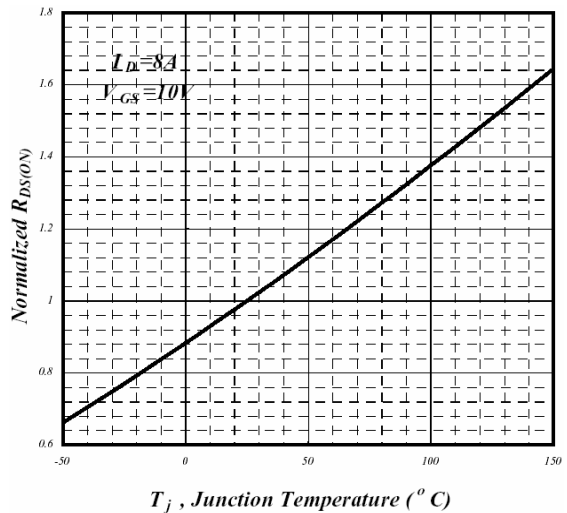


Fig 4. Normalized On-Resistance v.s. Junction Temperature

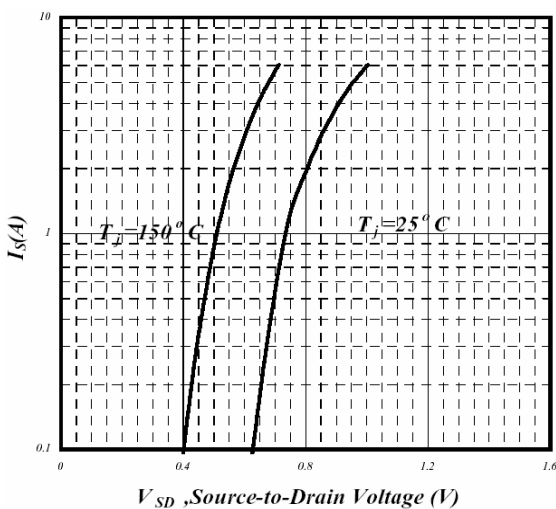


Fig 5. Forward Characteristics of Reverse Diode

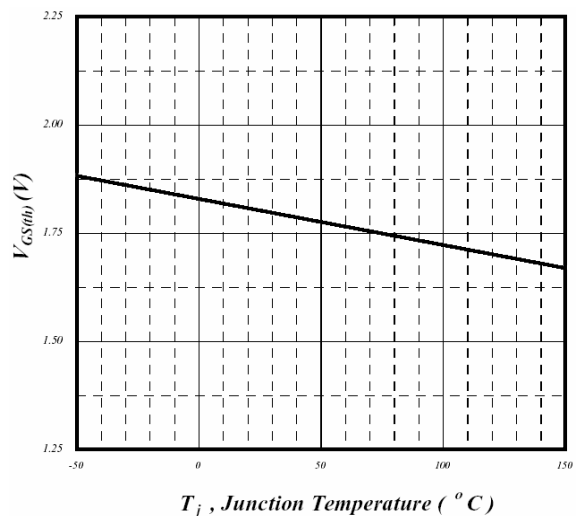


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

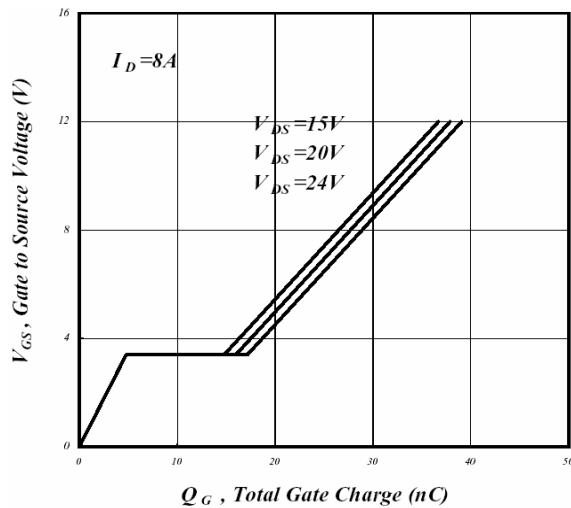


Fig 7. Gate Charge Characteristics

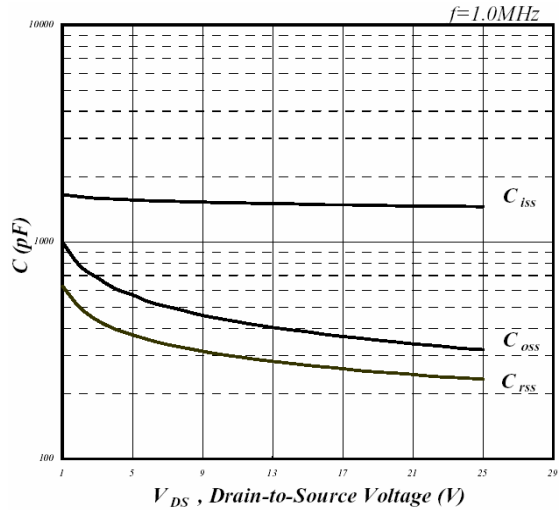


Fig 8. Typical Capacitance Characteristics

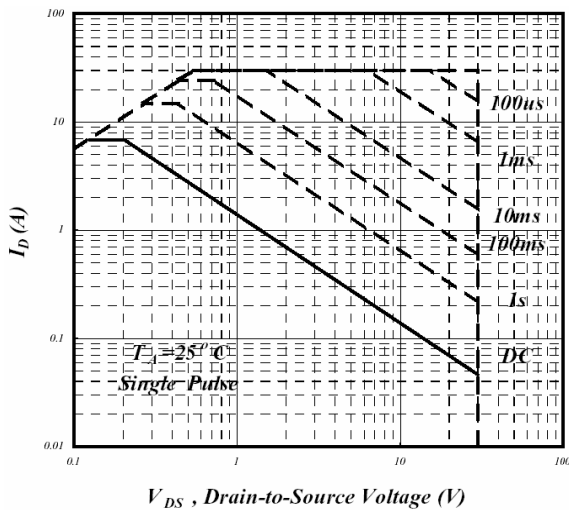


Fig 9. Maximum Safe Operating Area

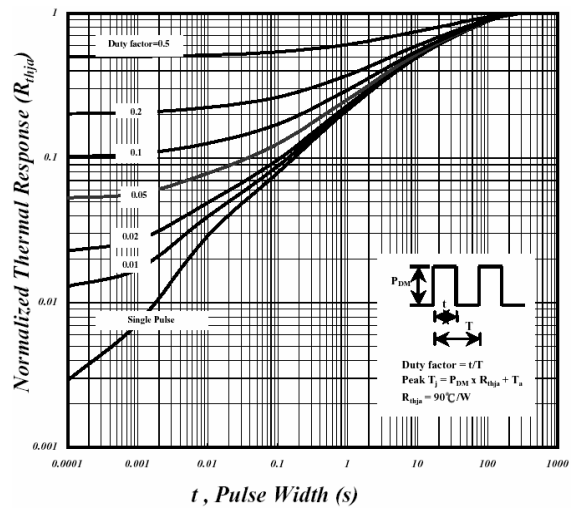


Fig 10. Effective Transient Thermal Impedance

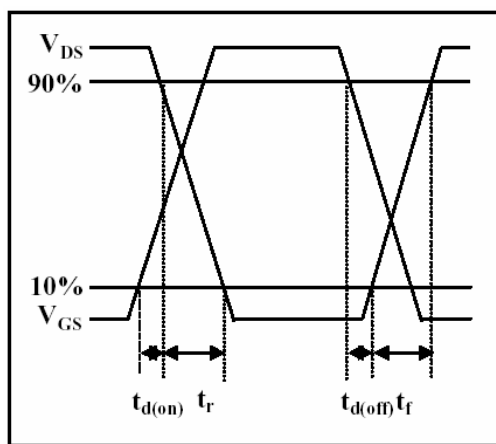


Fig 11. Switching Time Waveform

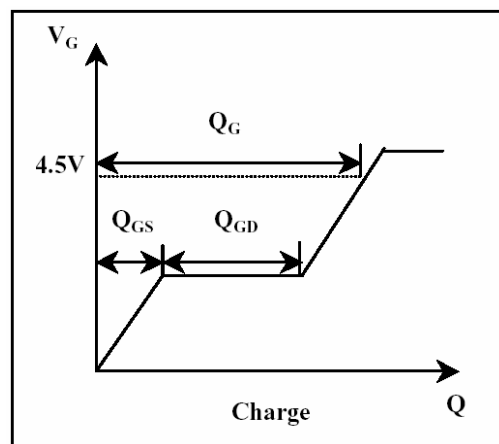


Fig 12. Gate Charge Waveform

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