

SBT2222A

NPN Silicon Transistor

Descriptions

- General purpose application
- Switching application

Features

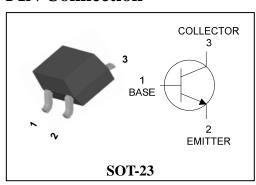
- Low Leakage current
- Low collector saturation voltage enabling low voltage operation
- Complementary pair with SBT2907A

Ordering Information

Type NO.	Marking	Package Code
SBT2222A	<u>1P</u> □ ① ②	SOT-23

①Device Code ②Year& Week Code

PIN Connection



Absolute maximum ratings

Ta=25°C

110001ate maximam ratings	1u-25 C		
Characteristic	Symbol	Ratings	Unit
Collector-Base voltage	V_{CBO}	75	V
Collector-Emitter voltage	V_{CEO}	40	V
Emitter-base voltage	V_{EBO}	5	V
Callector ourment	I _C	0.6	A(DC)
Collector current	I _{CP} *	1.2	A(Pulse)
Collector dissipation	Pc**	350	mW
Junction temperature	Tj	150	°C
Storage temperature range	T _{stg}	-55~150	°C

^{*:} Single pulse, tp= 300 μ s

KSD-T5C052-003

^{**:} Package mounted on 99.5% alumina 10×8×0.6mm

SBT2222A

Electrical Characteristics

Ta=25°C

2

Characteristic	Symbol	ool Test Condition		Typ.	Max.	Unit
Collector-Base breakdown voltage	BV _{CBO}	$I_C = 10 \mu A, I_E = 0$	75	-	-	V
Collector-Emitter breakdown voltage	BV _{CEO}	I _C =1mA, I _B =0	40	-	-	V
Emitter-Base breakdown voltage	BV _{EBO}	$I_E = 10 \mu A, I_C = 0$		-	-	V
Collector cut-off current	I _{CBO}	V _{CB} =75V, I _E =0	-	-	20	nA
Collector cut-off current	I _{CEX}	V _{CE} =30V, V _{EB} =0.5V	-	-	50	nA
DC current gain	h _{FE}	V _{CE} =10V, I _C =10mA	100	-	-	-
Collector-Emitter saturation voltage	V _{CE(sat)}	I _C =150mA, I _B =15mA	-	-	0.4	V
Transition frequency	f _T	V_{CE} =20V, I_{C} =20mA, f =100MHz	250	-	-	MHz
Collector output capacitance	C _{ob}	$V_{CB}=10V$, $I_{E}=0$, $f=1MHz$	-	-	8	pF
Delay time	t _d	$V_{CC}=30V_{dc}$, $V_{BE(off)}=0.5V_{dc}$,	-	-	10	ns
Rise time	t _r	$I_{C}=150\text{mA}_{dc}, I_{B1}=15\text{mA}_{dc}$	-	-	25	ns
Storage time	t _s	$V_{CC} = 30V_{dc}, I_{C} = 150mA_{dc},$	-	-	225	ns
Fall Time	t _f	$I_{B1} = I_{B2} = 15 \text{mA}_{dc}$	_	-	60	ns

Electrical Characteristic Curves

Fig. $1 P_C - T_a$

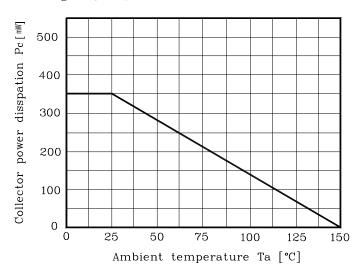


Fig. 2 $h_{FE}\$ - $\ I_{C}$

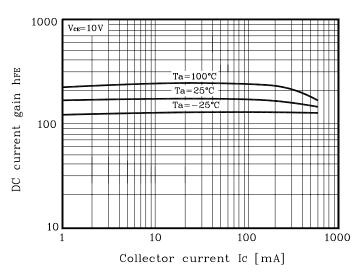


Fig. 3 I_C - $V_{CE(SAT)}$

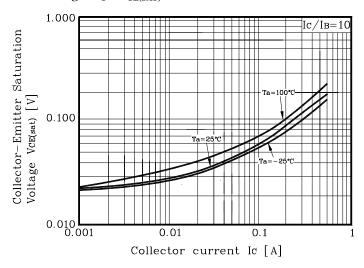


Fig. 4 $I_C - V_{BE(SAT)}$

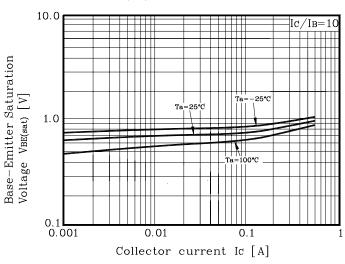
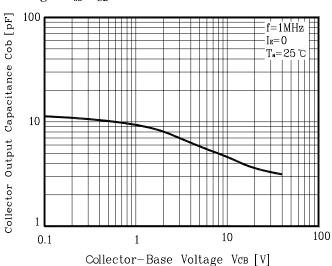
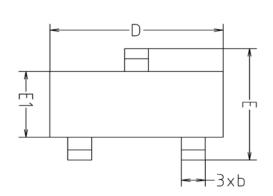


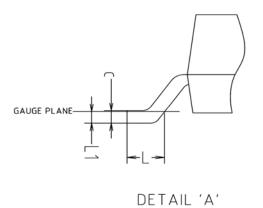
Fig. 5 C_{ob} - V_{CB}

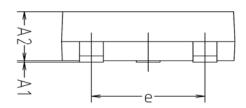


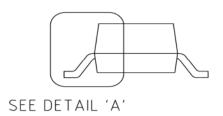
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Outline Dimension



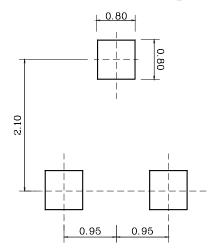






SYMBOL	MILLIMETERS			NOTE
3111000	MINIMUM	NOMINAL	MAXIMUM	11012
A1	0.00	-	0.10	
A2	0.82	-	1.02	
Ь	0.39	0.42	0.45	
С	0.09	0.12	0.15	
D	2.80	2.90	3.00	
E	2.20	2.40	2.60	
E1	1.20	1.30	1.40	
е	1.90BSC			
L	0.20	-	-	
L1		0.12BSC		

*Recommend PCB solder land [Unit: mm]



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