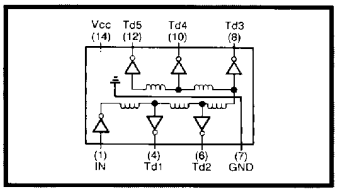
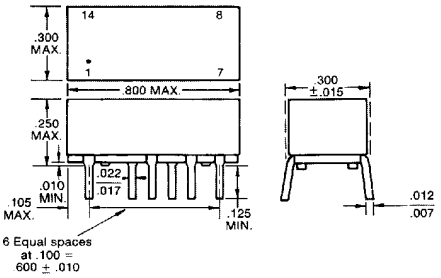
T-47-13

Standard-Performance TTL Delay Modules

Part No.	Tap Delays (ns)					All Taps (Max.)	
	T_D1	T_D2	T_D3	T_D4	T_D5	T_{R0}	T_{F0}
LTTLDL025	5.0	10.0	15.0	20.0	25.0	2.0	2.0
LTTLDL050	10.0	20.0	30.0	40.0	50.0	2.0	2.0
LTTLDL075	15.0	30.0	45.0	60.0	75.0	2.0	2.0
LTTLDL100	20.0	40.0	60.0	80.0	100.0	2.0	5.0
LTTLDL125	25.0	50.0	75.0	100.0	125.0	2.0	6.0
LTTLDL150	30.0	60.0	90.0	120.0	150.0	2.0	7.0
LTTLDL200	40.0	80.0	120.0	160.0	200.0	2.0	8.0
LTTLDL250	50.0	100.0	150.0	200.0	250.0	2.0	9.0
LTTLDL500	100.0	200.0	300.0	400.0	500.0	2.0	9.0

Delay Characteristics measured at V_{CC} = 5.0V, 25°C no load.
Delay Tolerance ± 2 ns or 5% whichever is greater.
Rise time measured @ 0.8V to 2.0V levels.
For minimum input pulse width—contact factory.





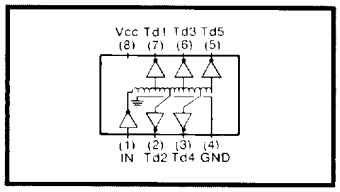
- 5 to 500 ns delays available.
- Temperature coefficient ± 2 ns or ±4% (whichever is greater) at maximum delay, 0 to 70°C.
- Compatible with Schottky TTL, low-power Schottky TTL, FACT, AS, ALS and FAST logic circuits.
- Transfer-molded packaging for highest reliability.
- Designed for leading edge timing. Trailing edge timing available.
- Supply voltage + 5Vdc.
- 10-tap models available for LTTLDL series. Contact factory for details.

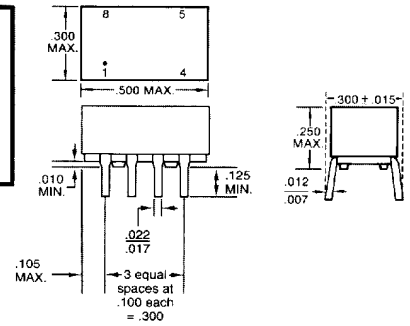
5-Tap TTL Delay Modules

- Standard size (excluding leads)—0.8″ L × 0.3″ W × 0.25″ H.
- Five equal taps in 20% increments of total delay.
- 14-pin DIP package.

Part No.	Tap Delays (ns)					All Taps (Max.)	
	T_D1	T_D2	T_D3	T_D4	T_D5	T_{R0}	T_{F0}
HTTLDL025	5.0	10.0	15.0	20.0	25.0	2.0	2.0
HTTLDL050	10.0	20.0	30.0	40.0	50.0	2.0	2.0
HTTLDL075	15.0	30.0	45.0	60.0	75.0	2.0	2.0
HTTLDL100	20.0	40.0	60.0	80.0	100.0	2.0	5.0
HTTLDL125	25.0	50.0	75.0	100.0	125.0	2.0	6.0
HTTLDL150	30.0	60.0	90.0	120.0	150.0	2.0	7.0
HTTLDL200	40.0	80.0	120.0	160.0	200.0	2.0	8.0
HTTLDL250	50.0	100.0	150.0	200.0	250.0	2.0	9.0
HTTLDL500	100.0	200.0	300.0	400.0	500.0	2.0	9.0

Delay Characteristics measured at V_{CC} = 5.0V, 25°C no load.
Delay Tolerance ± 2 ns or 5% whichever is greater.
Rise time measured @ 0.8V to 2.0V levels.
For minimum input pulse width—contact factory.





Compact 5-Tap TTL Delay Modules

- Compact size (excluding leads)—0.5″ L × 0.3″ W × 0.25″ H.
- Five equal taps in 20% increments of total delay.
- 8-pin DIP package.

Notes

Only the pins specified in the schematics are provided with each package.

Pin numbers shown are for reference only and are not necessarily marked on unit.

Lead material is electro tin plated (alloy 42) or solder dipped.

All specifications are subject to change without notice.

Technitrol®

1952 East Allegheny Avenue, Philadelphia, PA 19134 USA 215-426-9105 • Fax 215-426-2836