

## ■GENERAL DESCRIPTION

The AMS6816 series are highly precise, low power consumption, 3 terminal, positive voltage regulators manufactured using CMOS and laser trimming technologies. The series provides large currents with a significantly small dropout voltage.

The AMS6816 consists of a current limiter circuit, a driver transistor, a precision reference voltage and an error correction circuit. The series is compatible with low ESR ceramic capacitors. The current limiter's foldback circuit operates as a short circuit protection as well as the output current limiter for the output pin.

Output voltages are internally by laser trimming technologies. It is selectable in 0.1V increments within a range of 1.5V to 5.0V.

SOT-23, SOT-89, TO-92 are available.

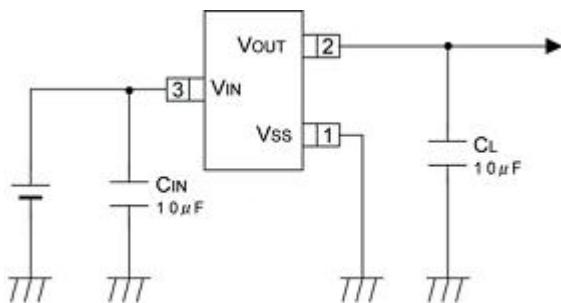
## ■APPLICATIONS

- Battery powered equipment
- Reference voltage sources
- Cameras, video cameras
- Portable AV systems
- Mobile phones
- Portable games
- Cordless phones,  
wireless communication equipment

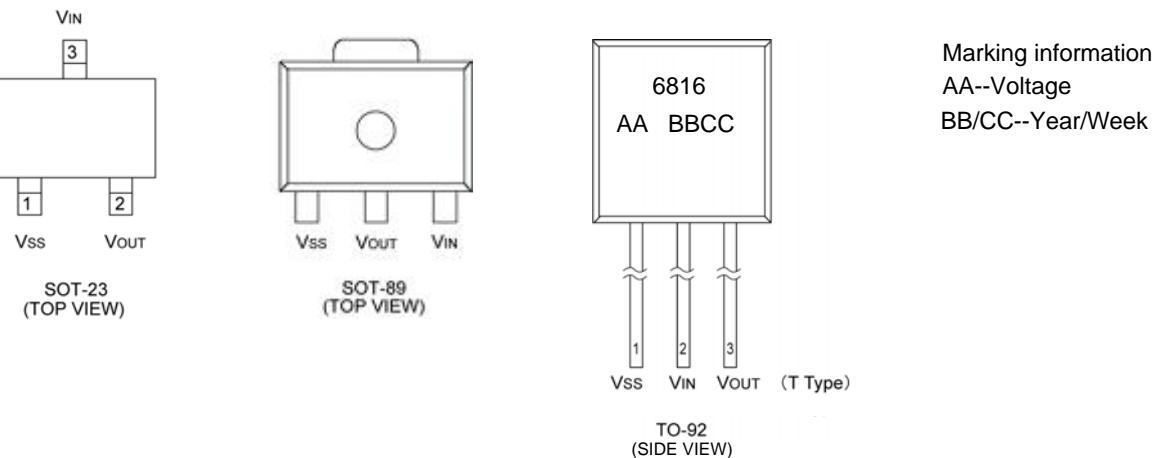
## ■FEATURES

<b>Maximum Output Current</b>	: 500mA (Vin=3.8V, Vout=3.3V)
<b>Dropout Voltage</b>	: 0.5V@500mA
<b>Maximum Operating Voltage</b>	: 6.0V
<b>Output Voltage Range</b>	: 1.5V ~ 5.0V (0.1V increments)
<b>Highly Accurate</b>	: ±2%
<b>Low Power Consumption</b>	: 8 μ A (TYP.)
<b>Low ESR Capacitor</b>	: Ceramic capacitor compatible
<b>Protection</b>	: Current Limit Circuit Built-in
<b>Operating Ambient Temperature</b>	: -25°C~ +85°C
<b>Packages</b>	: SOT-23 SOT-89 TO-92
<b>Environmentally Friendly</b>	: EU RoHS Compliant, Pb Free

## ■TYPICAL APPLICATION CIRCUIT



## ■ PIN CONFIGURATION



## ■ PIN ASSIGNMENT

PIN NUMBER			PIN NAME	FUNCTIONS
SOT-23	SOT-89	TO-92		
1	1	1	V <sub>SS</sub>	Ground
3	3	2	V <sub>IN</sub>	Power Input
2	2	3	V <sub>OUT</sub>	Output

## ■ PRODUCT CLASSIFICATION

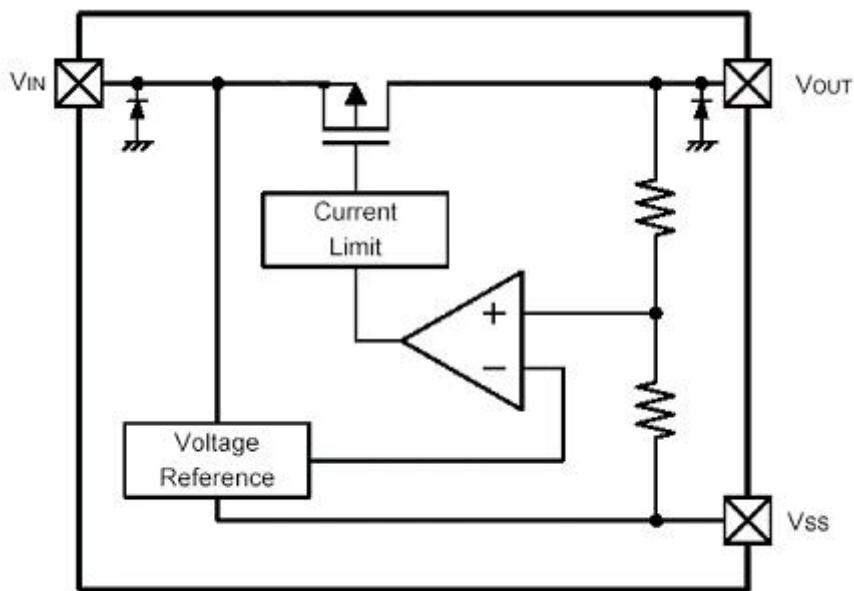
### ● Ordering Information

AMS6816 ①②③④⑤-⑥<sup>(\*)</sup>

DESIGNATOR	ITEM	SYMBOL	DESCRIPTION
①②	Output Voltage	12~50	e.g. V <sub>OUT</sub> : 3.0V → ①=3, ②=0
③	Accuracy	2	± 2% (V <sub>OUT</sub> ≥ 1.5V), ± 30mV (V <sub>OUT</sub> < 1.5V)
		1	± 1% (V <sub>OUT</sub> ≥ 2.0V)
④⑤-⑥	Packages (Order Unit)	MR	SOT-23 (3,000/Reel)
		MR-G	SOT-23 (3,000/Reel)
		PR	SOT-89 (1,000/Reel)
		PR-G	SOT-89 (1,000/Reel)
		TH	TO-92 (T type), Paper type (2,000/Tape)
		TH-G	TO-92 (T type), Paper type (2,000/Tape)
		TB	TO-92 (T type), Bag type (500/Bag)
		TB-G	TO-92 (T type), Bag type (500/Bag)

<sup>(\*)</sup> The “-G” suffix denotes Halogen and Antimony free as well as being fully RoHS compliant.

## ■ BLOCK DIAGRAM



## ■ ABSOLUTE MAXIMUM RATINGS

Ta=25°C			
PARAMETER	SYMBOL	RATINGS	UNITS
Input Voltage	V <sub>IN</sub>	6.0	V
Output Current	I <sub>OUT</sub>	500	mA
Output Voltage	V <sub>OUT</sub>	V <sub>SS</sub> - 0.3 ~ V <sub>IN</sub> + 0.3	V
Power Dissipation	SOT-23	300	mW
	SOT-89	500	
	TO-92	500	
Operating Ambient Temperature	T <sub>OPR</sub>	- 25 ~ + 85	°C
Storage Temperature	T <sub>STG</sub>	- 40 ~ + 125	°C



## ●AMS6816

Ta=25 °C

PARAMETER	SYMBOL	CONDITIONS <sup>(*)1</sup>	MIN.	TYP.	MAX.	UNIT S	
Output Voltage	Vout(E) <sup>(*)2</sup>	Iout=10mA , Vin=Vout+1V	x 0.98	VOUT(T) <sup>(*)1</sup>	x 1.02	V	
Maximum Output Current	IoutMAX	Vin=Vout+1V		500	-	mA	
Load Regulation	ΔVout	Vin=Vout+1V 1mA≤Iout≤100mA		14		mV	
Dropout Voltage <sup>(*)3</sup>	Vdif	Iout=500mA		500		mV	
Supply Current	Iss	Vin=Vout+1V		8		μA	
Line Regulation	ΔVOUT ΔVIN·VOUT	Vout+1V≤Vin≤6V Iout=40mA		0.03		%/V	
Input Voltage	VIN			-	6.0	V	
Power Supply Ripple Rejection Ratio	PSRR	Vin=[Vout+1]V+1Vp-pAC Iout=10mA, f=1KHz		50		dB	

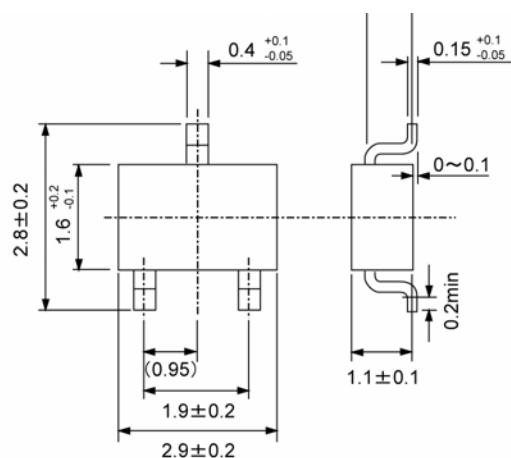
## NOTE:

- \* 1. Vout(T): Specified Output Voltage
- \* 2. Vout(E) :Effective output voltage (ie. The output voltage when "Vout(T)+1.0V" is provided at the Vin pin while maintaining a certain Iout value.)
- \* 3. Vdif : Vin1-Vout(E)'  
Vin1 :The input voltage when Vout(E)' appears as input voltage is gradually decreased.  
Vout(E)' : A voltage equal to 98% of the output voltage whenever an amply stabilized {VOUT(T) + 1.0V} is input .

## ■PACKAGING INFORMATION

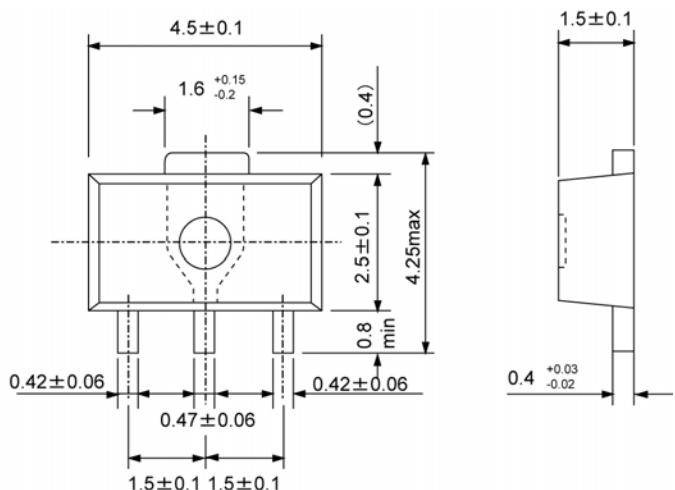
●SOT-23

Unit : mm



●SOT-89

Unit : mm



●TO-92

Unit : mm

