

SURFACE MOUNT SILICON PIN ATTENUATOR DIODE

DESCRIPTION:

The **ASI APA1000 SERIES** is a Silicon Pin Attenuator Diode in an SOT-23 Package.

MAXIMUM RATINGS

I	100 mA
V	200 V
P_{DISS}	250 mW @ T _C = 25 °C

PACKAGE STYLE SOT-23

Pin configuration:

Pin Configuration:

Single: Pin #1 = Anode, Pin #2 = N.C., Pin #3 = Cathode

Common Cathode: Pin #1 = Anode, Pin #2 = Anode, Pin #3 = Cathode/Cathode

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	2.80	3.04	0.1102	0.1197
B	1.20	1.40	0.0472	0.0551
C	0.89	1.11	0.0350	0.0440
D	0.37	0.50	0.0150	0.0200
G	1.78	2.04	0.0701	0.0807
H	0.013	0.100	0.0005	0.0040
J	0.085	0.177	0.0034	0.0070
K	0.45	0.60	0.0180	0.0236
L	0.89	1.02	0.0350	0.0401
S	2.10	2.50	0.0830	0.0984
V	0.45	0.60	0.0177	0.0236

CHARACTERISTICS T_C = 25 °C

MODEL NUMBER	Configuration	Min. Breakdown Voltage (V) I _R =10μA	Max. Capacitance (pF) f=1MHz V _R =50V	Max. Series Resistance (Ω) f=100MHz, I _F =10mA	Typical Series Resistance (Ω) f=100MHz (I _F =1mA/ I _F =10μA)	Typical Carrier Lifetime (μSec) I _F =10mA
APA1000	Single	200	0.35	6.0	30 / 1500	2.0
APA1000CC	Common Cathode	200	0.35	6.0	30 / 1500	2.0

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REV. A

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Specifications are subject to change without notice.