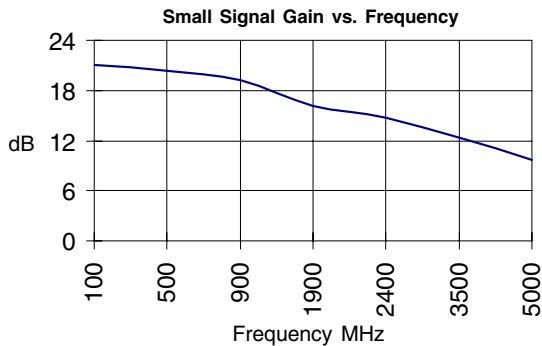


Product Description

Stanford Microdevices' SGA-6486 is a high performance cascadeable 50-ohm amplifier. This RFIC uses the latest Silicon Germanium Heterostructure Bipolar Transistor (SiGe HBT) process featuring 1 micron emitters with F_T up to 65 GHz.

This circuit uses a darlington pair topology with resistive feedback for broadband performance as well as stability over its entire temperature range. Internally matched to 50 ohm impedance, the SGA-6486 requires only DC blocking and bypass capacitors for external components.



Electrical Specifications at $T_a = 25^\circ\text{C}$

Symbol	Parameters: Test Conditions: $Z_o = 50 \text{ Ohms}$, $f = \text{DC-1800 MHz}$		Units	Min.	Typ.	Max.
P_{1dB}	Output Power at 1dB Compression	$f = 850 \text{ MHz}$ $f = 1950 \text{ MHz}$	dBm dBm		21.0 18.5	
S_{21}	Small Signal Gain	$f = \text{DC-1000 MHz}$ $f = 1000\text{-}2000 \text{ MHz}$ $f = 2000\text{-}5000 \text{ MHz}$	dB dB dB	17.5	19.7 16.7 12.3	
S_{12}	Reverse Isolation	$f = \text{DC-1000 MHz}$ $f = 1000\text{-}2000 \text{ MHz}$ $f = 2000\text{-}5000 \text{ MHz}$	dB dB		23.6 22.3 17.0	
S_{11}	Input VSWR	$f = \text{DC-5000 MHz}$	-		1.50:1	
S_{22}	Output VSWR	$f = \text{DC-5000 MHz}$	-		1.50:1	
IP_3	Third Order Intercept Point	$f = 850 \text{ MHz}$ $f = 1950 \text{ MHz}$	dBm dBm		35.0 32.5	
NF	Noise Figure	$f = \text{DC-1000 MHz}$ $f = 1000\text{-}2400 \text{ MHz}$	dB dB		3.0 3.3	
T_D	Group Delay	$f = 1000 \text{ MHz}$	pS		130.6	
V_D	Device Voltage		V	4.4	5.2	5.5
I_D	Device Current		mA		75	

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SGA-6486

DC-1800 MHz Silicon Germanium HBT Cascadeable Gain Block



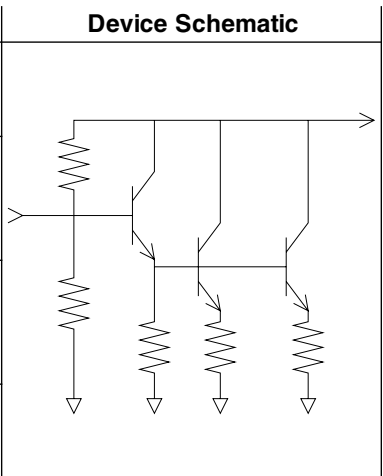
Product Features

- DC-1800 MHz Operation
- Single Voltage Supply
- High Output Intercept: +35.0 dBm typ. at 850 MHz
- High Output Power : 21.0 dBm typ. at 850 MHz
- High Gain : 19.7dB typ. at 850 MHz
- Internally Matched to 50 Ohms Input & Output

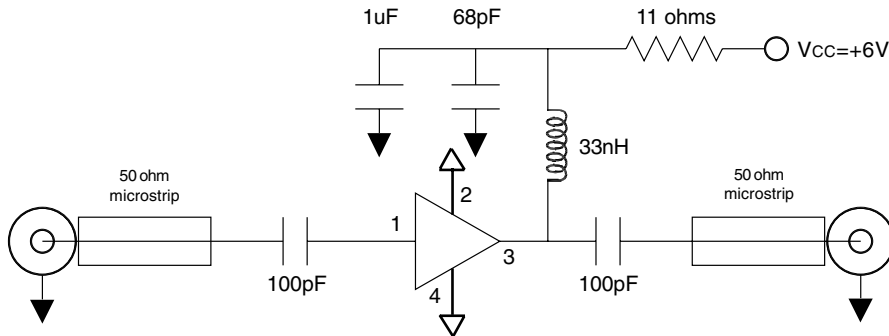
Applications

- Oscillator Amplifiers
- Final PA for Low Power Applications
- IF/ RF Buffer Amplifier
- Drivers for CATV Amplifiers

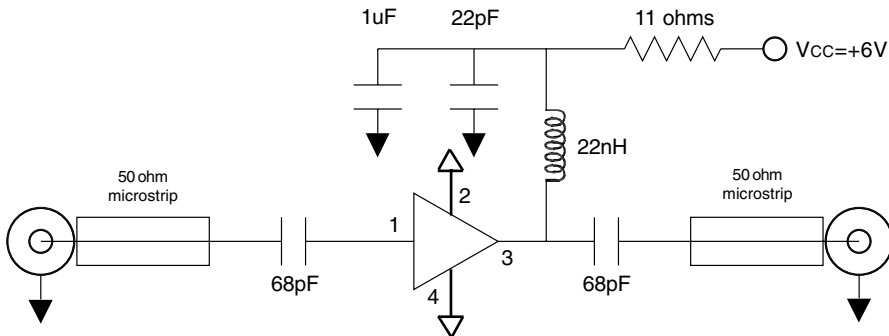
Parameter	Specification				Test Condition
	Min	Typ.	Max.	Unit	
Bandwidth					T= 25C
Frequency Range	DC		1800	MHz	
Device Bias					T= 25C
Operating Voltage		5.2		V	
Operating Current		75.0		mA	
500 MHz					T= 25C
Gain		20.3		dB	
Noise Figure		2.9		dB	
Output IP3		34.8		dBm	
Output P1dB		20.7		dBm	
Input Return Loss		23.3		dB	
Isolation		23.9		dB	
850 MHz					T= 25C
Gain		19.7		dB	
Noise Figure		3.0		dB	
Output IP3		35.0		dBm	
Output P1dB		21.0		dBm	
Input Return Loss		22.8		dB	
Isolation		23.6		dB	
1950 MHz					T= 25C
Gain		16.1		dB	
Noise Figure		3.6		dB	
Output IP3		32.5		dBm	
Output P1dB		18.5		dBm	
Input Return Loss		21.4		dB	
Isolation		21.3		dB	
2400 MHz					T= 25C
Gain		14.8		dB	
Noise Figure		3.7		dB	
Output IP3		31.4		dBm	
Output P1dB		17.2		dBm	
Input Return Loss		17.4		dB	
Isolation		19.7		dB	

Pin #	Function	Description	Device Schematic
1	RF IN	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.	
2	GND	Connection to ground. Use via holes for best performance to reduce lead inductance as close to ground leads as possible.	
3	RF OUT/ BIAS	RF output and bias pin. DC voltage is present on this pin, therefore a DC blocking capacitor is necessary for proper operation.	
4	GND	Sames as Pin 2	

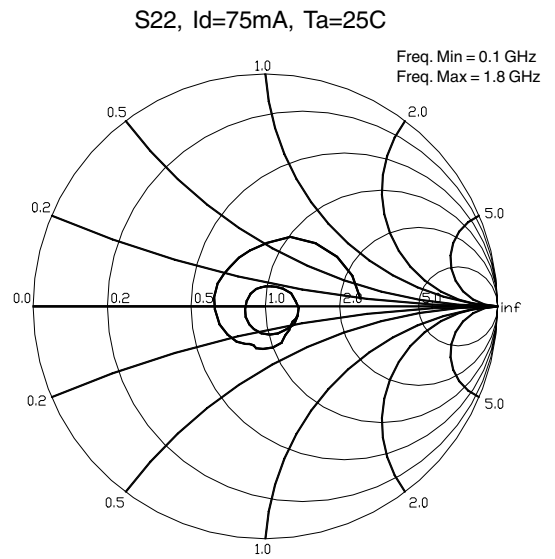
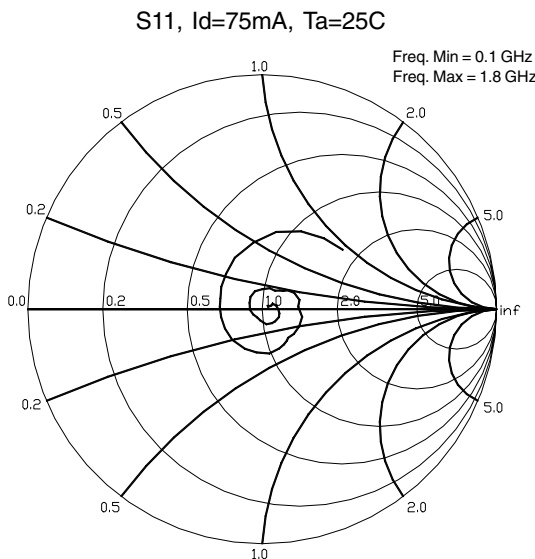
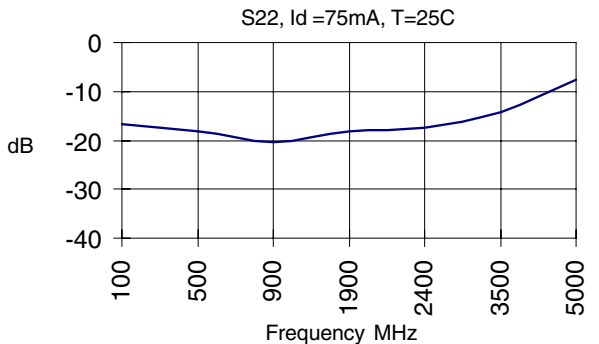
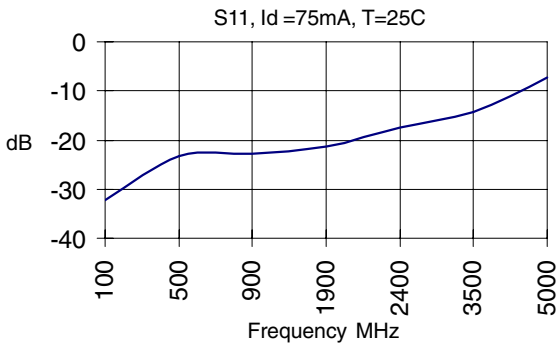
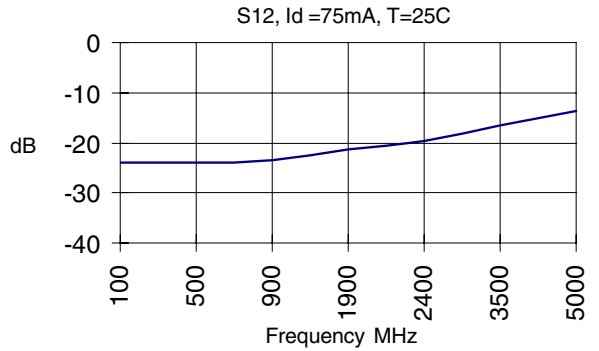
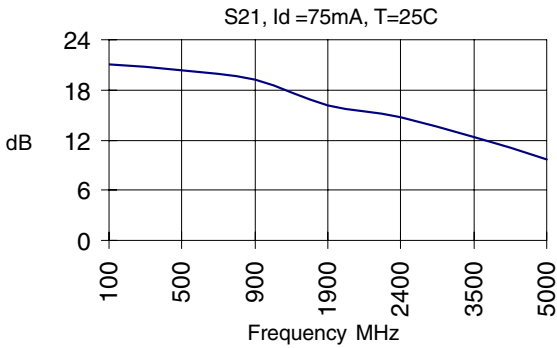
Application Schematic for +6V Operation at 900 MHz



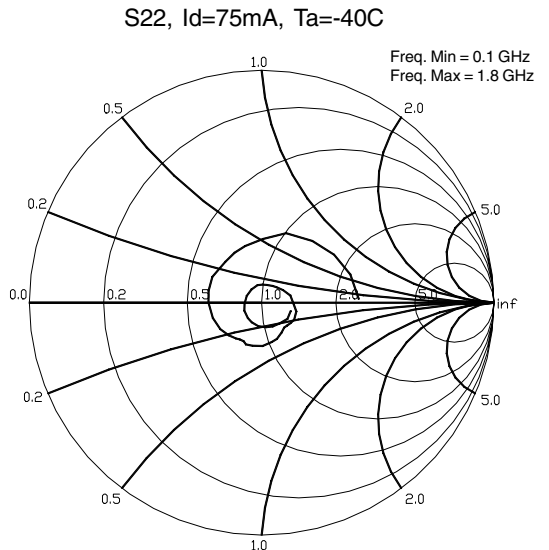
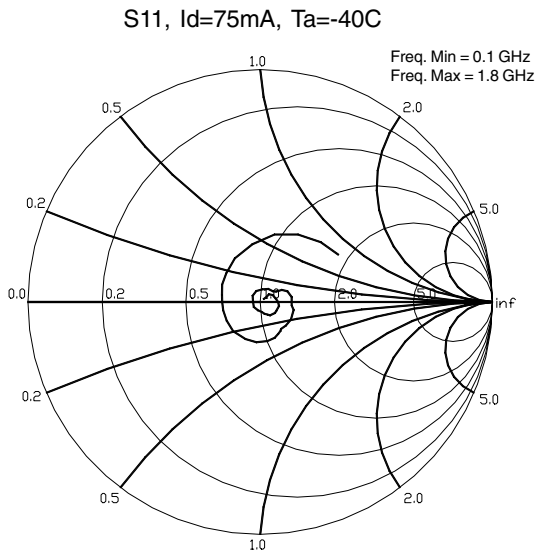
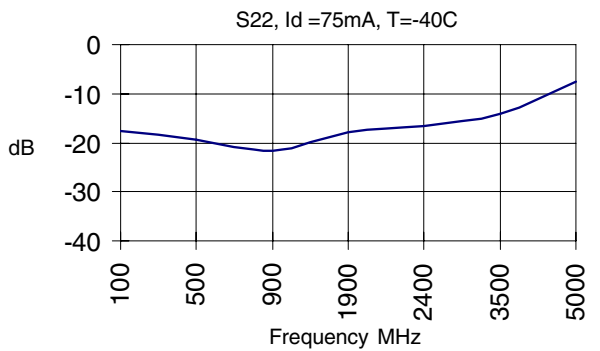
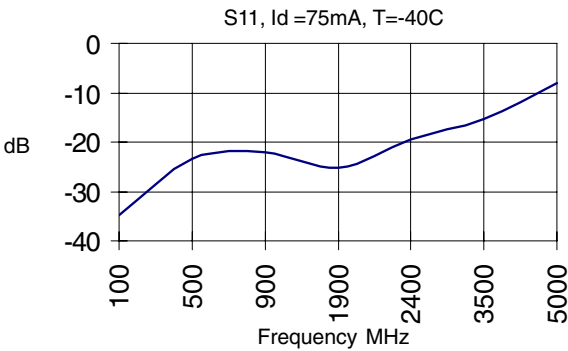
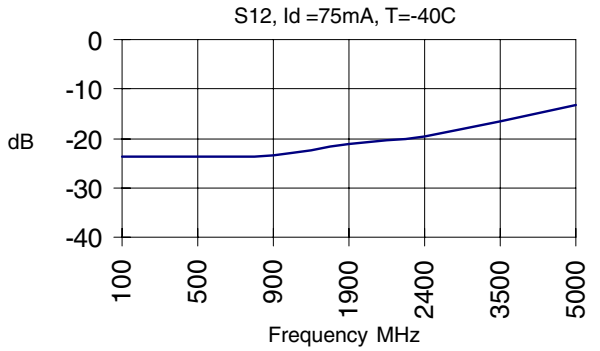
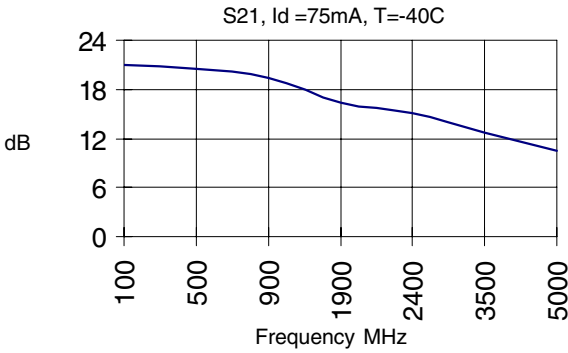
Application Schematic for +6V Operation at 1900 MHz



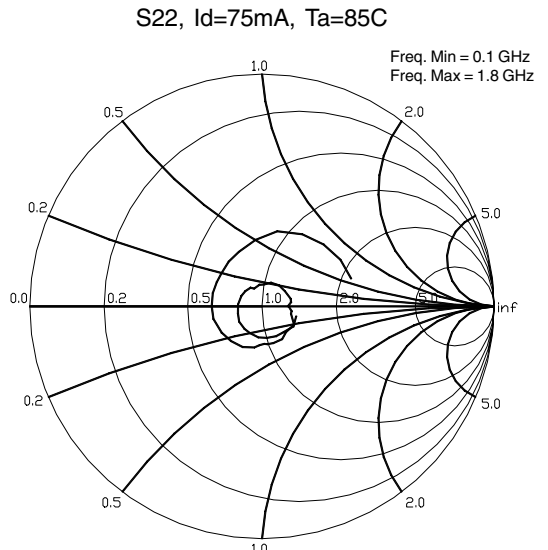
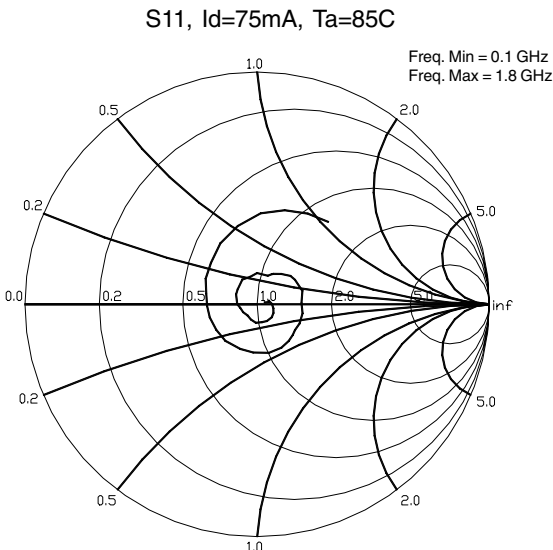
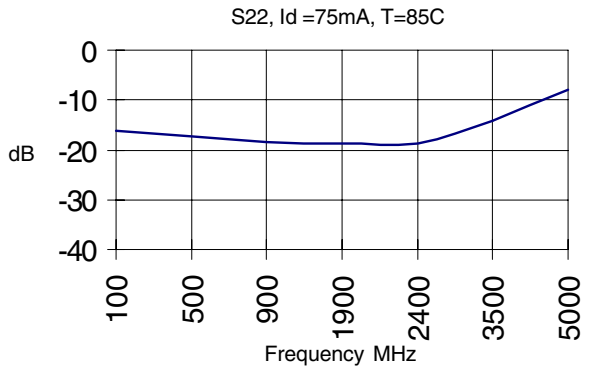
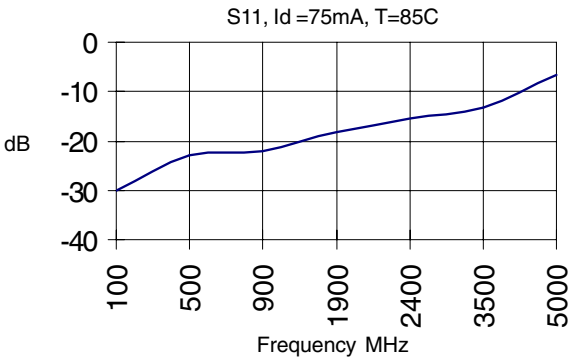
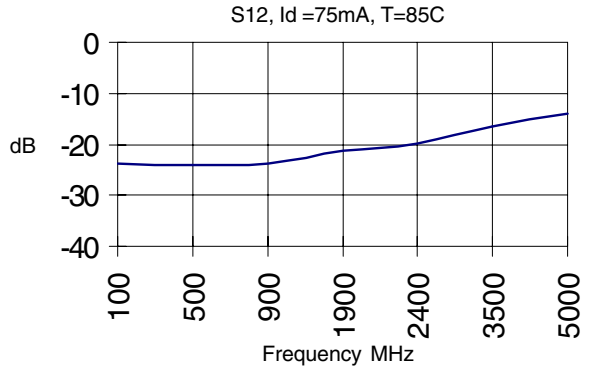
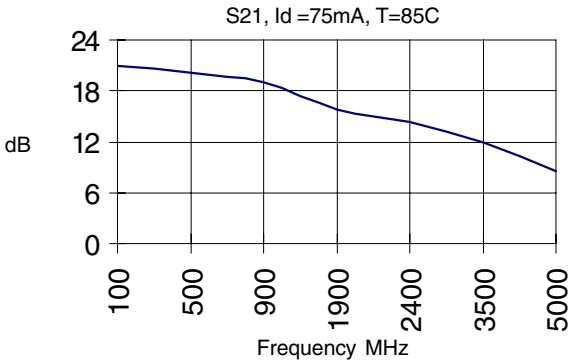
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