

Silicon NPN Power Transistors

2SD869

DESCRIPTION

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- With TO-3 package
- Built-in damper diode
- High voltage ,high speed
- Low collector saturation voltage

APPLICATIONS

- For color TV horizontal output applications

PINNING(see fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

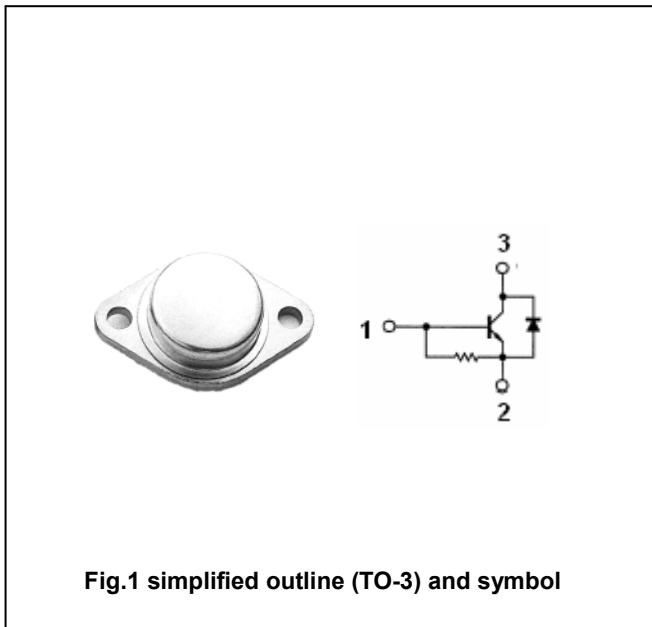


Fig.1 simplified outline (TO-3) and symbol

Absolute maximum ratings(Ta=□)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	1500	V
V _{CEO}	Collector-collector voltage	Open base	600	V
V _{EBO}	Emitter-base voltage	Open collector	5	V
I _C	Collector current		3.5	A
I _E	Emitter current		-3.5	A
P _T	Total power dissipation	T _C =25□	50	W
T _j	Junction temperature		150	□
T _{stg}	Storage temperature		-65~150	□

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CHARACTERISTICS

T_j=25°C unless otherwise specified

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SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =200mA; I _C =0	5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =3A; I _B =0.8A			8.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =3A; I _B =0.8A			1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =500V; I _E =0			10	μA
h _{FE}	DC current gain	I _C =0.5A; V _{CE} =5V	8			
V _F	Diode forward voltage	I _F =3.5A			2.0	V
f _T	Transition frequency	I _C =0.1A; V _{CE} =10V		3		MHz
C _{OB}	Collector output capacitance	I _E =0; V _{CB} =10V; f=1MHz		95		pF
t _f	Fall time	I _C =3A; I _{B1end} =0.8A			1.0	μs

PACKAGE OUTLINE

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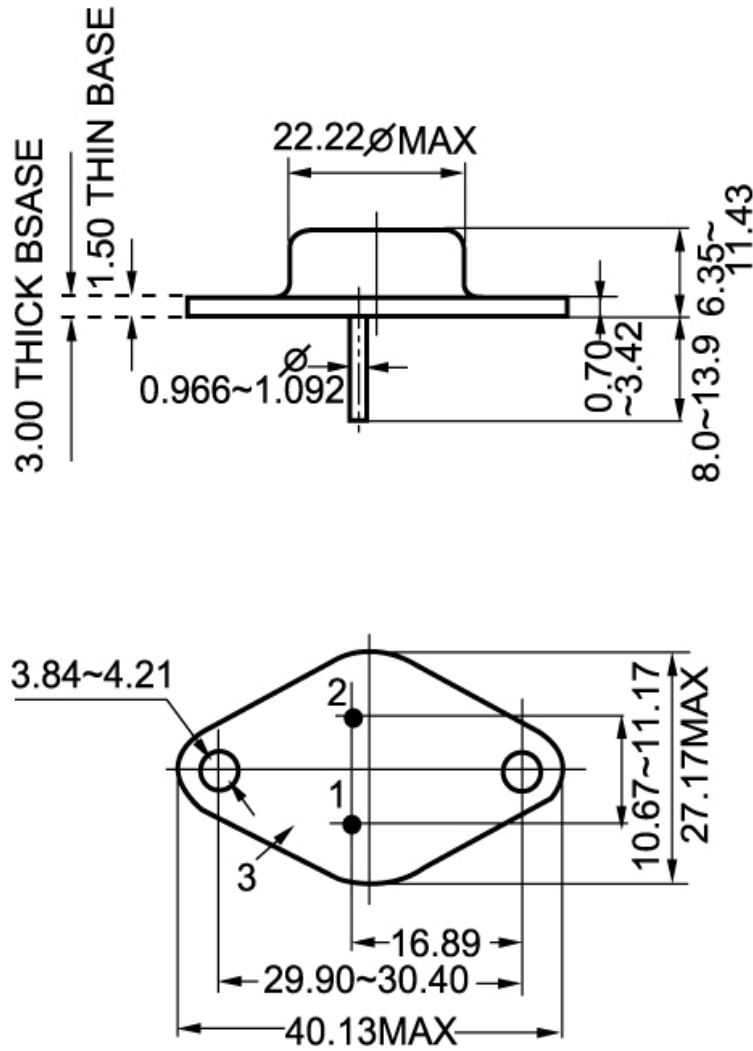


Fig.2 Outline dimensions