

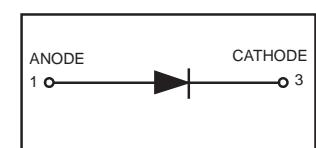
# SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

**LBAT750LT1G**
**FEATURES**

- Very Low Forward Voltage Drop
- High Conductance
- For Use in DC-DC Converter,PCMCIA, and Mobile Telecommunications Applications
- We declare that the material of product compliance with RoHS requirements.

**DEVICE MARKING AND ORDERING INFORMATION**

Device	Marking	Shipping
LBAT750LT1G	K77	3000/Tape&Reel
LBAT750LT3G	K77	10000/Tape&Reel


**MAXIMUM RATINGS** ( $T_J = 125^\circ\text{C}$  unless otherwise noted)

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	$V_{RRM}$		
Working Peak Reverse Voltage	$V_{RWM}$	40	Volts
DC Blocking Voltage	$V_R$		
RMS Reverse Voltage	$V_{R(\text{RMS})}$	28	Volts
Average Rectified Current(Note 1)	$I_0$	0.75	Adc
Non-Repetitive Peak Forward Current	$I_{FSM}$	5.5	Adc
Power Dissipation(Note 1)	$P_D$	350	mW
Typical Thermal Resistance, Junction to Ambient Air(Note 1)	$R_{\text{θJA}}$	286	$^\circ\text{C}/\text{W}$
Storage Temperature Range	$T_J, T_{\text{stg}}$	-55 to +150	$^\circ\text{C}$

**ELECTRICAL CHARACTERISTICS** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
Reverse Breakdown Voltage ( $I_R = 300 \mu\text{A}$ )(Note 2)	$V_{(\text{BR})R}$	40	45	—	Volts
Reverse Leakage ( $V_R = 30 \text{ V}$ ) (Note 2)	$I_R$	—	50	100	$\mu\text{Adc}$
Forward Voltage ( $I_F = 50 \text{ mAdc}$ ) (Note 2)	$V_F$	—	225	280	mVdc
Forward Voltage ( $I_F = 100 \text{ mAdc}$ ) (Note 2)	$V_F$	—	235	310	mVdc
Forward Voltage ( $I_F = 250 \text{ mAdc}$ ) (Note 2)	$V_F$	—	290	350	mVdc
Forward Voltage ( $I_F = 500 \text{ mAdc}$ ) (Note 2)	$V_F$	—	340	420	mVdc
Forward Voltage ( $I_F = 750 \text{ mAdc}$ ) (Note 2)	$V_F$	—	390	490	mVdc
Forward Voltage ( $I_F = 1000 \text{ mAdc}$ ) (Note 2)	$V_F$	—	420	540	mVdc
Forward Voltage ( $I_F = 1500 \text{ mAdc}$ ) (Note 2)	$V_F$	—	475	650	mVdc
Total Capacitance ( $V_R = 0 \text{ V}, f = 1.0 \text{ MHz}$ )	$C_T$	—	175	—	pF
Total Capacitance ( $V_R = 25 \text{ V}, f = 1.0 \text{ MHz}$ )	$C_T$	—	25	—	pF

Notes: 1.Part mounted on FR-4 PC board with recommended pad layout

2.Short duration test pulse used to minimize self-heating effect.

## LBAT750LT1G

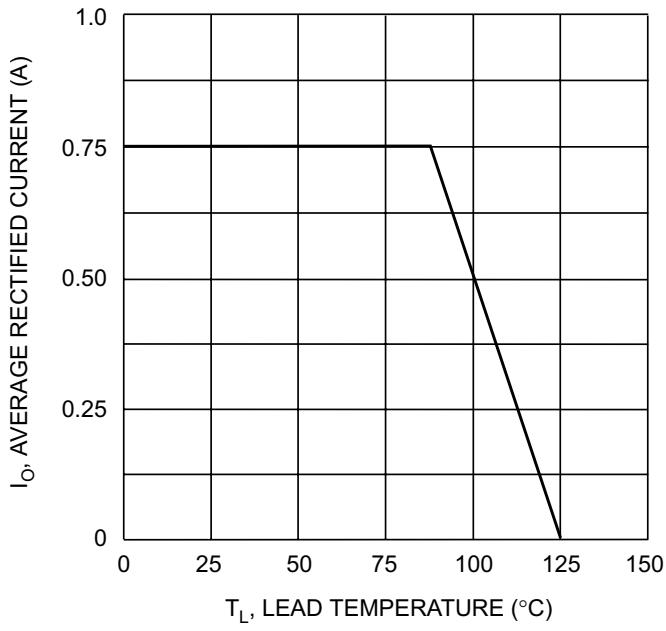


Fig. 1 Forward Current Derating Curve

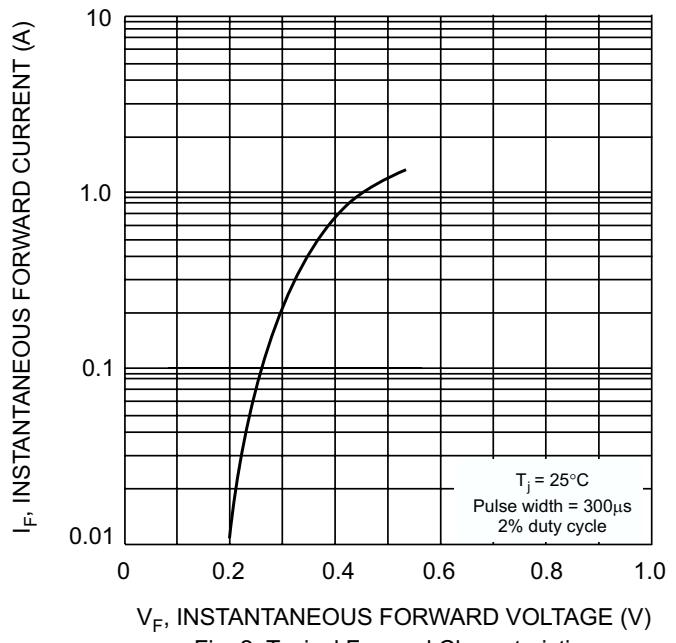


Fig. 2 Typical Forward Characteristics

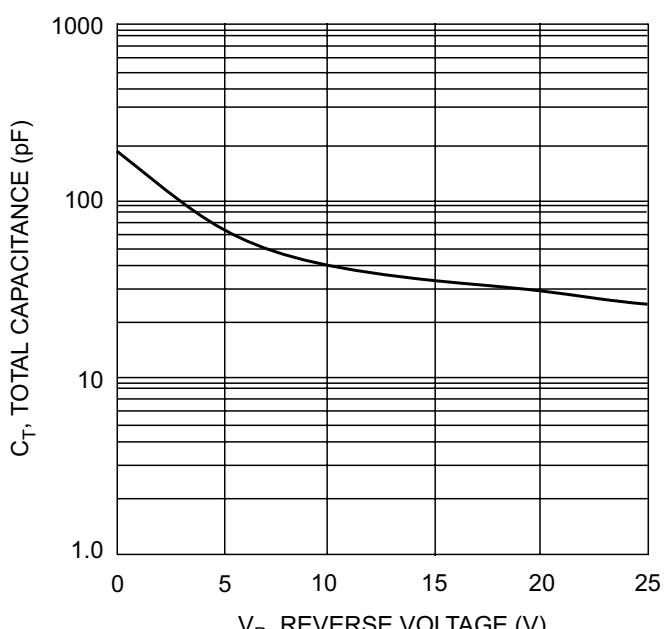


Fig. 3 Total Capacitance vs Reverse Voltage

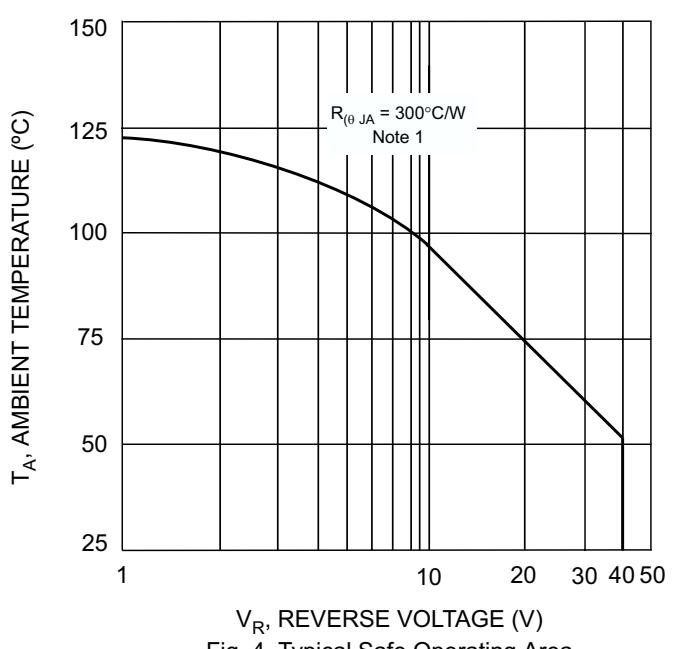
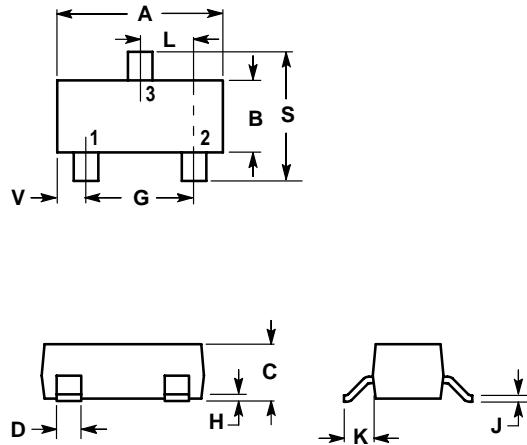


Fig. 4 Typical Safe Operating Area

Note: 1. Assumed application thermal conditions.  
 $R_{\theta JA}$  varies depending on application.

# LBAT750LT1G

## SOT-23



### NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.1102	0.1197	2.80	3.04
B	0.0472	0.0551	1.20	1.40
C	0.0350	0.0440	0.89	1.11
D	0.0150	0.0200	0.37	0.50
G	0.0701	0.0807	1.78	2.04
H	0.0005	0.0040	0.013	0.100
J	0.0034	0.0070	0.085	0.177
K	0.0140	0.0285	0.35	0.69
L	0.0350	0.0401	0.89	1.02
S	0.0830	0.1039	2.10	2.64
V	0.0177	0.0236	0.45	0.60

