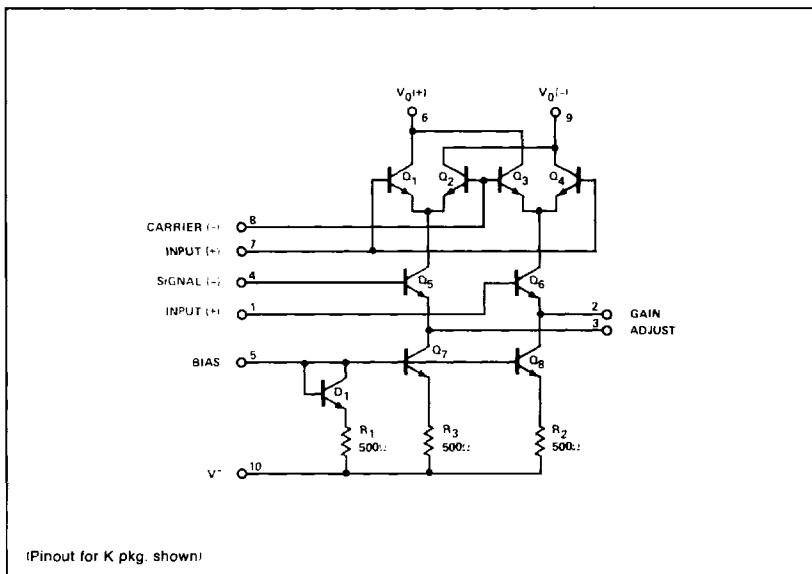


DESCRIPTION

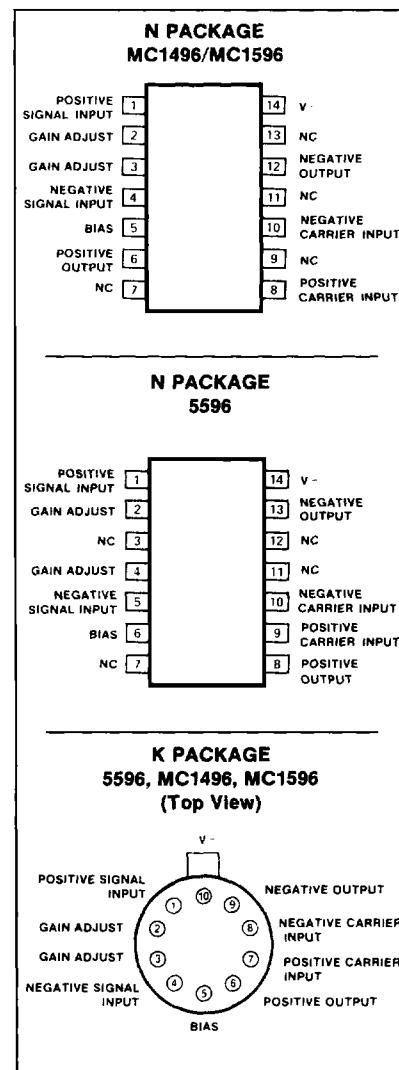
The 5596 is a monolithic Double-Balanced Modulator/Demodulator designed for use where the output voltage is a product of an input voltage (signal) and a switched function (carrier). The S5596 will operate over the full military temperature range of -55°C to +125°C. The N5596 is intended for applications within the range of 0°C to +70°C.

FEATURES

- Excellent carrier suppression
65dB typ @ 0.5MHz
50dB typ @ 10MHz
- Adjustable gain and signal handling
- Balanced inputs and outputs
- High common-mode rejection—85dB typ

EQUIVALENT SCHEMATIC**APPLICATIONS**

- Suppressed carrier and amplitude modulation
- Synchronous detection
- FM detection
- Phase detection
- Sampling
- Single sideband
- Frequency doubling

PIN CONFIGURATIONS**ABSOLUTE MAXIMUM RATINGS**

PARAMETER	RATING	UNIT
Applied voltage ^{1,2}	30	V
Differential input signal (V ₇ -V ₈)	±5.0	V
Differential input signal (V ₄ -V ₁)	(5 ± 15 R _e)	V
Input signal (V ₂ -V ₁ , V ₃ -V ₄)	5.0	V
Bias current (I ₅)	10	mA
Power dissipation (pkg. limitation)		
K package	680	mW
Derate above 25°C	5.4	mW/°C
A package (TO-116)	900	mW
Derate above 25°C	7.2	mW/°C
Operating temperature range	-55 to +125	°C
Storage temperature range	-65 to +150	°C

NOTES

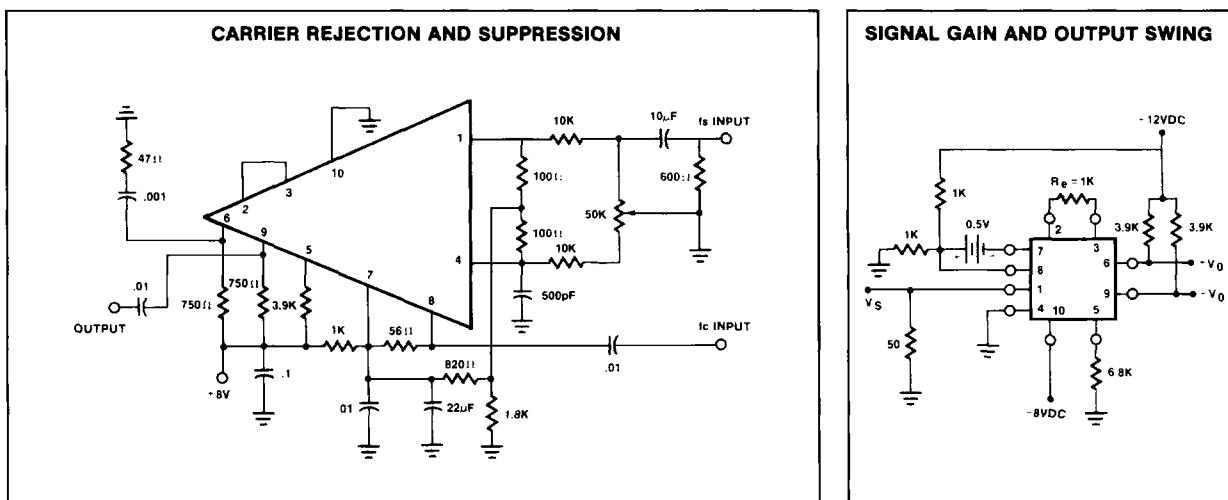
1. Voltage applied between pins 6-7, 8-1, 9-7, 9-8, 7-4, 7-1, 8-4, 6-8, 2-5, 3-5.
2. Pin number references pertain to K package pinout only.

DC ELECTRICAL CHARACTERISTICS $V^+ = +12\text{Vdc}$, $V^- = -9.0\text{Vdc}$, $I_5 = 1.0\text{mAdc}$, $R_L = 3.9\text{k}\Omega$, $R_E = 1.0\text{k}\Omega$, $T_A = 25^\circ\text{C}$ unless otherwise specified.

PARAMETER	TEST CONDITIONS	MC1496			MC1496/5596			UNIT
		Min	Typ	Max	Min	Typ	Max	
R_{ip} C_{ip}	Single-ended input impedance Parallel input resistance Parallel input capacitance							$\text{k}\Omega$ pF
R_{op} C_{op}	Single-ended output impedance Parallel output resistance Parallel output capacitance							$\text{k}\Omega$ pF
I_{bS} I_{bC}	Input bias current $I_{bS} = \frac{I_1 + I_4}{2}$ $I_{bC} = \frac{I_7 + I_8}{2}$				12	25	12	30
I_{ioS} I_{ioC}	Input offset current $I_{ioS} = I_1 - I_4$ $I_{ioC} = I_7 - I_8$				0.7	5.0	0.7	7.0
T_{clo} I_{oo}	Average temperature coefficient of input offset current Output offset current $I_{oo} = I_6 - I_9$				2.0		2.0	$\text{nA}/^\circ\text{C}$
T_{cloo} V_o	Average temperature coefficient of output offset current Common-mode quiescent Output voltage (Pin 6 or Pin 9)				14	50	15	80
I_{D+} I_{D-}	Power supply current $I_{D+} = I_6 + I_9$ $I_{D-} = I_{10}$				2.0	3.0	2.0	4.0
P_D	DC power dissipation				3.0	4.0	3.0	5.0
					33		33	
								mW

NOTE

Pin number references pertain to K package pinout only.



BALANCED MODULATOR/DEMODULATOR

MC1496/1-36/11-26

MC1496/MC1596/N5596-K,N

AC ELECTRICAL CHARACTERISTICS

$V^+ = +12Vdc$, $V^- = -9.0Vdc$, $I_5 = 1.0mA$, $R_L = 3.9k\Omega$, $R_E = 1.0k\Omega$, $T_A = +25^\circ C$ unless otherwise specified.

PARAMETER	TEST CONDITIONS	MC1596			MC1496/5596			UNIT	
		Min	Typ	Max	Min	Typ	Max		
VCFT	Carrier feedthrough	V _c = 60mVrms sinewave and offset adjusted to zero f _c = 1.0kHz f _c = 10MHz V _c = 300mVp-p squarewave: Offset adjusted to zero f _c = 1.0kHz Offset not adjusted f _c = 1.0kHz		40 140			40 140		µVrms
Vcs	Carrier suppressions	f _s = 10kHz, 300mVrms sinewave f _c = 500kHz, 60mVrms sinewave f _c = 10MHz, 60mVrms sinewave	50	65 50		40	65 50		dB
BW _{3dB}	Transadmittance bandwidth (Magnitude) (R _L = 50Ω)	Carrier input port, V _c = 60mVrms sinewave f _s ≈ 1.0kHz, 300mVrms sinewave Signal input port, V _s = 300mVrms sinewave V _c = 0.5Vdc		300 80			300 80		MHz
AVs	Signal gain	V _s = 100mVrms; f = 1.0kHz V _c = 0.5Vdc	2.5	3.5		2.5	3.5		V/V
CMV ACM	Common-mode input swing Common-mode gain	Signal port, f _s = 1.0kHz Signal port, f _s = 1.0kHz V _c = 0.5Vdc		5.0 -85			5.0 -85		Vp-p dB
DV _{OUT}	Differential output voltage swing capability			8.0			8.0		Vp-p

NOTE

Pin number references pertain to K package pinout only.

CARRIER REJECTION AND SUPPRESSION

