

<u>EUM6862/A/B</u>

24V PWM Fan Motor Driver

DESCRIPTION

EUM6862/A/B is a fan motor driver for the single coil brushless DC motor. With its high efficient direct PWM control mode, EUM6862/A/B controls the speed of brushless DC motor with a built in HALL IC interface. EUM6862/A/B is suitable to drive variable speed motors for personal computer's power supply radiation fans and CPU coolers.

EUM6862/A/B integrates PWM fan speed control, minimum speed mode, soft start, fan tachometer, lock protection, auto restart and Hall Bias functions. PWM mode controls fan speed in low noise and low vibration ways by adjusting RCTL voltage. EUM6862/A/B can set minimum fan speed by presetting RMIN voltage. With soft start function, EUM6862/A/B can drive motor from slow speed to fast speed in a settable time by setting the external capacitor between SS and GND. If a fan motor is stalled by the external force or obstacles, overdrive current may incur coil overheat and burning. To prevent motor from overheating, the lock protection circuit shuts down the internal power devices for a few seconds after the motor lock is detected. Then the auto restart circuit resumes to power up the internal power devices. If the lock persists, EUM6862/A/B shuts off power devices for a few seconds. The lock protection delay time is externally programmable by a capacitor. EUM6862 has built in motor rotation speed feedback (FG) output, motor rotation detection (RD) output and Hall sensor bias output.

FEATURES

- Built in 24V Full-wave Motor Driver for Fan Motor
- Built in Kickback Voltage Protection, No Zener Diode Need
- External Programmable Soft Start and Soft Restart Function
- Wide Input Range 3V~28V
- PWM Fan Speed Control
- Programmable Minimum Fan Speed
- Built in Soft Switching Function
- Built in Hall Bias
- Lock Protection and Auto Restart
- Fan Rotation Speed Feedback FG Output (EUM6862/B)
- Fan Rotation Detection RD Output (EUM6862/A)
- Thermal Shutdown Protection
- Available in TSSOP-16 and TDFN-14 Packages
- RoHS Compliant and 100% Lead (Pb)-Free Halogen-Free

APPLICATIONS

 PC/Server Fan Motor, CPU Coolers and Printer Cooling Fan

Application Circuit

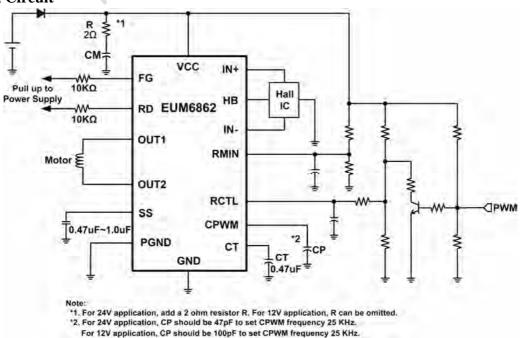


Figure 1. TSSOP -16 24V and 12V Typical Application Circuit



Application Circuit (continued)

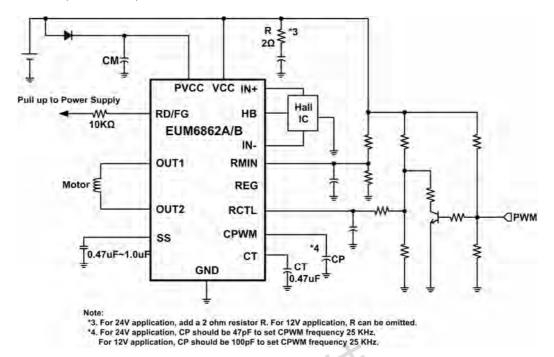
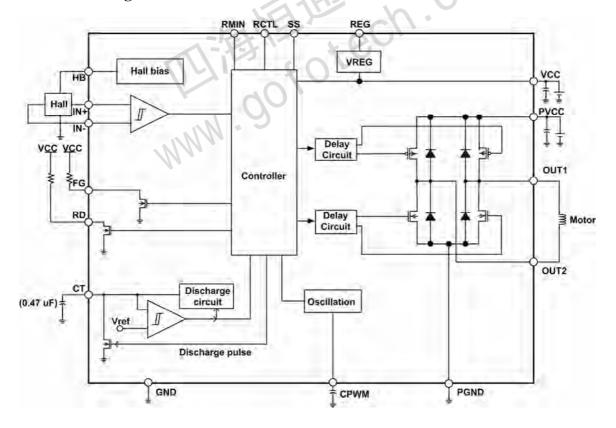


Figure 2. TDFN-14 24V and 12V Typical Application Circuit

Functional Block Diagram



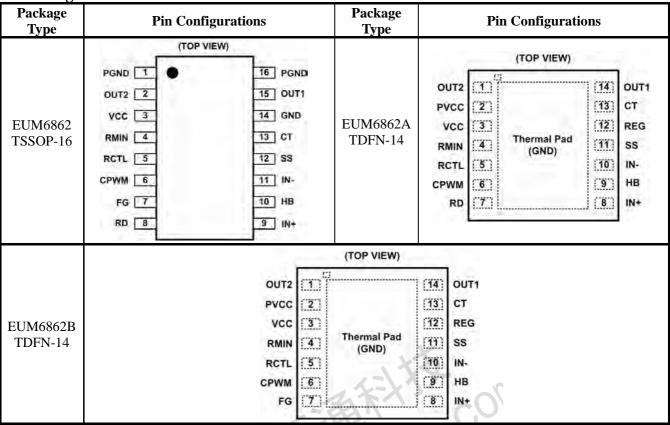
TSSOP-16 Short together VCC and PVCC, Separate PGND and GND TDFN-14 Separate VCC and PVCC, Short together PGND and GND

Figure 3. Functional Block Diagram





Pin Configurations



Pin Description

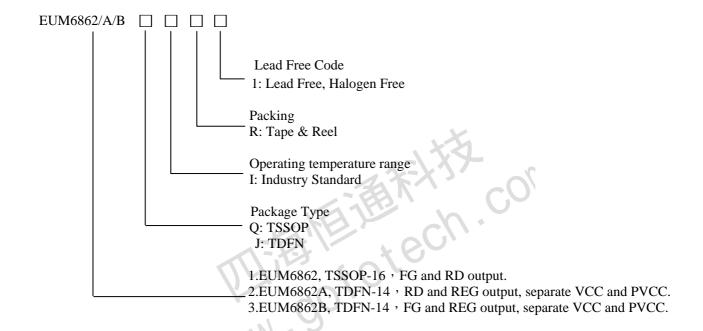
PIN	EUM6862	EUM6862A	EUM6862B	DESCRIPTION					
PGND	1,16	-), ·	Power device ground					
OUT2	2	1	1	Power device output terminal 2. Connect OUT2 to one side of the motor					
VCC	3	($M : \mathcal{I}$	Power supply of internal control circuitry and power devices					
VCC	-	3	3	Power supply of internal control circuitry					
RMIN	4	4	4	Motor minimum rotation speed control					
RCTL	5	5	5	Motor rotation speed control					
CPWM	6	6	6	External capacitor connection input for PWM OSC					
FG	7	-	7	Rotation speed feedback output					
RD	8	7	-	Rotation detection output					
IN+	9	8	8	Hall sensor positive input					
НВ	10	9	9	Hall sensor bias output					
IN-	11	10	10	Hall sensor negative input					
SS	12	11	11	Soft start time setting terminal					
CT	13	13	13	Lock protection time setup input					
	14	-	-	Analog control circuit ground					
GND	-	Thermal Pad	Thermal Pad	IC ground					
OUT1	15	14	14	Power device output terminal 1. Connect OUT1 to the other side of the motor.					
PVCC	-	2	2	Power supply of power devices					
REG	-	12	12	6V regulator					





Ordering Information

Order Number	Package Type	Marking	Operating Temperature Range
EUM6862QIR1	TSSOP-16		-30°C to +90°C
EUM6862AJIR1	TDFN-14	xxxxx M6862	-30°C to +90°C
EUM6862BJIR1	TDFN-14	xxxxx 6862B	-30°C to +90°C



Power Dissipation

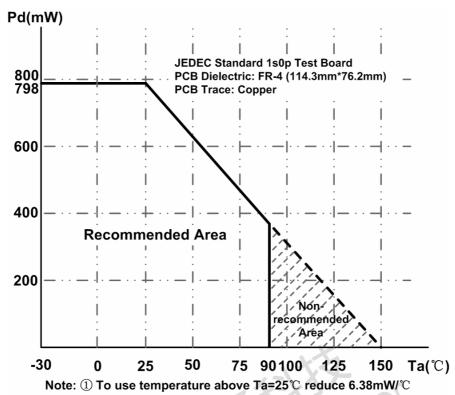


Figure 4. TSSOP-16 Package

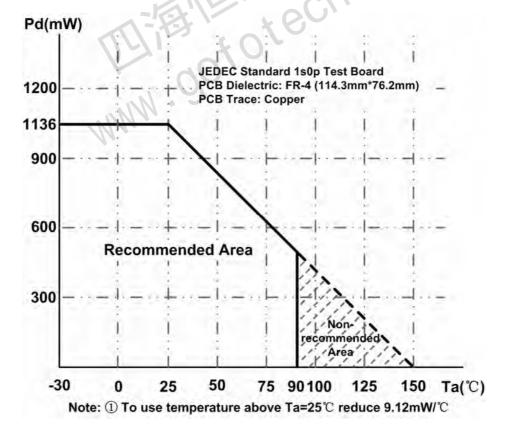


Figure 5. TDFN-14 Package



0V to 6V

Absolute Maximum Ratings (1)

■ VCC, RD, FG (3), OUT1, OUT2 to GND
■ RMIN, RCTL to GND
■ CPWM, SS, CT, IN+, IN-, HB to GND
■ PGND to GND
■ IOUT1, IOUT2 1.0A
■ IHB, IFG, IRD 10mA
■ Maximum Junction Temperature+150°C
■ Lead Temperature (Soldering, 10sec.)+300°C
Package Thermal Resistance θ _{JA} (TSSOP-16) 157°C /W
Package Thermal Resistance θ _{JA} (TDFN-14) 110°C /W
Power Dissipation P_D @ T_A =+25°C (TSSOP-16) 0.8W
Power Dissipation P_D @ T_A =+25°C (TDFN-14) 1.14W
■ Storage Temperature
■ ESD Ratings
Human Body Mode 2kV
■ Thermal Shut Down 180°C
■ Thermal Shut Down Hysteresis 35°C
Recommended Operating Conditions (2)
■ Supply Voltage VCC and PVCC 3V to 28V

Hall Input Voltage Range IN+, IN- ----- 0.2V to 3V

Note (1): Stress beyond those listed under "Absolute Maximum Ratings" may damage the device. Note (2): The device is not guaranteed to function outside the recommended operating conditions.

Note (3): FG is open drain output, it can bear 30V voltage when FG is off (IN+=H, IN-=L).

Electrical Characteristics

Specifications in standard type face are for $T_A=+25$ °C, and those with **boldface type** apply over the full operating temperature range T_A =-30°C ~+90°C. VCC=12V~24V unless otherwise specified.

Threshold Input Voltage RCTL, RMIN -----

Symbols	Parameters	Conditions	EUM6862/A/B			Unit	
Symbols	T at affecters	Conditions	Conditions			Max.	Omt
ICC1		Rotation mode	VCC=12V	-	3.6	5.5	mA
ICC2	Onarating augment	Lock protection mode	VCC=12V	-	1.8	3.2	
ICC3	Operating current	Rotation mode	VCC 24V	-	4.7	7	
ICC4		Lock protection mode	VCC=24V	-	2.6	4.2	
Hall Input a	Hall Input and Hall Bias						
VHN	Hall input sensitivity	ensitivity Zero to peak (Offset & Hysteresis included)		-	10	15	mV
VHB	HB Output Voltage	IHB=5mA	1.1	1.2	1.3	V	
Output							
VOL	Output lower side saturation	utput lower side saturation Io=200mA				0.13	v
VOH	Output upper side saturation	Io=200mA	-	0.20	0.26	v	
Soft Start B	lock					•	
ISS	SS pin discharge current	VSS = 3.6V	0.37	0.50	0.64	μΑ	





Electrical Characteristics (continued)

Specifications in standard type face are for T_A =+25°C, and those with **boldface type** apply over the full operating temperature range T_A =-30°C ~+90°C. VCC=12V~24V unless otherwise specified.

Symbols	Parameters	Conditions	EUM6862/A/B			Unit	
Symbols	rarameters	Conditions	•	Min.	Тур.	Max.	Omt
PWM Block	xs .						
FPWM1	CPWM OSC frequency			21	25	30	KHz
VCRL1	CPWM low level voltage	CP = 100pF	VCC=12V	1.05	1.15	1.25	V
VCRH1	CPWM high level voltage			1.95	2.05	2.15	V
FPWM2	CPWM OSC frequency			21	25	30	KHz
VCRL2	CPWM low level voltage	CP = 47pF	VCC=24V	2.2	2.3	2.4	V
VCRH2	CPWM high level voltage			3.95	4.1	4.25	V
Lock Protec	tion Block						
ICT1	CT charge current	VCT = 0V		1.57	2	2.37	μΑ
ICT2	CT discharge current	VCT = 4.2V	0.16	0.20	0.25	μΑ	
RCT	Ratio of charge current to discharge current	RCT = ICT1 / ICT2	8	10	11	-	
VCTH	CT high level voltage		3.62	3.90	4.22	V	
VCTL	CT low level voltage	1.45	1.78	1.80	1.82	V	
FG & RD Si	ignal Output	-1891	, (,O'			
VFG	FG & RD pin low voltage	IFG = 5mA	2/V ·	-	0.1	-	V
IFGL	FG & RD pin leak current	VFG = 24V	O,	-	-	5	μΑ
Regulator O	Output	in the					
VREG	Regulator Output Voltage	IREG=3mA		5.8	6.0	6.2	V
	www	0					L
	MAN						



Application Notes

Hall Sensor Connection

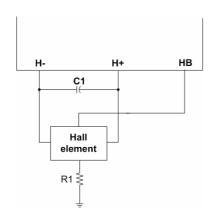


Figure 6. Hall Sensor Inputs

Hall signal input terminals (H+ · H-)

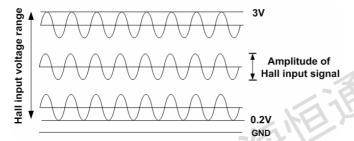


Figure 7. Hall Sensor Input Voltage Range

Set Hall sensor input signal range 0.2V to 3V by adjusting the Hall input level setting resistor R1. In the case of long board wiring pattern from hall element to hall signal input terminal, connect a capacitor between IN+ and IN- to avoid noise. The amplitude of Hall input signal is recommended to be 60mV or higher due to the Hall input amplifier 20mV hysteresis. The Hall bias is 1.2V.

VCC and PVCC Bypass

In TSSOP-16, Connect a ceramic capacitor CM $1\mu F$ or more between VCC and GND to absorb kick back voltage resulting from the high side re-circulation current;

In TDFN-14, Connect a ceramic capacitor $0.1\mu F$ or more between VCC and GND. And connect a ceramic capacitor CM $1\mu F$ or more between PVCC and GND to absorb kick back voltage resulting from the high side re-circulation current.

GND and PGND Line

In TSSOP-16, GND is connected to internal analog control circuits, and PGND is connected to power devices. Connect GND to PGND at only one place on the PCB board.

PWM Oscillator Frequency

The PWM OSC frequency is programmed by the CP capacitor.

- (1-1) At VCC=12V, a 100pF CP sets PWM frequency 25KHz and the PWM OSC ramp CPWM swings between 1.15V to 2.05V.
- (1-2) At VCC=24V, a 47pF CP sets PWM frequency 25KHz and the PWM OSC ramp CPWM swings between 2.3V to 4.1V.

PWM Control Speed Mode

PWM control mode works by comparing the voltage of RCTL and CPWM. When RCTL is low, one side upper power device and the other side lower side power device are turned on to charge the motor coil. When RCTL is high, the upper side power device is turned off. The motor coil current is re-circulated between lower side power devices. The lower RCTL is, the bigger the output duty is. Big coil current makes motor run fast. FG output feedbacks motor rotational speed. The motor coil is charged all the time and motor speed becomes full speed when RCTL voltage is lower than 2.3V at VCC=24V.

Soft Start Time

Connect a capacitor between SS and GND to set soft start time. EUM6862/A/B drives a motor from slow speed to fast speed during soft start time which is set by the external capacitor between SS and GND. Floating SS pin if soft start function is not used. If motor start current is too big, please increase capacitor between SS and GND to make start time longer and start current smaller. If motor start current is too small to run, please decrease capacitor between SS and GND to make start time shorter and start current bigger.

Minimum Speed Mode

Minimum speed of motor is set by the RMIN voltage. When RCTL voltage is higher than RMIN voltage, the lowest duty is set by comparing RMIN and PWM oscillator ramp CPWM voltage. When minimum speed mode function is not used, connect RMIN to RCTL. If RMIN pin is connected to RCTL and the RCTL/RMIN voltage is above 4.1V at VCC=24V, EUM6862/A/B PWM duty cycle is 0% and motor stops.

Lock Protection and Auto Restart

When the motor is locked, the EUM6862/A/B outputs will be disabled by the lock protection function. After a few seconds, the auto restart circuit will restart the motor. If the motor lockup persists, the lock protection will keep EUM6862/A/B outputs off until the lock removes. Rotation detection output RD pin is open-drain output, and RD is internally pulled down during rotation mode. When motor lock is detected, RD pin becomes high impedance. See Figure 8.



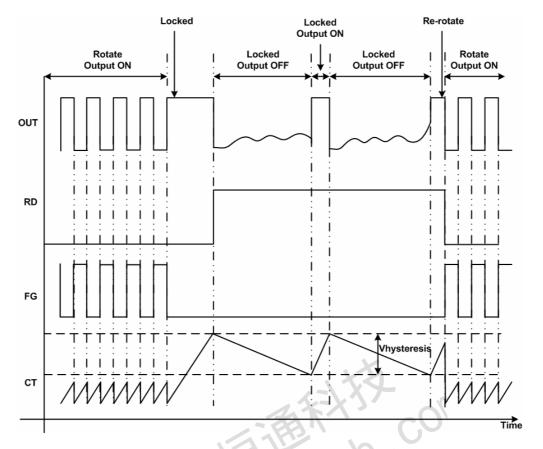


Figure 8. Lock Protection and Auto Restart

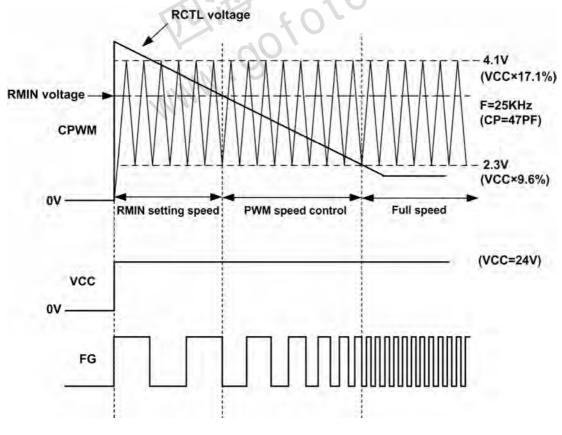


Figure 9. Control Time Chart





EUM6862/A/B Operation Truth Table

(CPWM-H=CPWM > RCTL, CPWM-L=CPWM < RCTL)

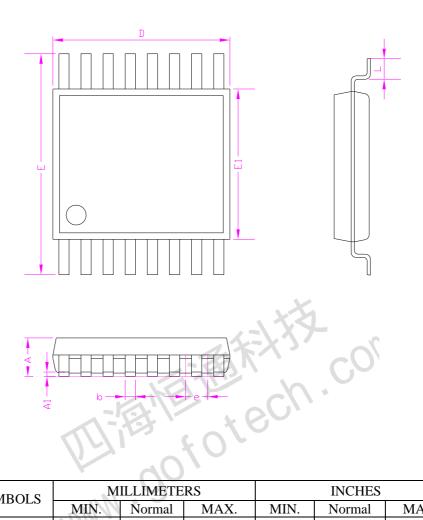
CT	CPWM	IN+	IN-	OUT1	OUT2	FG	RD	Mode	
L	Н	L	Н	Н	L	L		Rotation (Drive)	
		Н	L	L	Н	OFF	ON		
	L	L	Н	L	L	L		Rotation (Recirculation)	
		Н	L	L	L	OFF			
Н	-	L	Н	L	L	L	OFF	Loak Protection	
		Н	L	L	L	OFF	OFF	Lock Protection	





Packaging Information

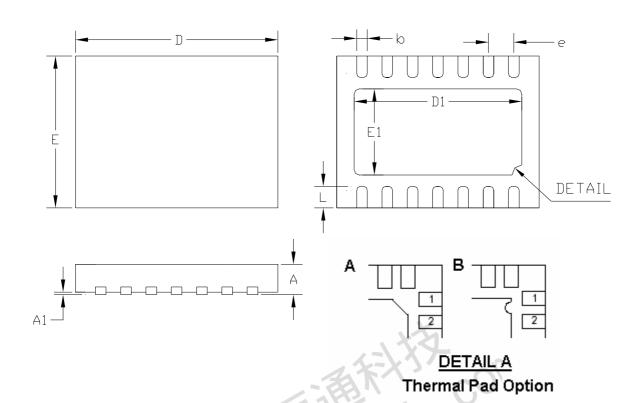
TSSOP-16



SYMBOLS	M	ILLIMETE	RS	INCHES			
STWIDOLS	MIN.	Normal	MAX.	MIN.	Normal	MAX.	
A	MJ_A	1	1.20	-	-	0.047	
A1	0.00	1	0.15	0.000	-	0.006	
b	0.15	1	0.30	0.006	-	0.012	
E1	4.25	4.40	4.55	0.167	0.173	0.179	
D	4.85	5.00	5.15	0.191	0.197	0.202	
Е	6.20	6.40	6.60	0.244	0.252	0.260	
e	0.65				0.026		
L	0.45	0.60	0.75	0.018	0.024	0.030	



TDFN-14



SYMBOLS	M	ILLIMETER	RS	INCHES			
STMBOLS	MIN.	Normal	MAX.	MIN.	Normal	MAX.	
A	0.70	0.75	0.80	0.028	0.030	0.032	
A1	0.00	0.02	0.05	0.000	0.001	0.002	
b	0.15	0.25	0.35	0.006	0.010	0.014	
D	3.90	4.00	4.10	0.154	0.157	0.161	
D1	2.70	3.00	3.30	0.106	0.118	0.129	
Е	2.90	3.00	3.10	0.114	0.118	0.122	
E1	1.50	1.60	1.70	0.059	0.063	0.067	
e	0.50			0.020			
L	0.30	0.40	0.50	0.012	0.016	0.020	