SPECIFICATION

FOR

APPROVAL

(♦) Preliminary Specification

( ) Final Specification

Title	13.3” XGA TFT LCD

BUYER	
MODEL	

SUPPLIER	LG LCD Inc.
MODEL	LP133X7-A2

SIGNATURE	DATE
/	
/	
/	

APPROVED BY	DATE
S.H.Kang / G.Manager	
REVIEWED BY S.C.Yoon / Manager B.H.Koo / Manager	
PREPARED BY H.S.Song / Engineer H.J.Kim / Engineer	

Please return 1 copy for your confirmation with your signature and comments.

Product Engineering Dept.
LG LCD Inc.

CONTENTS

RECORDS OF REVISIONS

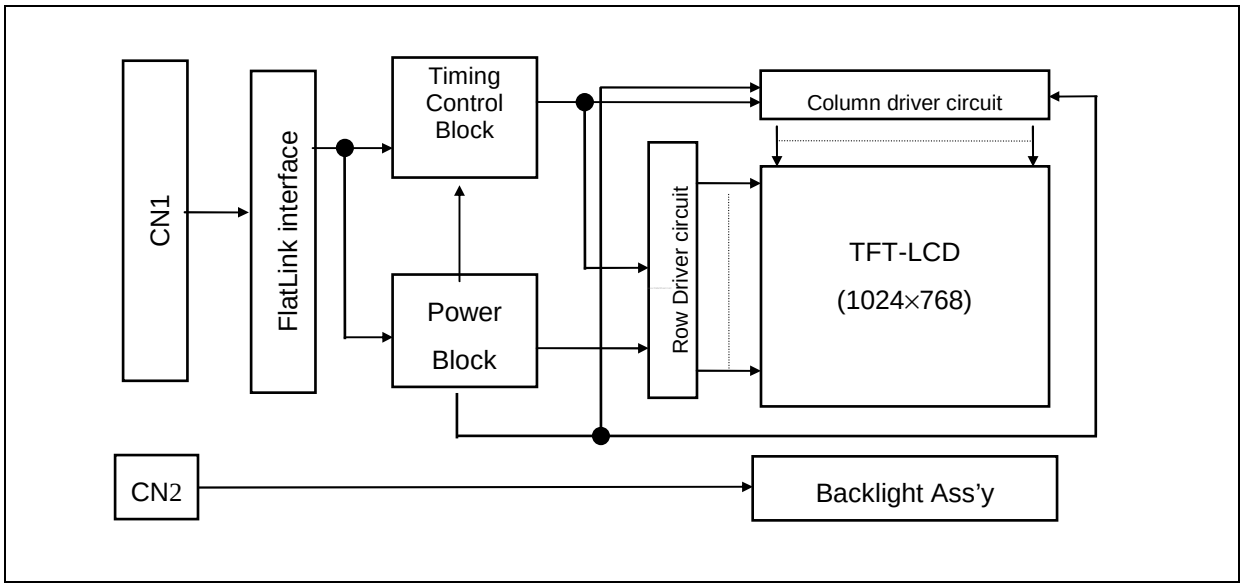
Version No	Date	Page	DESCRIPTION
1.0	JUN. 7, 1999	-	First Draft

1. General Description

The LP133X7-A2 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Tube(CCFT) back light system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has a 13.3 inch diagonally measured active display area with XGA resolution(768 vertical by 1024 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP133X7-A2 has been designed to apply the interface method that enables low power, high speed low EMI. Flat Link must be used as a LVDS(Low Voltage Differential Signaling) chip.

The LP133X7-A2 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP133X7-A2 characteristics provide an excellent flat panel display for office automation products such as Notebook PC.



General Features

The following are general feature of the model LP133X7-A2 LCD;

Active screen size	13.3 inches(33.78cm) diagonal
Outline dimensions	284(H) × 214.5(V) × 5.5(D) mm (typ)
Pixel pitch	0.264 mm × 0.264 mm
Pixel format	1024 horiz. By 768 vert. pixels RGB stripe arrangement
Color depth	6-bit, 262,144 colors
Luminance,White	130 cd/m^2 (typ)
Power Consumption	Total 4.2Watt(typ)
Weight	480g (typ)
Display operating mode	transmissive mode, normally white
Surface treatments	hard coating(3H), anti-glare treatment of the front polarizer

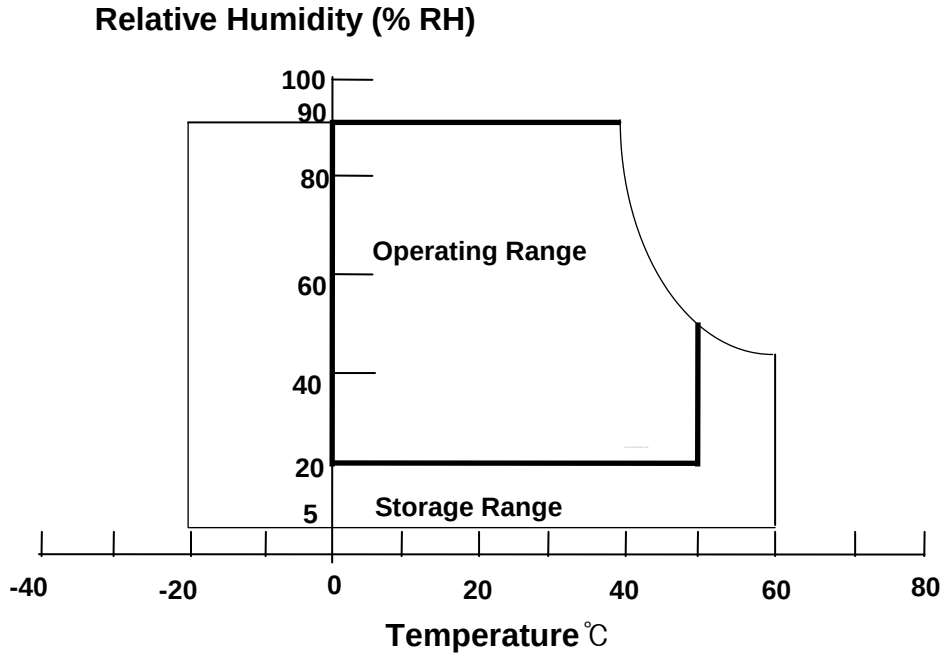
2. Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1 ABSOLUTE MAXIMUM RATINGS

Parameter	symbol	Values		Units	Notes
		Min.	Max.		
Power Input Voltage	V_{CC}	-0.3	+3.6	Vdc	at 25℃
Operating Temperature	T_{OP}	0	+50	℃	1
Storage Temperature	T_{ST}	-20	+60	℃	1

Note: 1. Temperature and relative humidity range are shown in the figure below.



3. Electrical Specifications

3-1. Electrical Characteristics

The LP133X7-A2 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Table 2 ELECTRICAL CHARACTERISTICS:

Parameter	Symbol	Values			Units	Notes
		Min.	Typ.	Max.		
MODULE:						
Power Supply Input Voltage	V_{CC}	3.0	3.3	3.6	Vdc	
Power Supply Input Current	I_{CC}	0.185	0.215	0.245	A	1
Differential Impedance	Zm	90	100	110	ohm	2
Power Consumption	P_c	0.61	0.71	0.81	Watts	1
Rush current	I_{RUSH}		1.5	1.8	A	3
LAMP						
Operating Voltage	V_{BL}	560	630	765	V_{RMS}	4
Operating Current	I_{BL}	3.0	5.5	7.0	mA	
Established Starting Voltage						5
at 25℃				1050	V_{RMS}	
at 0℃		-	-	1260	V_{RMS}	
Operating Frequency	f_{BL}	40	55	70	kHz	
Power Consumption	P_{BL}	-	3.47	3.92	Watts	6
Life Time		10,000	15,000		Hrs	7

Notes: 1. The current draw and power consumption specified is for 3.3 Vdc at 25℃ and fv at 60Hz.(at 64 Gray pattern displayed)

2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
3. The duration of rush current is about 20ms.
4. The variance of the voltage is ±10%.
5. The transformer output voltage in the inverter must be high considering to the loss of the ballast capacitor in the inverter.
6. The lamp power consumption shown above does not include loss of external inverter.
7. The life time is determined as the time at which brightness of lamp is 50% compare to that of initial value at the typical lamp current

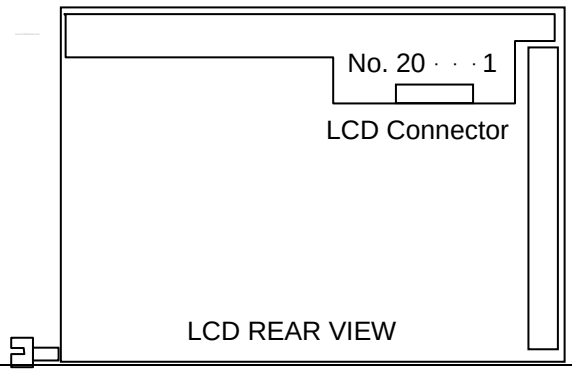
3-2. Interface Connections

Interface chip must be used FlatLink ,part No. THC63LVDM63A(Transmitter) , KZ4E038C12CFP(Receiver) made by THine Microsystems,Inc.

This LCD employs two interface connections, a 20 pin connector is used for the module electronics and two connectors is used for the integral backlight system.

The electronics interface connector is a model DF19KR-20P-1H manufactured by HIROSE. The pin configuration for the connector is shown in the table below.

Table 3 MODULE CONNECTOR PIN CONFIGURATION (LVDS)

Pin	Symbol	Description	Notes
1	VCC	Power (3.3V)	1. Interface chips 1.1 LCD : KZ4E038C12CFP (THC63LVDF64A Core) 1.2 System : THC63LVDM63A 48TSSOP * Pin to Pin compatible with TI LVDS 2. Connector 2.1 LCD : DF19KR-20P-1H(HIROSE) or GT100-20-LS-SMT-R(LG CABLE) → Pin to Pin compatible with HIROSE 2.2 Mating -. Wire type : DF19G-20S-1C(HIROSE) -. FPC type : DF19-20S-1F(HIROSE) 2.3 Connector pin arrangement 
2	VCC	Power (3.3V)	
3	GND	Ground	
4	GND	Ground	
5	A0M	Difference Signal	
6	A0P	Difference Signal	
7	GND	Ground	
8	A1M	Difference Signal	
9	A1P	Difference Signal	
10	GND	Ground	
11	A2M	Difference Signal	
12	A2P	Difference Signal	
13	GND	Ground	
14	CLKM	Difference Signal	
15	CLKP	Difference Signal	
16	GND	Ground	
17	NC	No Connection	
18	NC	No Connection	
19	GND	Ground	
20	GND	Ground	

The backlight interface connector is a model BHSR-02VS-1, manufactured by JST. The mating connector part number is SM02B-BHSS-1 or equivalent. The pin configuration for the connector is shown in the table below.

Table 4 BACKLIGHT CONNECTOR PIN CONFIGURATION

Pin	Symbol	Description	Notes
1	HV	Lamp power input	1
2	LV	Ground	2

Notes: 1. The input power terminal is colored pink. Ground pin color is white.
2. The lamp ground should be common with GND.

Table 5 REQUIRED SIGNAL ASSIGNMENT FOR FlatLink

Pin #	Pin Name	Require Signals	Pin #	Pin Name	Require Signals
1	D4	R4	48	D3	R3
2	Vcc	Vcc	47	D2	R2
3	D5	R5	46	GND	GND
4	D6	G0	45	D1	R1
5	GND	GND	44	D0	R0
6	D7	G1	43	NC	NC
7	D8	G2	42	LVDS GND	LVDS GND
8	Vcc	Vcc	41	Y0M	A0M
9	D9	G3	40	Y0P	A0P
10	D10	G4	39	Y1M	A1M
11	GND	GND	38	Y1P	A1P
12	D11	G5	37	LVDS Vcc	LVDS Vcc
13	D12	B0	36	LVDS GND	LVDS GND
14	NC	NC	35	Y2M	A2M
15	D13	B1	34	Y2P	A2P
16	D14	B2	33	CLKOUTM	CLKM
17	GND	GND	32	CLKOUTP	CLKP
18	D15	B3	31	LVDS GND	LVDS GND
19	D16	B4	30	PLLGND	PLL GND
20	D17	B5	29	PLLVcc	PLL Vcc
21	Vcc	Vcc	28	PLLGND	PLL GND
22	D18	HSYNC	27	$\overline{\text{SHDN}}$	$\overline{\text{SHDN}}$
23	D19	VSYNC	26	CLKIN	Dclk
24	GND	GND	25	D20	DE(Data Enable)

Notes : Refer to LVDS Transmitter Data Sheet for detail descriptions.

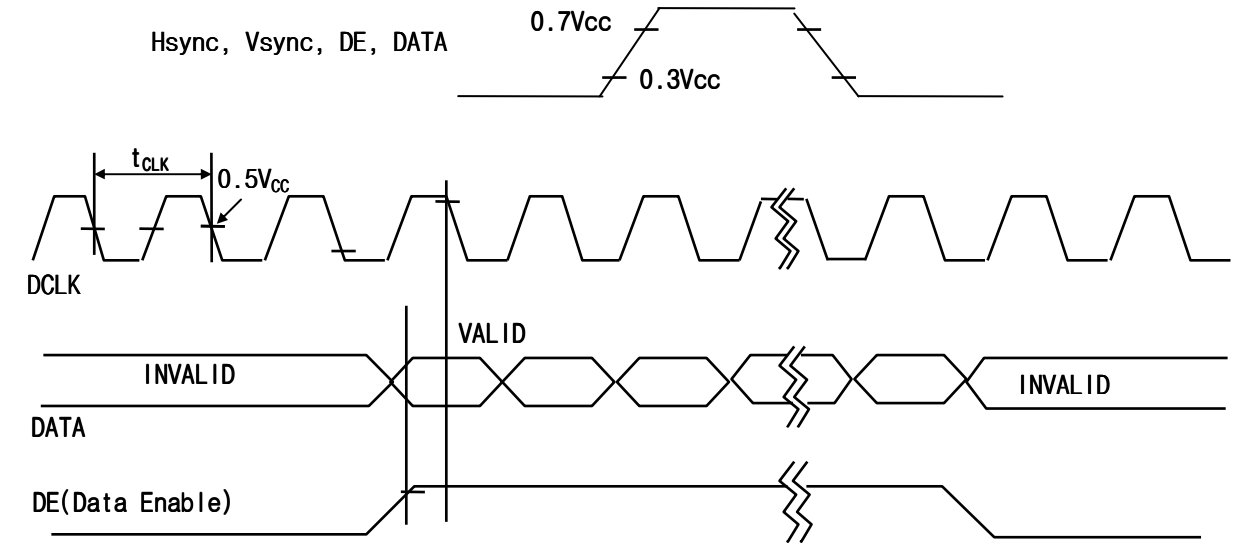
3-3. Signal Timing Specification

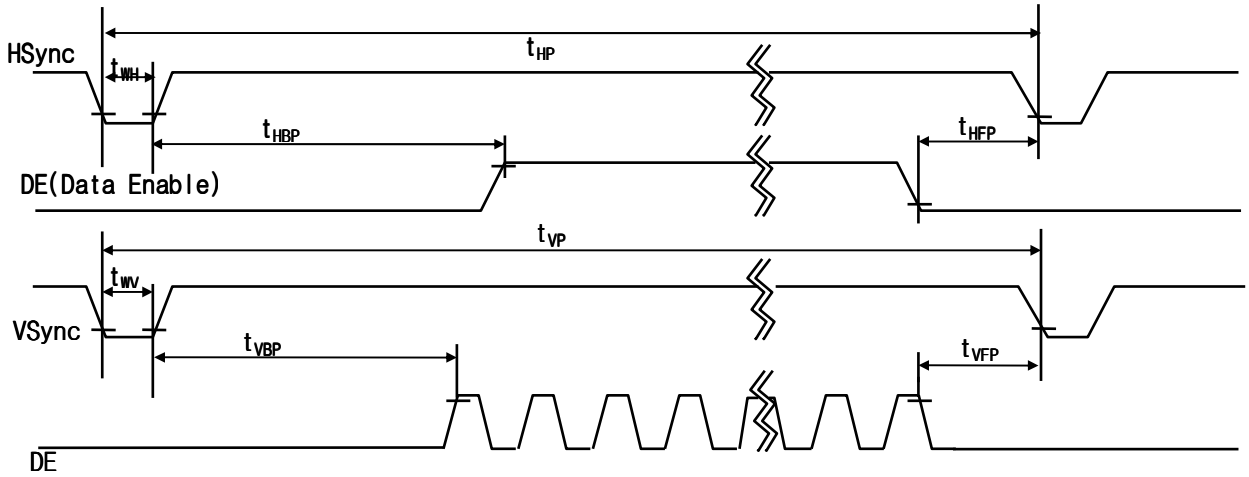
This is the signal timing required at the input of the LVDS Transmitter. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

Table 6 Timing Table

ITEM		SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
Dclk	Period	t_{CLK}	15.4	15.4	15.4		65MHz
Hsync	Period	t_{HP}	1206	1344	1364	t_{CLK}	
	Width-Active	t_{WH}	16	136	240		
Vsync	Period	t_{VP}	780	806	830	t_{HP}	
	Frequency	f_V	-	60	-	Hz	
	Width-Active	t_{WV}	1	6	24	t_{HP}	
DE (Data Enable)	Horizontal Back Porch	t_{HBP}	32	160	-	t_{CLK}	
	Horizontal Front Porch	t_{HFP}	10	24	-		
	Vertical Back Porch	t_{VBP}	2	29	-	t_{HP}	
	Vertical Front Porch	t_{VFP}	1	3	-		

3-4. Signal Timing Wave forms





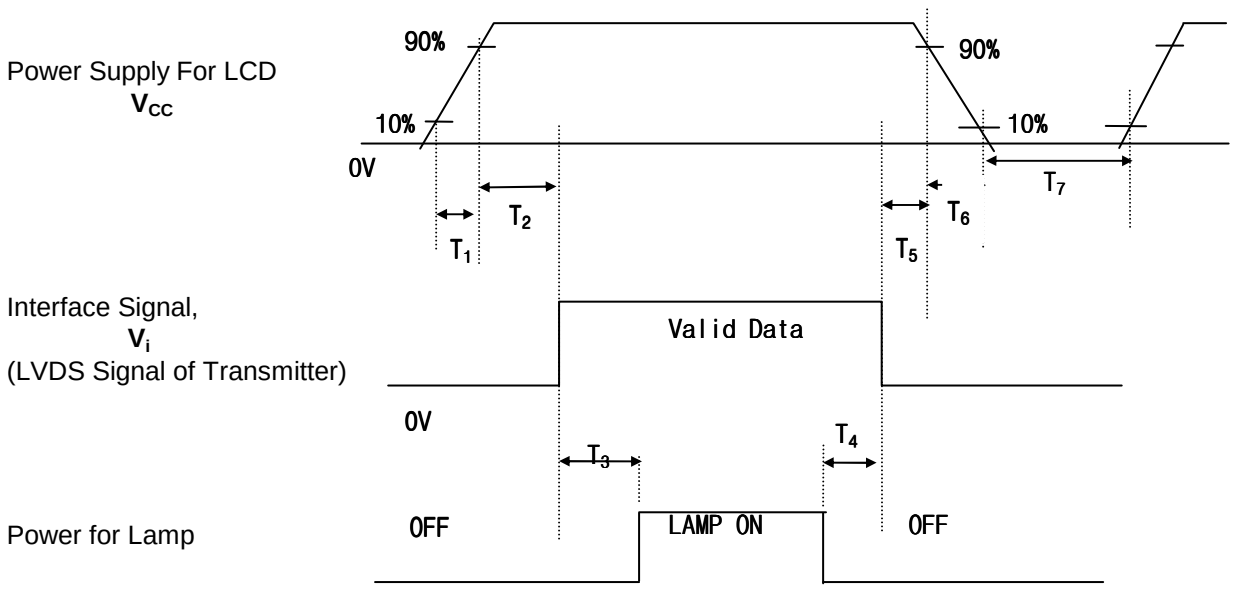
3-5. Color Input Data Reference

The brightness of each primary color(red, green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7 COLOR DATA REFERENCE

Color		Input Color Data																	
		Red						Green						Blue					
		MSB					LSB	MSB					LSB	MSB					LSB
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Red(00) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(02)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63) Bright	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Green	Green(00)Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(02)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)Bright	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Blue	Blue(00) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(02)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63) Bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

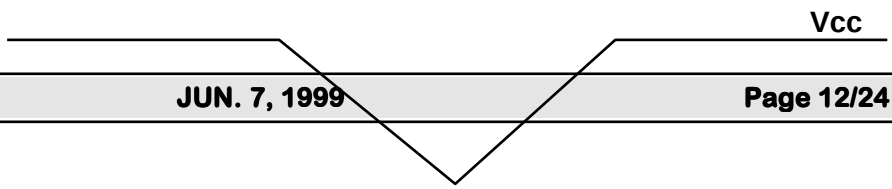
3-6. Power Sequence

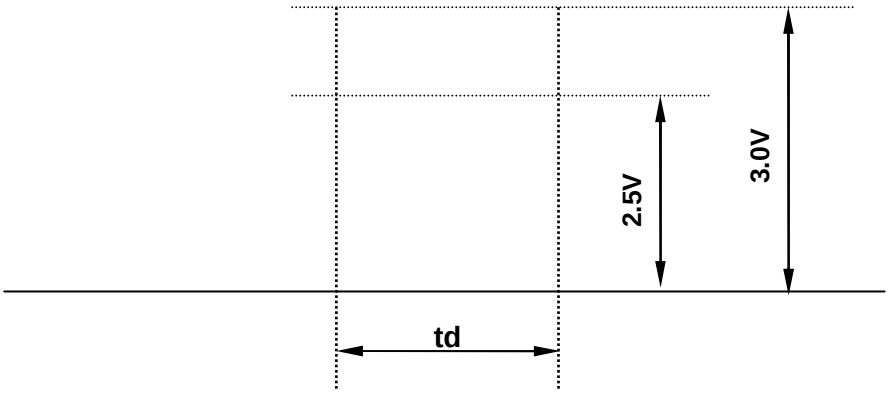


Parameter	Values			Units
	Min.	Typ.	Max.	
T_1	-	-	10	ms
T_2	0.01	-	20	ms
T_3	10	-	-	ms
T_4	10	-	-	ms
T_5	0.01	-	20	ms
T_6	0.01	-	20	ms
T_7	1	-	-	s

Notes: 1. Please avoid floating state of interface signal at invalid period.
2. When the interface signal is invalid, be sure to pull down the power supply for LCD V_{CC} to 0V.
3. Lamp power must be turn on after power supply for LCD and interface signal are valid.

3-7. Vcc Dip Condition





1)2.5V ≤ Vcc < 3.0V
td ≤ 20 ms
2)Vcc < 2.5V
Vcc-dip conditions should also follow the Power Up/Down conditions
for supply voltage

Notes : This phenomenon is caused by row driver IC initialization after power on (1 vertical period) .

4. Optical Specifications

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25℃. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0°.
Appendix A -1 presents additional information concerning the measurement equipment and method..

Table 8 OPTICAL CHARACTERISTICS (Ta=25℃, Vcc=3.3V, f_V =60Hz, Dclk=65MHz, I_{BL}=5.5mA)

Parameter	Symbol	Values			Units	Notes
		Min.	Typ.	Max.		
Contrast Ratio	CR	150	200	-		1
Surface Luminance, white	L_{WH}	110	130	-	cd/m^2	2
Luminance Variation	δ WHITE	-	1.25	1.45		3
Response Time	Tr				msec	4
Rise Time	Tr_R	-	30	50		
Decay Time	Tr_D	-	30	50		
CIE Color Coordinates						
Red	x_R	0.549	0.579	0.609		
	y_R	0.320	0.350	0.380		
Green	x_G	0.287	0.317	0.347		
	y_G	0.521	0.551	0.581		
Blue	x_B	0.125	0.155	0.185		
	y_B	0.120	0.150	0.180		
White	x_W	0.280	0.310	0.340		
	y_W	0.327	0.357	0.387		
Viewing Angle						
x axis, right (Φ=0º)	Θx	+40	-	-	degree	5
x axis, left(Φ=180º)	Θx	-40	-	-		
y axis, up(Φ=90º)	Θy	+10	-	-		
y axis, down (Φ=270º)	Θy	-30	-	-		
Gray Scale		-	-	-		6

Notes 1. Contrast Ratio (CR) is defined mathematically as :

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

2. Surface luminance is the center point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see Appendix A - 2.
3. The variation in surface Luminance, δ WHITE is determined by measuring L_{ON} at each test position 1 through 5, and then dividing the maximum L_{ON} of 5 points luminance by minimum L_{ON} of 5 points luminance. For more information see Appendix A - 2.

   δ WHITE = Maximum (L_{ON1}, L_{ON2}, ...L_{ON5}) ÷ Minimum (L_{ON1}, L_{ON2}, ...L_{ON5})
4. Response time is the time required for the display to transition from white to black (Rise Time, Tr_R) and from black to white (Decay Time, Tr_D). For additional information see Appendix A - 3.
5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see Appendix A - 4

6.Gray scale specification.

Gray Level	Luminance(%) (typ)
L0	0.4
L7	1.5
L15	3.7
L23	8.7
L31	19.6
L39	37.5
L47	61.4
L55	86.8
L63	100

5. Mechanical Characteristics

The chart below provides general mechanical characteristics for the model LP133X7-A2 LCD. In addition, the figure below is a detailed mechanical drawing of the LCD. Note that dimension are given for reference purposes only.

Outside dimensions :

Horizontal	284±0.5 mm
Vertical	214.5±0.5 mm
Depth	5.5±0.3 mm

Bezel area :

Horizontal	274.2±0.5 mm
Vertical	206.6 ±0.5 mm

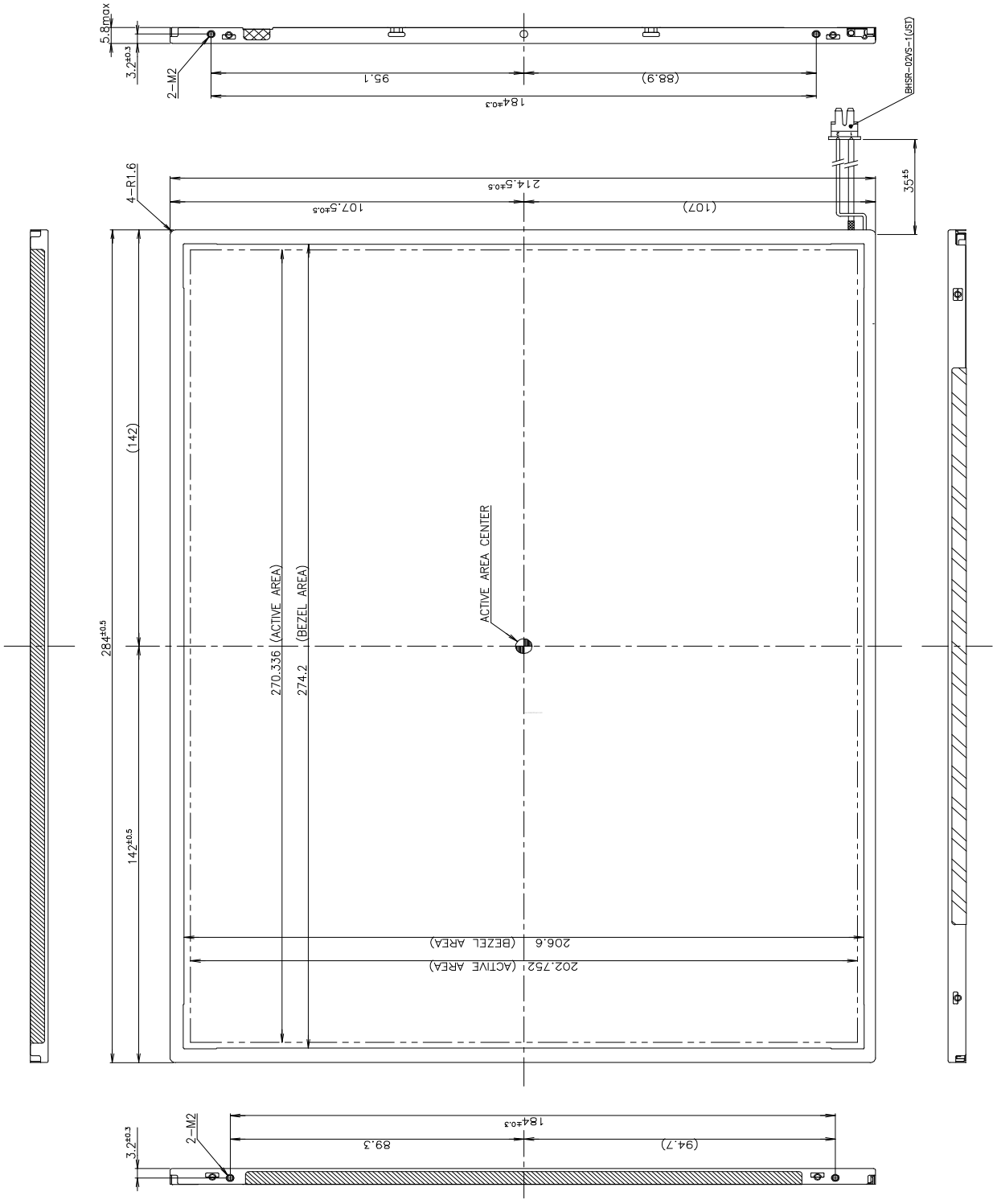
Active Display area :

Horizontal	270.34 mm
Vertical	202.75 mm

Weight (approximate) : 480g (typ), 500g(max)

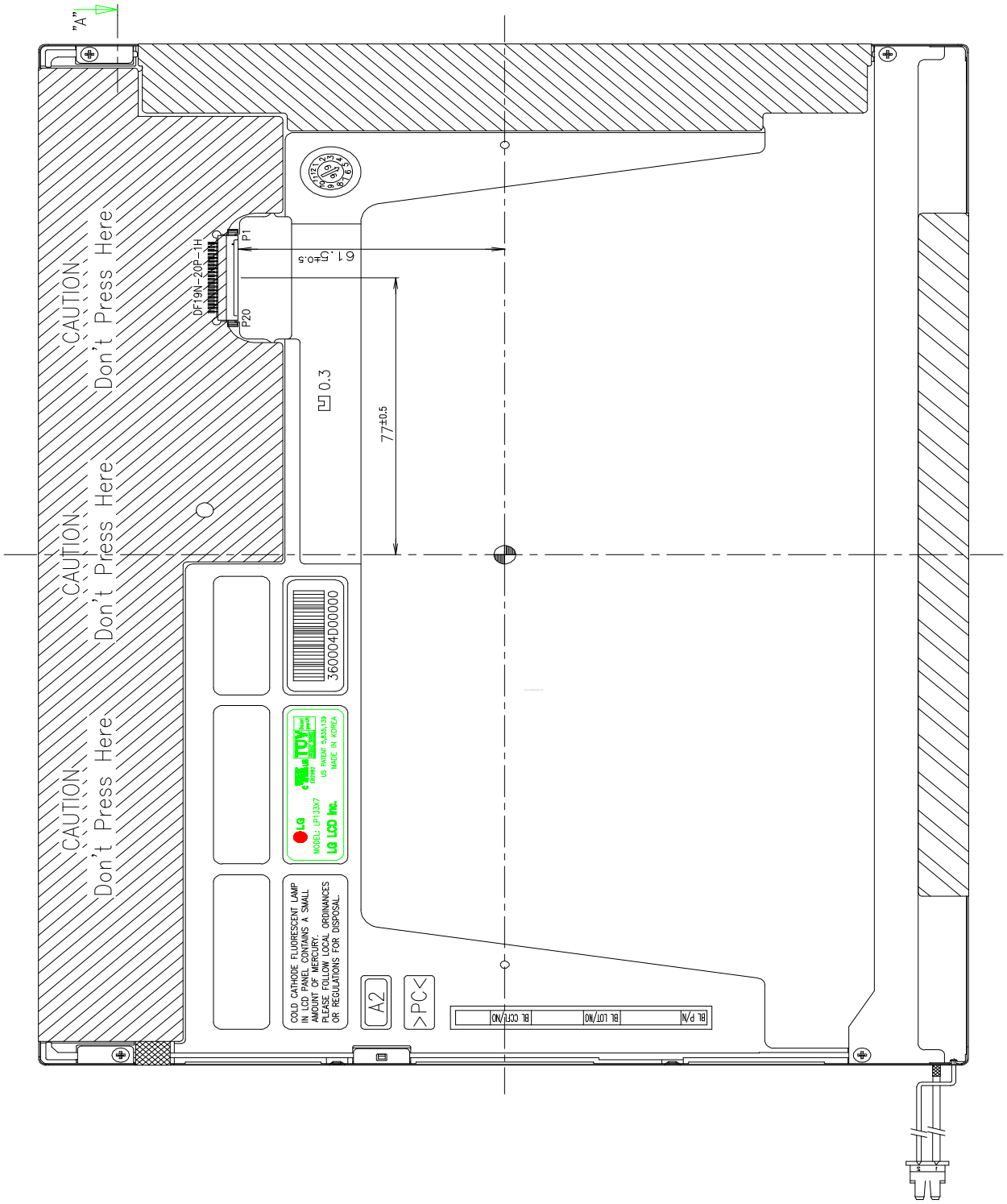
Surface Treatment : Hard coating 3H.
Anti-glare treatment of the front polarizer

< FRONT VIEW >

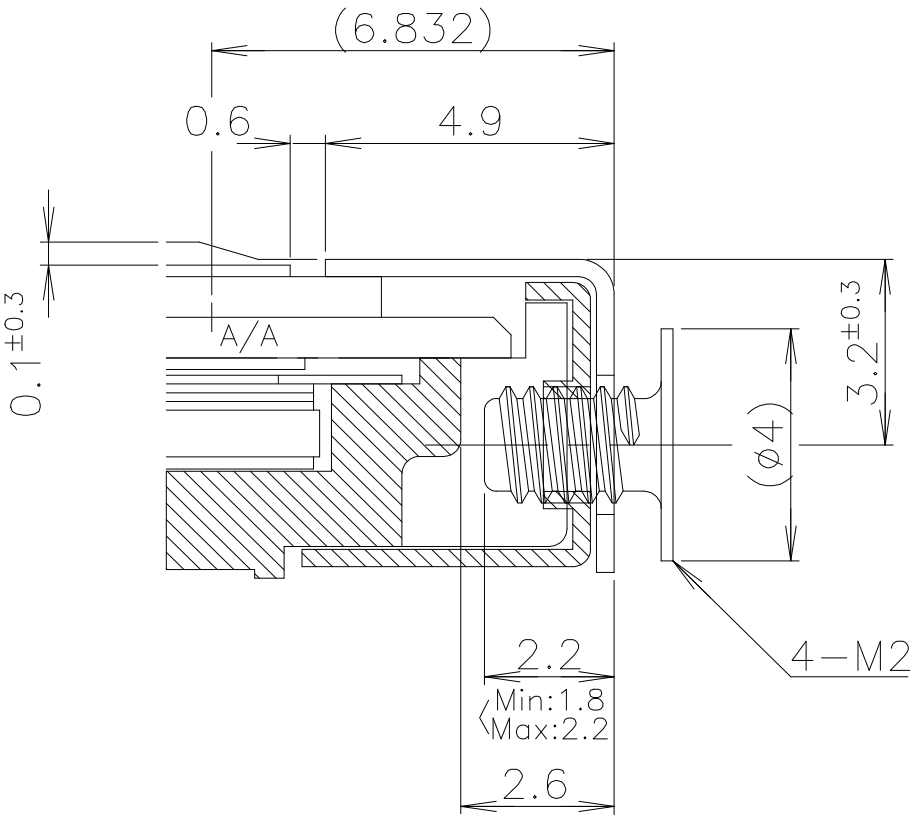


Notes 1. Unspecified tolerance ±0.5mm

< REAR VIEW >


"A"
CAUTION
Don't Press Here
CAUTION
Don't Press Here
CAUTION
Don't Press Here
DF19N-20P-1H
P1
P20
61±0.5
77±0.5
0.3
360004000000
COLD CATHODE FLUORESCENT LAMP IN LCD PANEL CONTAINS A SMALL AMOUNT OF MERCURY. PLEASE FOLLOW LOCAL ORDINANCES OR REGULATIONS FOR DISPOSAL.
A2
>PC<
BL P/N
BL LOT/NO
BL CCRT/NO

<Detail description of side mounting screw>



Notes

1. Screw Torque : 1.3-1.5kgf.cm

6. Reliability

- Environment test condition

No.	Test ITEM	Conditions
1	High temperature storage test	Ta = 60℃ 240h
2	Low temperature storage test	Ta = -20℃ 240h
3	High temperature operation test	Ta = 50℃ 50%RH 240h
4	Low temperature operation test	Ta = 0℃ 240h
5	Vibration test (non-operating)	Sine wave, 10~500~10Hz, 1.5G, 0.37oct/min, 3 axis, 1 hour/axis
6	Shock test (non-operating)	half sine wave, 180G, 2ms, one shock of each six faces (i.e. run 180G 2ms for all six faces.)
7	Altitude operating storage/shipment	 0 - 10,000 feet (3048m) 0 - 40,000 feet (12192m)

{Result Evaluation Criteria}

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

- ON/OFF Cycle

  : The display module will be capable of being operated over 24,000 ON/OFF cycles (Lamp power & Vcc ON/OFF)

- Mean Time Between Failure

  : The LCD Panel and interface board assembly (excluding the CCFTs) shell have a mean time between failures of 30,000 hours with a confidence level 90%.

7. International Standards

7-1. Safety

a) UL 1950 Third Edition, Underwriters Laboratories, Inc. Jan. 28, 1995.
Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.

b) CAN/CSA C22.2 No. 950-95 Third Edition, Canadian Standards Association, Jan. 28, 1995.
Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.

c) EN 60950 : 1992 + A1 : 1993 + A2 : 1993 + A3 : 1995 + A4 : 1997 + A11 : 1997
IEC 950 : 1991 + A1 : 1992 + A2 : 1993 + A3 : 1995 + A4 : 1996
European Committee for Electrotechnical Standardization (CENELEC)
EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7-1. EMC

a) ANSI C63.4 “Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9kHz to 40GHz.” American National Standards Institute(ANSI),1992.

b) C.I.S.P.R “Limits and Methods of Measurement of Radio Interference Characteristics of Information Technology Equipment.” International Special Committee on Radio Interference

c) EN 55022 “Limits and Methods of Measurement of Radio Interference Characteristics of Information Technology Equipment.” European Committee for Electrotechnical Standardization (CENELEC),1988

8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A	B	C	D	E	F	G	H	I	J	K	L	M

A, B, C : INCH CODE
D : YEAR
E : MONTH
F,G : PANEL FACTORY
H : MODULE LINE
I, J, K, L, M : SERIAL NO

Note 1. YEAR(D)

YEAR	89	90	91	92	93	94	95	96	97	98	99
Mark	9	0	1	2	3	4	5	6	7	8	9

2. MONTH(E)

MONTH	Jan.	Feb.	Mar.	Apr.	May	Jun.	Jun.	Aug.	Sep.	Oct.	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	0	N	D

3. MODULE LINE(H)

LINE	1	2	3	4	5	6	7	8	9	10	11	12	13
Mark	1	2	3	4	5	6	7	8	9	A	B	C	D

b) Location of Lot Mark

Serial NO. Is printed on the label. The label is attached to the backside of the LCD module.
This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box : 10 PCS

b) Box Size : 374mm X 329mm X 311mm

9.PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9.1 MOUNTING PRECAUTIONS

(1) You must mount a module using holes arranged in four corners.
(2) You should consider the mounting structure so that uneven force (ex. twisted stress) is not applied to the module.
And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
(3) Please attach the surface with a transparent protective plate in order to protect the polarizer LC cell.
Transparent protective plate should have sufficient strength in order to resist external force.
(4) You should adopt radiation structure to satisfy the temperature specification.
(5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polalizer at high temperature and the latter causes circuit break by electro-chemical reaction.
(6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil ead. And Please do not rub with dust clothes with chemical treatment.
Do not touch the surface df polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
(7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaked with petrolium benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluen and alcohol because they cause chemical damage to the polarizer.
(8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
(9) Do not open the case because inside circuits do not have sufficient strength.

9.2 OPERATING PRECAUTIONS

(1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
V = ± 200mV (Over and under shoot voltage).
(2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
(3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
(4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
(5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
(6) A module has high frequency circuit. If you need to shield the electromagnetic noise, please do in yours. When a Back-light unit is operating, it sounds. If you need to shield the noise, please do in yours.

9.3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits. it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc . And don't touch I/F pin directly.

9.4 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9.5 STORAGE

When storing modules as spares for a long time. The following precautions are necessary.

(1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5℃ and 35℃ at normal humidity.

(2) The polarizer surface should not come in contact with any other object.
It is recommended that they be stored in the container in which they were shipped.

9.6 HANDLING PRECAUTIONS FOR PROTECTION FILM

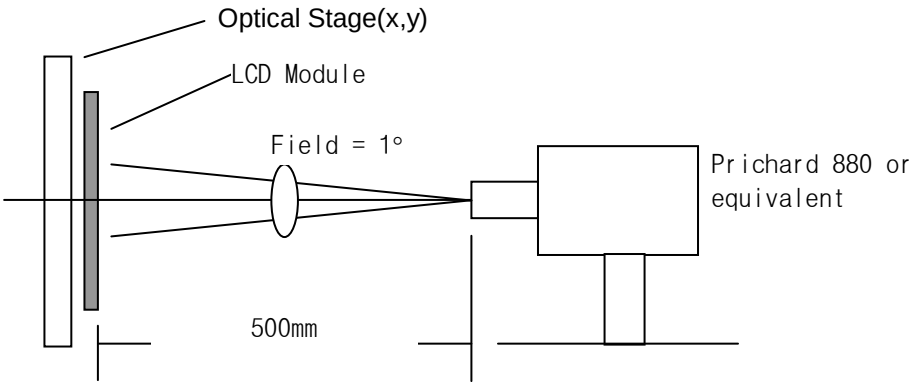
(1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion- blown equipment or in such a condition, etc..

(2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
Please carefully peel off the protection film without rubbing it against the polarizer.

(3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.

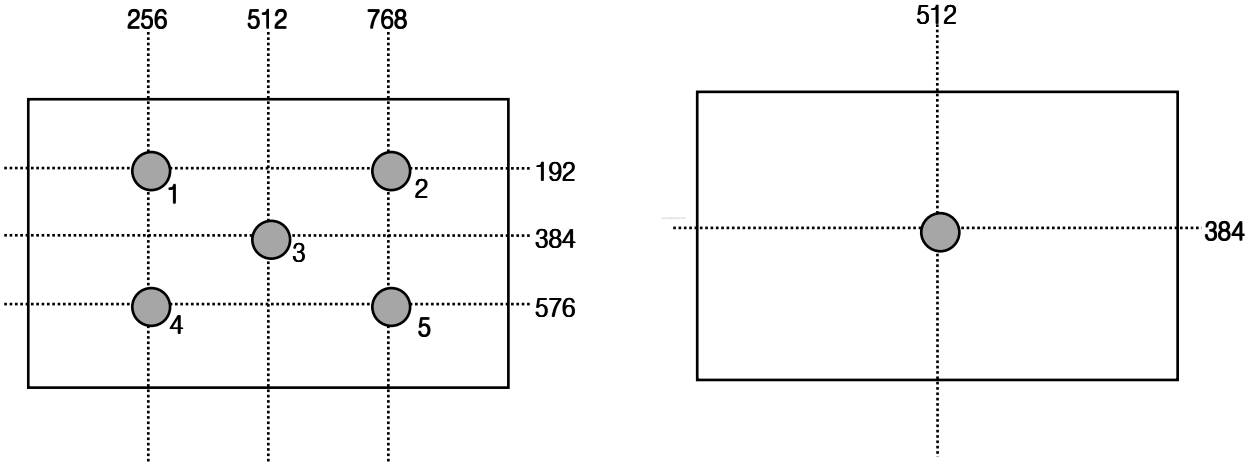
(4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

A-1 Optical Characteristic Measurement Equipment and Method



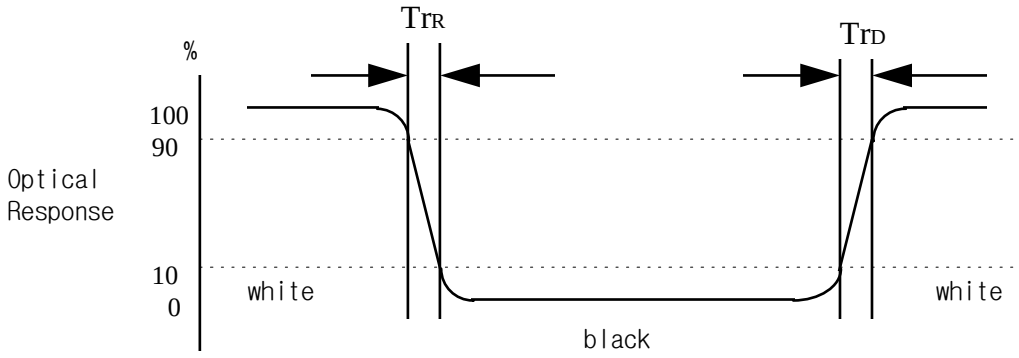
A-2 Luminance

<measuring point for luminance variation> <measuring point for surface luminance >



A-3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for “black” and “white”.



A-4 Viewing angle

<Definition of viewing angle range>