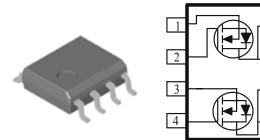
## **Dual N-Channel 30-V (D-S) MOSFET**

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low  $r_{DS(on)}$  and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

PRODUCT SUMMARY				
$V_{DS}(V)$	$r_{DS(on)} m(\Omega)$ $I_D(A)$			
30	$13.5 @ V_{GS} = 10V$	10		
	$20 @ V_{GS} = 4.5V$	8		

- Low r<sub>DS(on)</sub> provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe SOIC-8 saves board space
- Fast switching speed
- High performance trench technology



ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C UNLESS OTHERWISE NOTED)						
Parameter		Symbol	Limit	Units		
Drain-Source Voltage			30	V		
Gate-Source Voltage			±20	v		
Continuo Dario Commut <sup>a</sup>	$T_A=25^{\circ}C$	ī	10			
Continuous Drain Current <sup>a</sup>	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$		8.2	Α		
Pulsed Drain Current <sup>b</sup>	$I_{DM}$	±50				
Continuous Source Current (Diode Conduction) <sup>a</sup>		$I_S$	2.3	A		
D D: : ,: a	$T_A=25^{\circ}C$	D	2.1	W		
Power Dissipation <sup>a</sup>	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	l D	1.3			
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	-55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Maximum	Units		
Maximum Junction-to-Case <sup>a</sup>	t <= 5 sec	$R_{ heta JC}$	40	°C/W		
Maximum Junction-to-Ambient <sup>a</sup>	t <= 5 sec	$R_{ heta JA}$	60	°C/W		

#### Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

SPECIFICATIONS (T <sub>A</sub> = 25°C UNLESS OTHERWISE NOTED)							
Parameter	Crymbal	Test Conditions	Limits			T I24	
rarameter	Symbol	Symbol Test Conditions		Тур	Max	Unit	
Static							
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0 \text{ V}, I_D = 250 \text{ uA}$	30			V	
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$	1			v	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}, V_{GS} = 20 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$			1	11.Λ	
Zero Gate Voltage Drain Current	<sup>1</sup> DSS	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			25	uA	
On-State Drain Current <sup>A</sup>	$I_{D(on)}$	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			A	
		$V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$			13.5		
Drain-Source On-Resistance <sup>A</sup>	$r_{DS(on)}$	$V_{GS} = 4.5 \text{ V}, I_D = 8 \text{ A}$			20	mΩ	
		$V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}, T_J = 55^{\circ}\text{C}$			15		
Forward Tranconductance <sup>A</sup>	$g_{\mathrm{fs}}$	$V_{DS} = 15 \text{ V}, I_D = 10 \text{ A}$		40		S	
Diode Forward Voltage	$V_{\mathrm{SD}}$	$I_S = 2.3 \text{ A}, V_{GS} = 0 \text{ V}$		0.7		V	
Pulsed Source Current (Body Diode) <sup>A</sup>	$I_{SM}$			5		Α	
Dynamic <sup>b</sup>							
Total Gate Charge	$Q_{g}$	$V_{DS} = 15 \text{ V}, V_{GS} = 5 \text{ V},$		20			
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 13 \text{ V}, V_{GS} = 3 \text{ V},$ $I_D = 10 \text{ A}$		7.0		пC	
Gate-Drain Charge	$Q_{gd}$	1 <sub>D</sub> - 10 A		7.0		]	
Turn-On Delay Time	t <sub>d(on)</sub>			20			
Rise Time	t <sub>r</sub>	$V_{DD} = 25 \text{ V}, R_L = 25 \Omega, I_D = 1 \text{ A},$		9		nS	
Turn-Off Delay Time	$t_{d(off)}$	$V_{GEN} = 10 \text{ V}$		70		113	
Fall-Time	$t_{\mathrm{f}}$			20			

#### Notes

- a. Pulse test:  $PW \le 300us duty cycle \le 2\%$ .
- b. Guaranteed by design, not subject to production testing.

FREESCALE reserves the right to make changes without further notice to any products herein. REESCALE makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does freescale assumeany liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in freescale data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. freescale does not convey any license under its patent rights nor the rights of others. freescale products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the freescale product could create a situ ation where personal injury or death may occur. Should Buyer purchase or use freescale products for any such unintended or unauthorized application, Buyer's hall indemnify and hold freescale and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that freescale was negligent regarding the design or manufacture of the part. freescale is an Equal Opportunity/Affirmative Action Employer.

## Typical Electrical Characteristics (N-Channel)

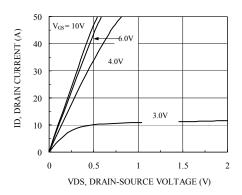


Figure 1. On-Region Characteristics

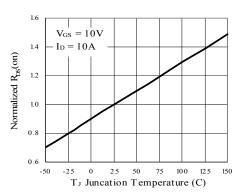


Figure 3. On-Resistance Variation with Temperature

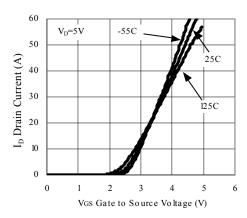


Figure 5. Transfer Characteristics

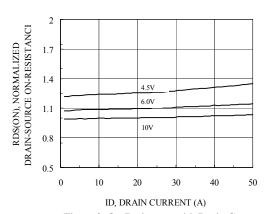


Figure 2. On-Resistance with Drain Current

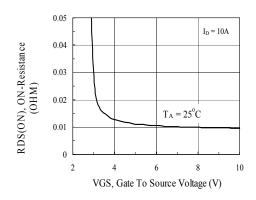


Figure 4. On-Resistance Variation with Gate to Source Voltage

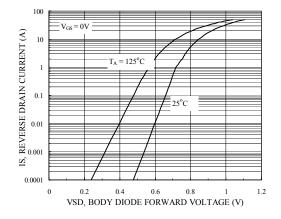


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature

Freescale AO4824/MC4824

## Typical Electrical Characteristics (N-Channel)

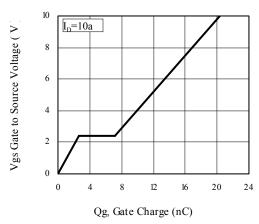


Figure 7. Gate Charge Characteristics

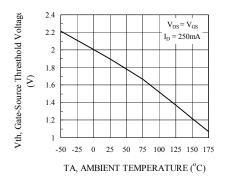


Figure 9. Threshold Vs Ambient Temperature

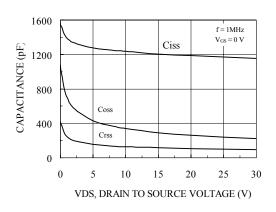


Figure 8. Capacitance Characteristics

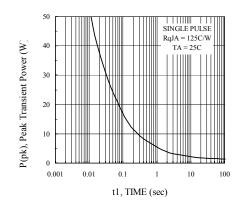


Figure 10. Single Pulse Maximum Power Dissipation

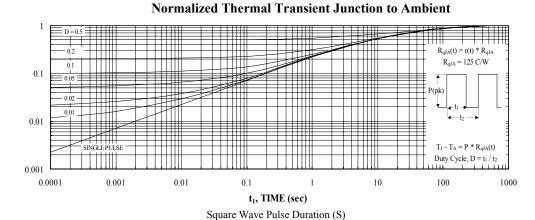
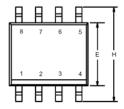
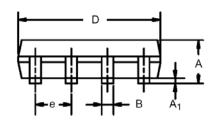


Figure 11. Transient Thermal Response Curve

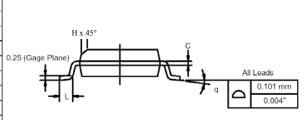
## Package Information

SO-8: 8LEAD





	MILLIN	IETERS	INCHES		
Dim	Min	Max	Min	Max	
Α	1.35	1.75	0.053	0.069	
A <sub>1</sub>	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
E	3.80	4.00	0.150	0.157	
е	1.27 BSC		0.050 BSC		
Н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	



Freescale AO4824/MC4824

# Ordering information

• AM4910N-T1-XX

- A: Analog Power

- M: MOSFET

- 4910: Part number

– N: N-Channel

- T1: Tape & reel

- XX: Blank: Standard

PF: Leadfree