

# UNISONIC TECHNOLOGIES CO., LTD

UF50N20 Preliminary Power MOSFET

# 50A, 200V N-CHANNEL POWER MOSFET

#### DESCRIPTION

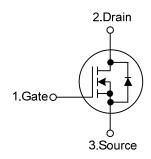
The UTC **UF50N20** is an N-channel power MOSFET using UTC's advanced technology to provide the customers with perfect  $R_{DS(ON)}$ , high switching speed, high current capacity and low gate charge.

The UTC **UF50N20** is suitable for motor control, AC-DC or DC-DC converters and audio amplifiers, etc.

#### ■ FEATURES

- \*  $R_{DS(ON)}$ <40m $\Omega$  @  $V_{GS}$ =10V, $I_D$ =50A
- \* High Switching Speed
- \* High Current Capacity
- \* Low Gate Charge(typical 130nC)

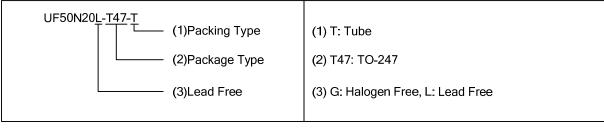
#### ■ SYMBOL

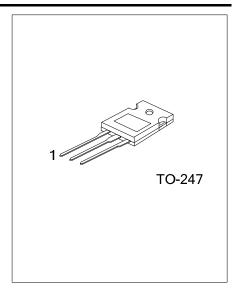


#### ORDERING INFORMATION

Ordering Number		Deelsess	Pin Assignment			De alsisas	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UF50N20L-T47-T	UF50N20G-T47-T	TO-247	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source





## ■ ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage (V <sub>GS</sub> =0)		$V_{DSS}$	200	V
Gate-Source Voltage		$V_{GSS}$	±20	V
Drain Current	Continuous	$I_{D}$	50	Α
	Pulsed (Note 1)	$I_{DM}$	200	Α
Avalanche Current		$I_{AR}$	60	Α
Avalanche Energy		E <sub>AS</sub>	600	mJ
Power Dissipation		$P_D$	125	W
Junction Temperature		T <sub>J</sub>	150	°C
Storage Temperature		$T_{STG}$	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Pulse width limited by safe operating area

## ■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	$\theta_{JA}$	62.5	°C/W
Junction to Case	$\theta_{ m JC}$	1	°C/W

# ■ ELECTRICAL CHARACTERISTICS (T<sub>C</sub>=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage		$BV_{DSS}$	I <sub>D</sub> =250μA, V <sub>GS</sub> =0V				٧	
Drain-Source Leakage Current		I <sub>DSS</sub>	V <sub>DS</sub> =200V, V <sub>GS</sub> =0V			1	μΑ	
Gate-Source Leakage Current	Forward	I <sub>GSS</sub>	$V_{GS}$ =+20V, $V_{DS}$ =0V			+100	nA	
	Reverse		V <sub>GS</sub> =-20V, V <sub>DS</sub> =0V			-100	nA	
ON CHARACTERISTICS (Note 2)								
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	2	3	4	V	
Static Drain-Source On-State Resistance		R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =30A			40	mΩ	
DYNAMIC PARAMETERS								
Input Capacitance		C <sub>ISS</sub>			3900		pF	
Output Capacitance		Coss	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1.0MHz		950		pF	
Reverse Transfer Capacitance		$C_{RSS}$			250		pF	
SWITCHING PARAMETERS								
Total Gate Charge		$Q_G$			130	170	nC	
Gate to Source Charge		$Q_GS$	V <sub>GS</sub> =10V, V <sub>DD</sub> =100V, I <sub>D</sub> =50A		26		nC	
Gate to Drain Charge		$Q_GD$			55		nC	
Turn-ON Delay Time		$t_{D(ON)}$			30		ns	
Rise Time		$t_R$	$V_{DD}$ =30V, $I_{D}$ =25A, $R_{G}$ =4.7 $\Omega$ ,		180		ns	
Fall-Time		$t_{F}$	V <sub>GS</sub> =10V		35		ns	
Off-Voltage Rise Time		t <sub>R(OFF)</sub>			135		ns	
SOURCE- DRAIN DIODE RATI	NGS AND (	CHARACTER	RISTICS					
Maximum Body-Diode Continuous Current		Is	(Note 1)			50	Α	
Maximum Body-Diode Pulsed Current		I <sub>SM</sub>				200	Α	
Drain-Source Diode Forward Voltage		$V_{\text{SD}}$	I <sub>SD</sub> =50A, V <sub>GS</sub> =0V (Note 2)			1.6	V	

Notes: 1. Pulse width limited by safe operating area

2. Pulsed: Pulse duration=300µs, Duty cycle 1.5%

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