

## SOT-723 Plastic-Encapsulate MOSFETS

### 2SK3541 N-Channel MOSFET

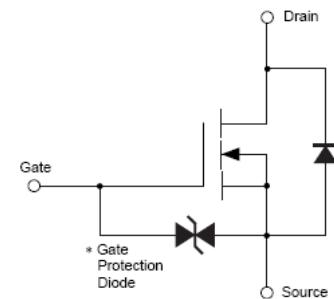
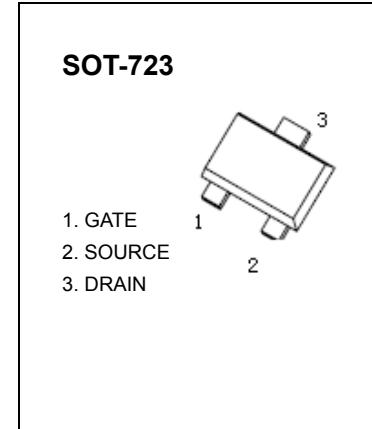
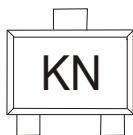
#### FEATURES

- Low on-resistance
- Fast switching speed
- Low voltage drive makes this device ideal for portable equipment
- Drive circuits can be simple
- Parallel use is easy

#### APPLICATIONS

Interfacing , Switching

#### MARKING:KN



\*A protection diode is included between the gate and the source terminals to protect the diode against static electricity when the product is in use. Use a protection circuit when the fixed voltages are exceeded.

#### Maximum ratings ( $T_a=25^\circ\text{C}$ unless otherwise noted)

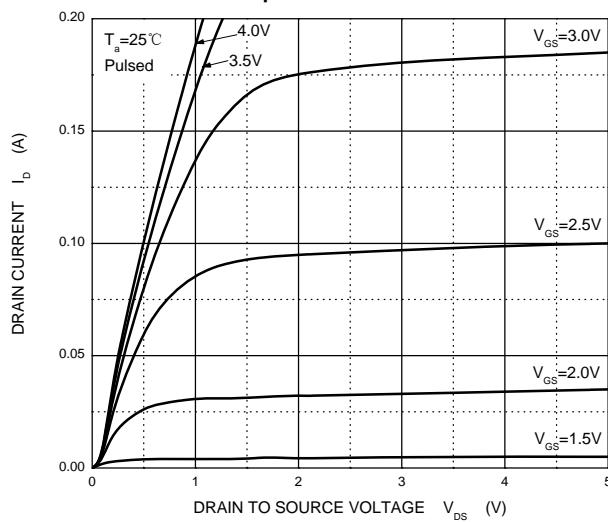
| Parameter                                   | Symbol          | Value     | Units                     |
|---|-----------------|-----------|---------------------------|
| Drain-source voltage                        | $V_{DS}$        | 30        | V                         |
| Gate-source voltage                         | $V_{GS}$        | $\pm 20$  |                           |
| Continuous drain current                    | $I_D$           | $\pm 100$ | mA                        |
| Power dissipation                           | $P_D$           | 0.15      | W                         |
| Thermal resistance from junction to ambient | $R_{\theta JA}$ | 833       | $^\circ\text{C}/\text{W}$ |
| Junction temperature                        | $T_J$           | 150       | $^\circ\text{C}$          |
| Storage temperature                         | $T_{stg}$       | -55 ~+150 |                           |

\*  $P_w \leq 10\mu\text{s}$  ,Duty cycle $\leq 1\%$

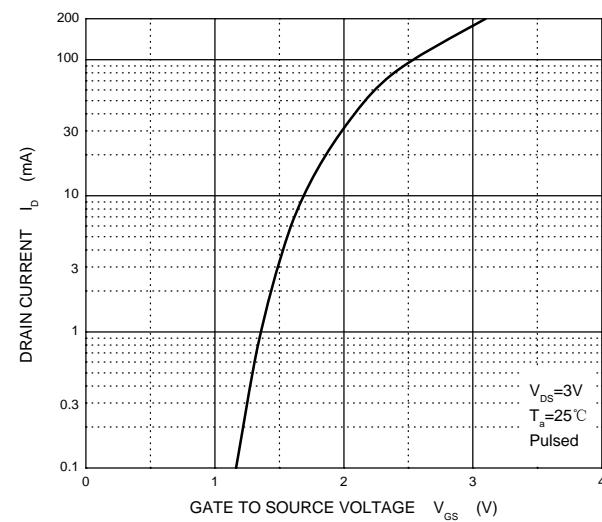
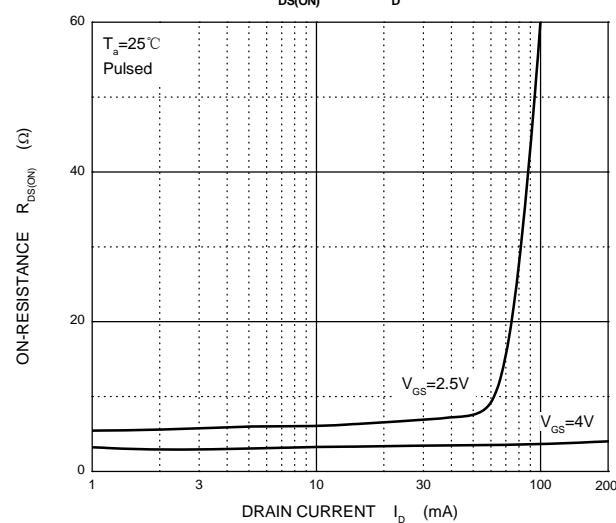
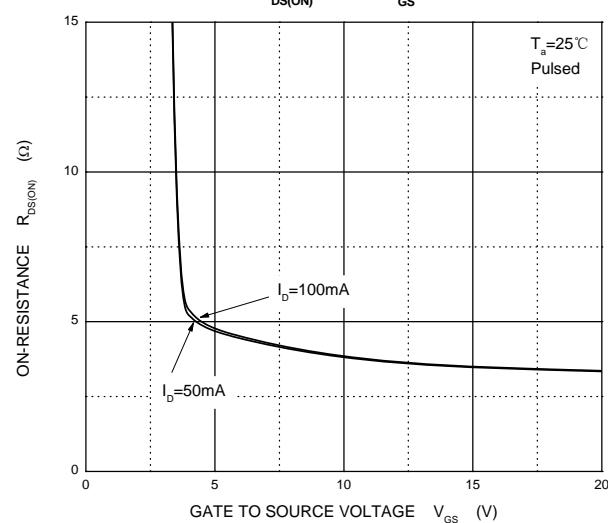
**Electrical characteristics ( $T_a=25^\circ\text{C}$  unless otherwise noted)**

| Parameter                               | Symbol                      | Test Condition   | Min | Typ | Max     | Unit          |
|---|-----------------------------|--|-----|-----|---------|---------------|
| Drain-source breakdown voltage          | $V_{(\text{BR})\text{DSS}}$ | $V_{\text{GS}} = 0\text{V}, I_D = 10\mu\text{A}$   | 30  |     |         | V             |
| Gate-source leakage current             | $I_{\text{GSS}}$            | $V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$  |     |     | $\pm 2$ | $\mu\text{A}$ |
| Zero gate voltage drain current         | $I_{\text{DSS}}$            | $V_{\text{DS}} = 30\text{V}, V_{\text{GS}} = 0\text{V}$  |     |     | 1.0     | $\mu\text{A}$ |
| Gate threshold voltage                  | $V_{\text{GS}(\text{th})}$  | $V_{\text{DS}} = 3\text{V}, I_D = 100\mu\text{A}$  | 0.8 |     | 1.5     | V             |
| Static drain-source on-state resistance | $R_{\text{DS}(\text{on})}$  | $V_{\text{GS}} = 4\text{V}, I_D = 10\text{mA}$   |     | 5   | 8       | $\Omega$      |
|   |                             | $V_{\text{GS}} = 2.5\text{V}, I_D = 1\text{mA}$  |     | 7   | 13      |               |
| Forward transconductance                | $g_{\text{FS}}$             | $V_{\text{DS}} = 3\text{V}, I_D = 10\text{mA}$   | 20  |     |         | mS            |
| Input capacitance                       | $C_{\text{iss}}$            | $V_{\text{DS}} = 5\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$  |     | 13  |         | pF            |
| Output capacitance                      | $C_{\text{oss}}$            |  |     | 9   |         |               |
| Reverse transfer capacitance            | $C_{\text{rss}}$            |  |     | 4   |         |               |
| Turn-on delay time                      | $t_{\text{d}(\text{on})}$   | $V_{\text{GS}} = 5\text{V}, V_{\text{DD}} = 5\text{V}, I_D = 10\text{mA}$<br>$R_L = 500\Omega, R_G = 10\Omega$ |     | 15  |         | ns            |
| Rise time                               | $t_r$                       |  |     | 35  |         |               |
| Turn-off delay time                     | $t_{\text{d}(\text{off})}$  |  |     | 80  |         |               |
| Fall time                               | $t_f$                       |  |     | 80  |         |               |

## Output Characteristics



## Transfer Characteristics

 $R_{DS(ON)}$  —  $I_D$  $R_{DS(ON)}$  —  $V_{GS}$  $I_s$  —  $V_{SD}$ 