

N- AND P-CHANNEL ENHANCEMENT MODE POWER MOSFET

MTS3572G6

Description

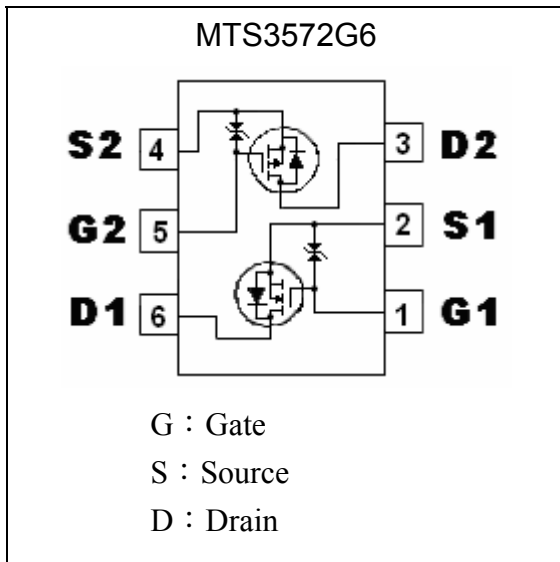
The MTS3572G6 consists of a N-channel and a P-channel enhancement-mode MOSFET in a single TSOP-6 package, providing the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TSOP-6 package is universally preferred for all commercial-industrial surface mount applications.

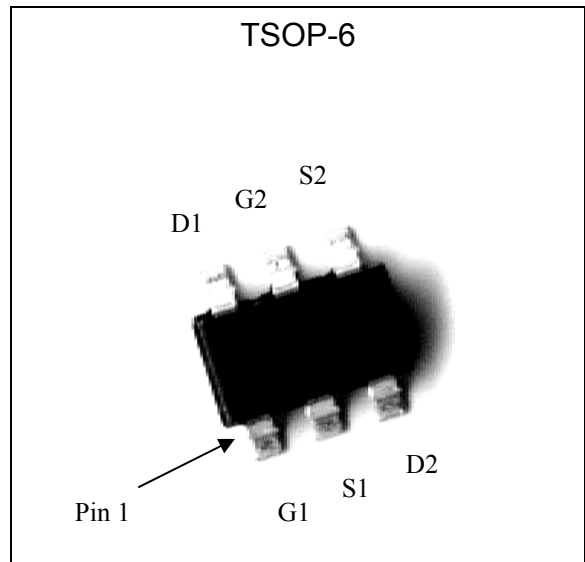
Features

- Simple drive requirement
- Low gate charge
- Low on-resistance
- Fast switching speed
- Pb-free package

Equivalent Circuit



Outline





Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits		Unit
		N-channel	P-channel	
Drain-Source Breakdown Voltage	BV _{DSS}	60	-20	V
Gate-Source Voltage	V _{GS}	±20	±12	V
Continuous Drain Current @T _A =25 °C (Note 1)	I _D	0.3	-4.3	A
Continuous Drain Current @T _A =70 °C (Note 1)	I _D	0.24	-3.4	A
Pulsed Drain Current (Note 2)	I _{DM}	0.8	-20	A
Total Power Dissipation (Note 1)	P _d	1.14		W
Linear Derating Factor		0.01		W / °C
Operating Junction and Storage Temperature	T _j , T _{stg}	-55~+150		°C
Thermal Resistance, Junction-to-Ambient (Note 1)	R _{th,ja}	110		°C/W

Note : 1.Surface mounted on 1 in² copper pad of FR-4 board, t≤5 sec; 180°C/W when mounted on minimum copper pad
 2.Pulse width limited by maximum junction temperature

N-Channel Electrical Characteristics (Tj=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	60	-	-	V	V _{GS} =0, I _D =250μA
V _{GS(th)}	1.0	-	2.5	V	V _{DS} =V _{GS} , I _D =250μA
I _{GSS}	-	-	±10	μA	V _{GS} =±20V, V _{DS} =0
I _{DSS}	-	-	1		V _{DS} =60V, V _{GS} =0
	-	-	10		V _{DS} =48V, V _{GS} =0, T _j =70°C
*R _{DS(ON)}	-	1.5	2.5	Ω	I _D =300mA, V _{GS} =10V
	-	1.9	3		I _D =100mA, V _{GS} =4.5V
	-	3.3	6		I _D =10mA, V _{GS} =2.5V
*G _{FS}	100	240	-	mS	V _{DS} =10V, I _D =100mA
Dynamic					
C _{iss}	-	30.6	-	pF	V _{DS} =10V, V _{GS} =0, f=1MHz
C _{oss}	-	5.5	-		
C _{rss}	-	4	-		
*t _{d(ON)}	-	2.6	-	ns	V _{DS} =30V, I _D =100mA, V _{GS} =5V, R _G =25 Ω
*t _r	-	7.1	-		
*t _{d(OFF)}	-	8.6	-		
*t _f	-	15	-		
*Q _g	-	0.7	-	nC	V _{DS} =30V, I _D =0.3A, V _{GS} =4.5V
*Q _{gs}	-	0.3	-		
*Q _{gd}	-	0.1	-		
Source-Drain Diode					
*I _S	-	-	0.3	A	
*I _{SM}	-	-	0.8		
*V _{SD}	-	-	1.2	V	V _{GS} =0V, I _S =100mA

*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%



P-Channel Electrical Characteristics (Tj=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	-20	-	-	V	V _{GS} =0, I _D =-250μA
V _{GS(th)}	-0.4	-	-1	V	V _{DS} =V _{GS} , I _D =-250μA
I _{GSS}	-	-	±20	μA	V _{GS} =±12V, V _{DS} =0
I _{DSS}	-	-	-1		V _{DS} =-20V, V _{GS} =0
	-	-	-25		V _{DS} =-16V, V _{GS} =0, Tj=70°C
*R _{DSON}	-	-	52	mΩ	I _D =-4A, V _{GS} =-4.5V
	-	-	60		I _D =-4A, V _{GS} =-2.5V
	-	-	75		I _D =-2A, V _{GS} =-1.8V
*G _{FS}	-	4	-	S	V _{DS} =-10V, I _D =-1A
Dynamic					
C _{iss}	-	1220	-	pF	V _{DS} =-10V, V _{GS} =0, f=1MHz
C _{oss}	-	103	-		
C _{rss}	-	92	-		
*t _{d(ON)}	-	12.5	-	ns	V _{DS} =-10V, I _D =-1A, V _{GS} =-4.5V, R _G =6Ω, R _D =10Ω
*t _r	-	12	-		
*t _{d(OFF)}	-	70	-		
*t _f	-	20	-		
*Q _g	-	10	-	nC	V _{DS} =-10V, I _D =-4.3A, V _{GS} =-4.5V
*Q _{gs}	-	1.9	-		
*Q _{gd}	-	2.7	-		
Source-Drain Diode					
*I _S	-	-	-1	A	
*I _{SM}	-	-	-4		
*V _{SD}	-	-	-1.2	V	V _{GS} =0V, I _S =-1A

*Pulse Test : Pulse Width ≤300μs, Duty Cycle ≤2%

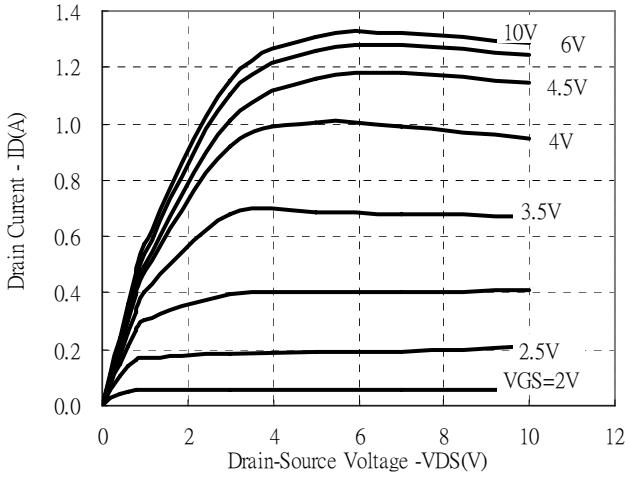
Ordering Information

Device	Package	Shipping	Marking
MTS3572G6	TSOP-6 (Pb-free lead plating package)	3000 pcs / Tape & Reel	3572

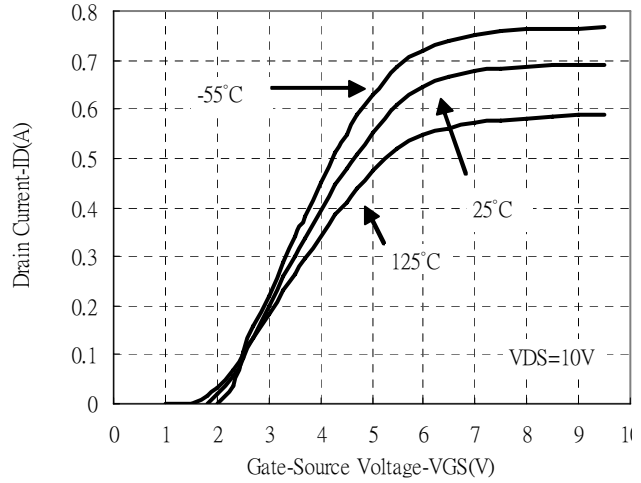


N-channel Characteristic Curves

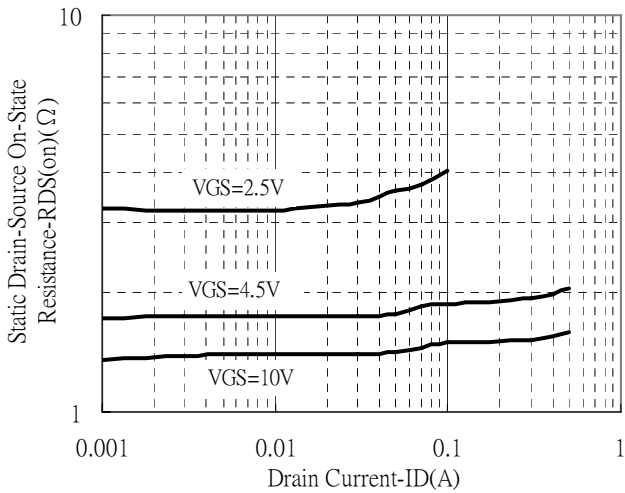
Typical Output Characteristics



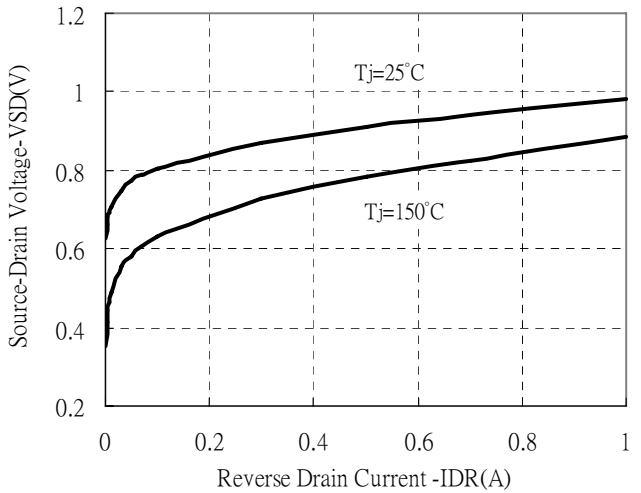
Drain Current vs Gate-Source Voltage



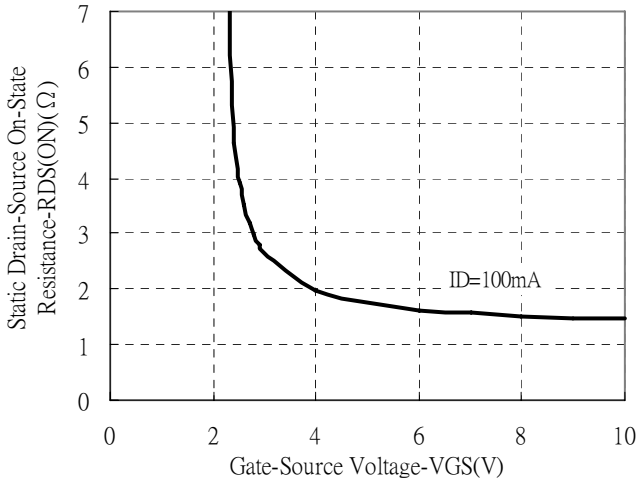
Static Drain-Source On-State resistance vs Drain Current



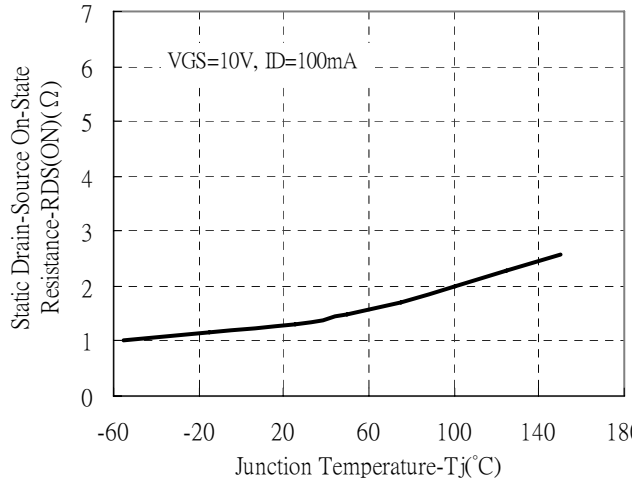
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

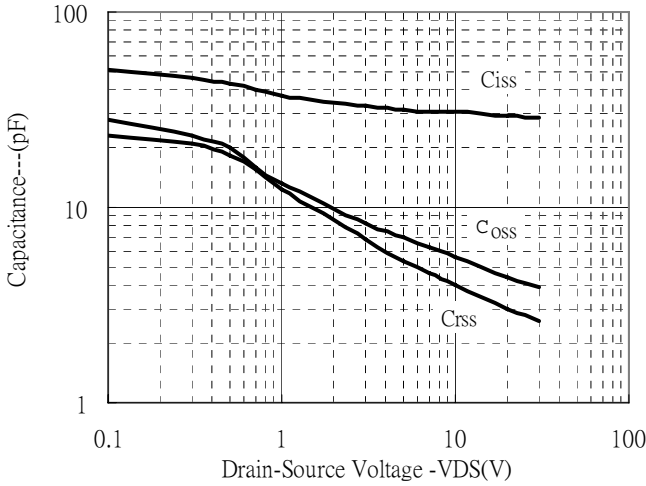


Drain-Source On-State Resistance vs Junction Temperature

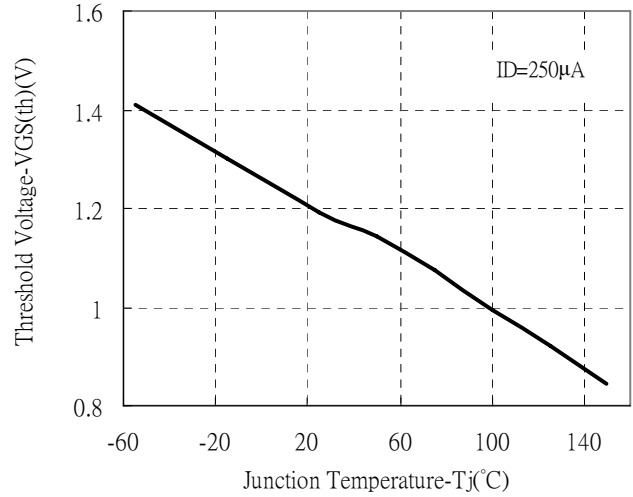


N-channel Characteristic Curves(Cont.)

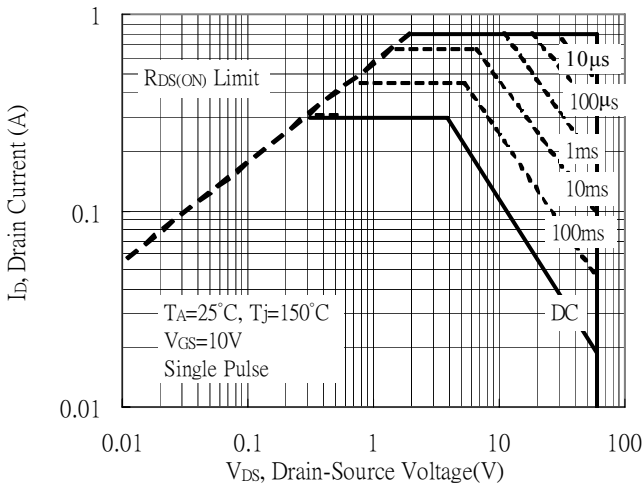
Capacitance vs Drain-to-Source Voltage



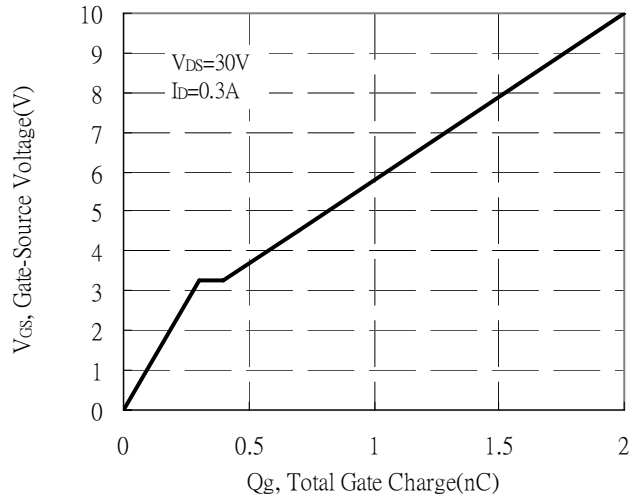
Threshold Voltage vs Junction Temperature



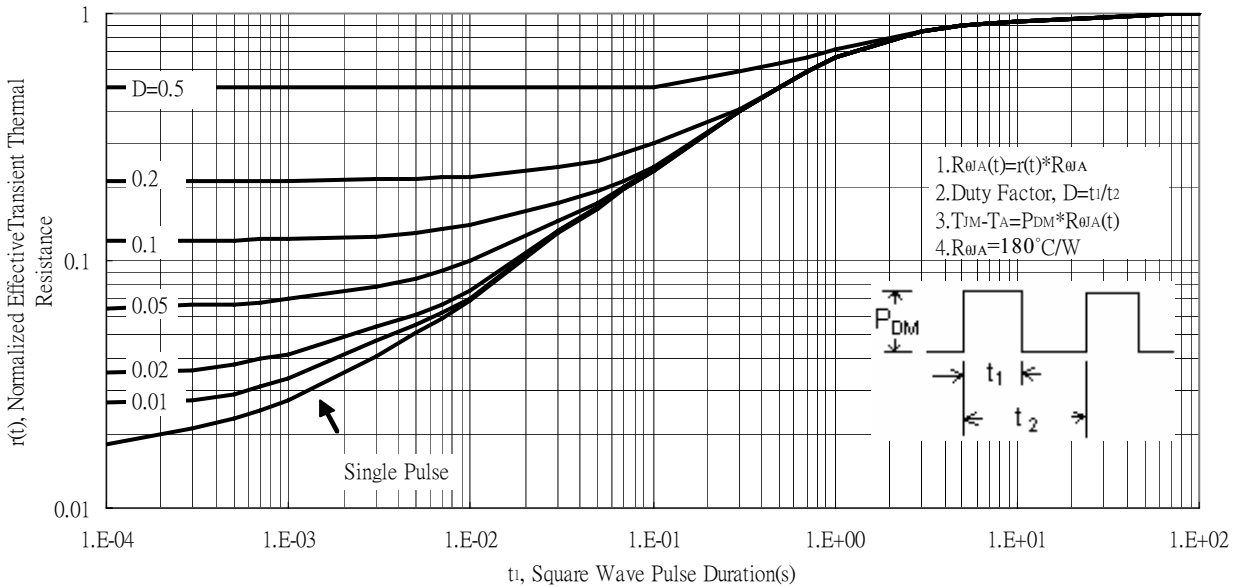
Maximum Safe Operating Area



Gate Charge Characteristics

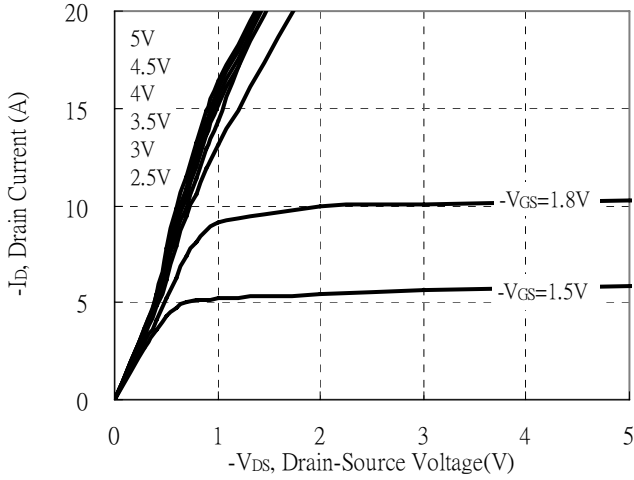


Transient Thermal Response Curves

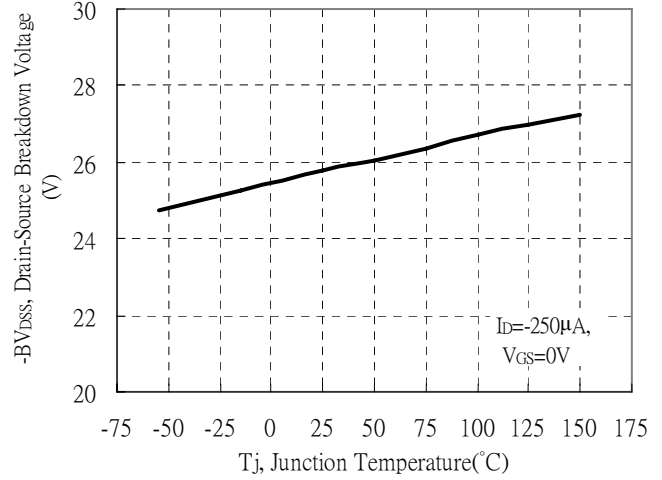


P-channel Characteristic Curves

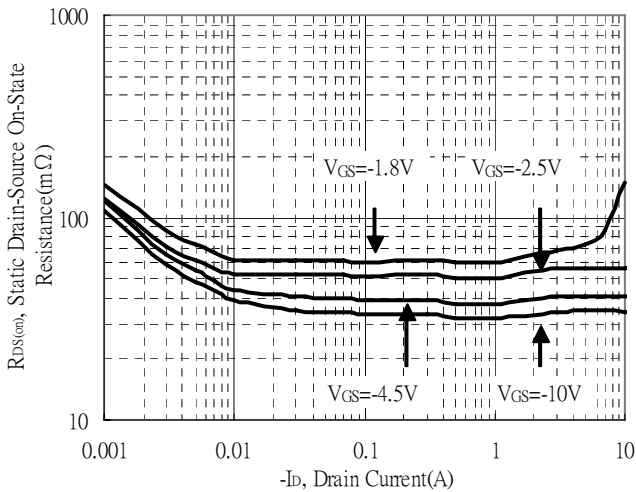
Typical Output Characteristics



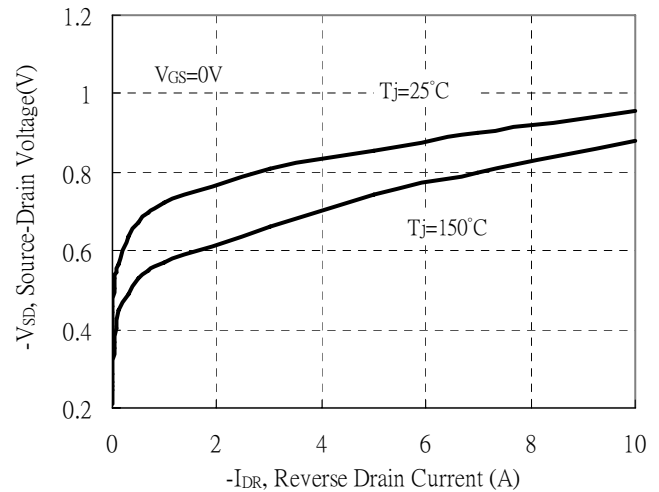
Breakdown Voltage vs Ambient Temperature



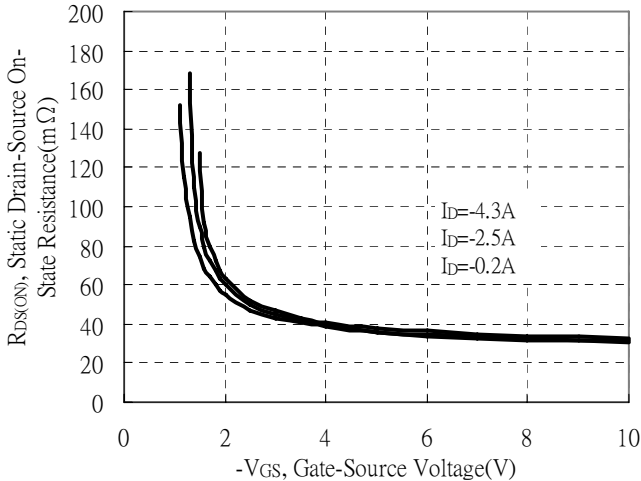
Static Drain-Source On-State resistance vs Drain Current



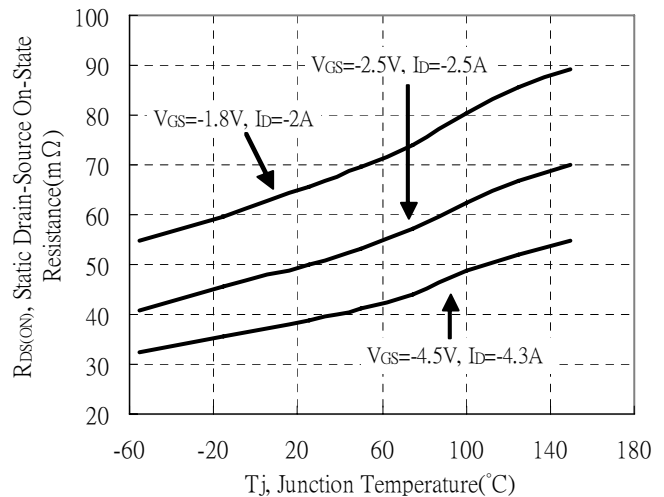
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

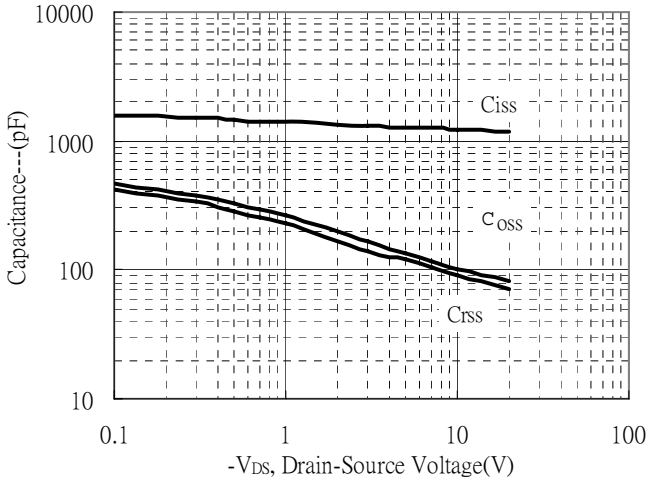


Drain-Source On-State Resistance vs Junction Temperature

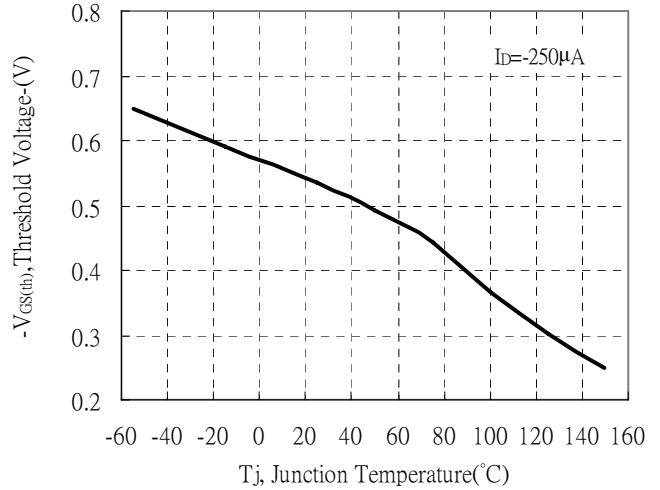


P-channel Characteristic Curves(Cont.)

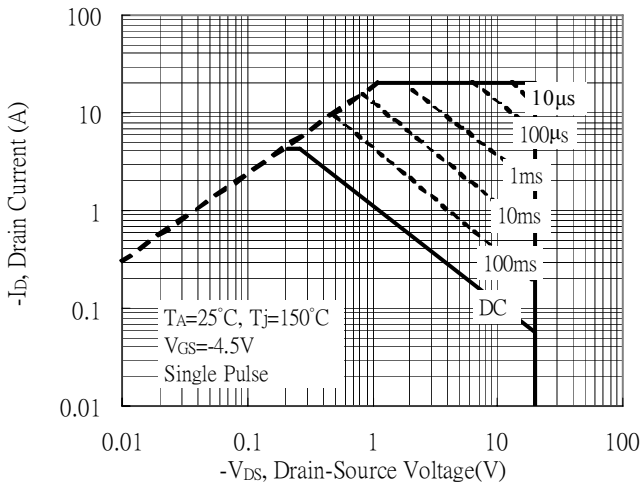
Capacitance vs Drain-to-Source Voltage



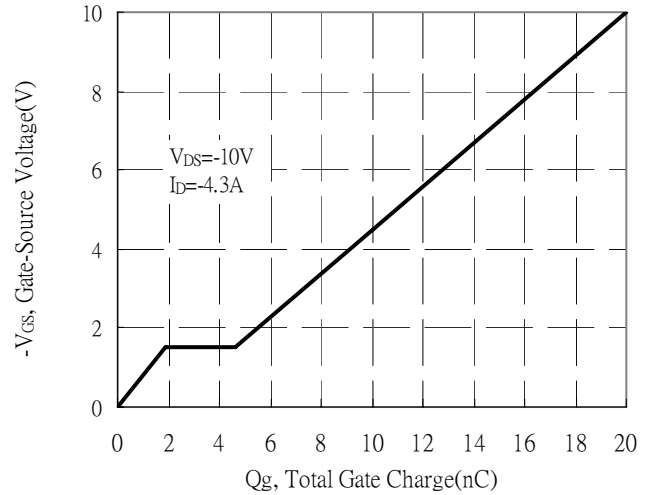
Threshold Voltage vs Junction Temperature



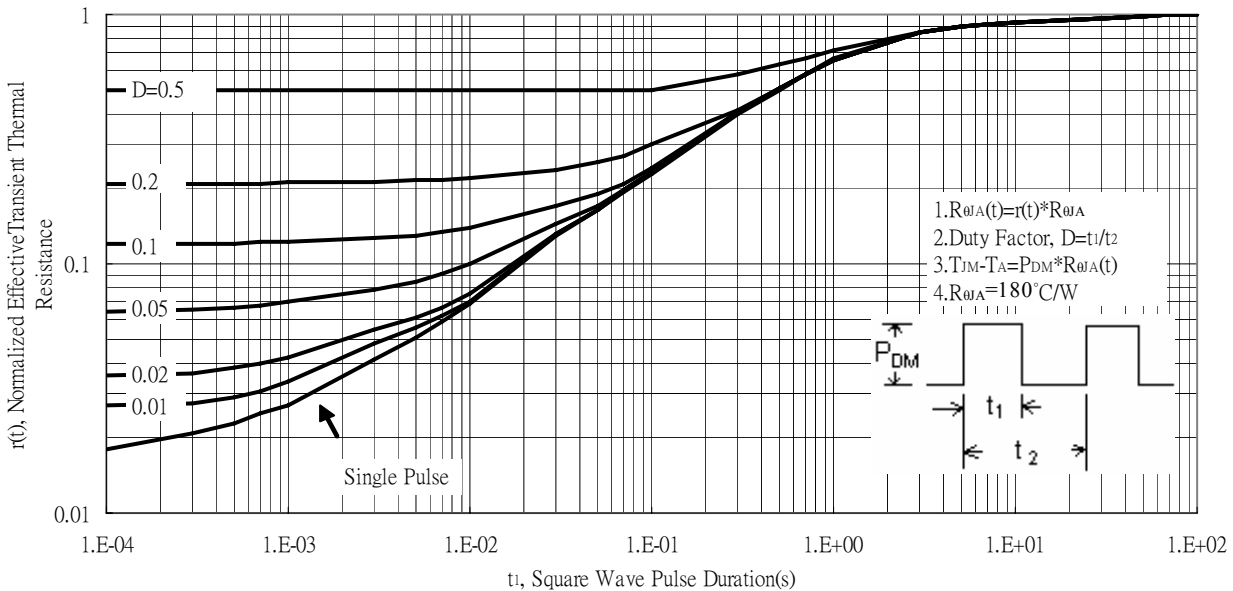
Maximum Safe Operating Area



Gate Charge Characteristics



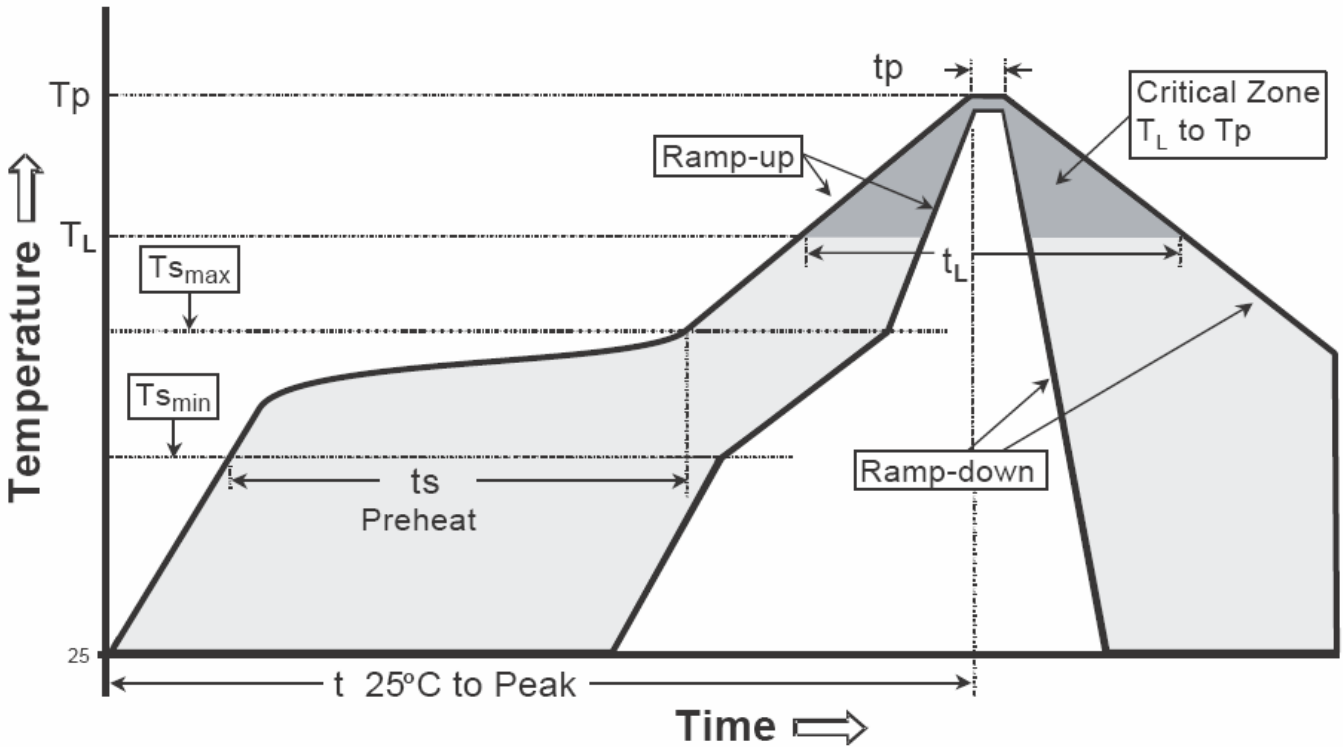
Transient Thermal Response Curves



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

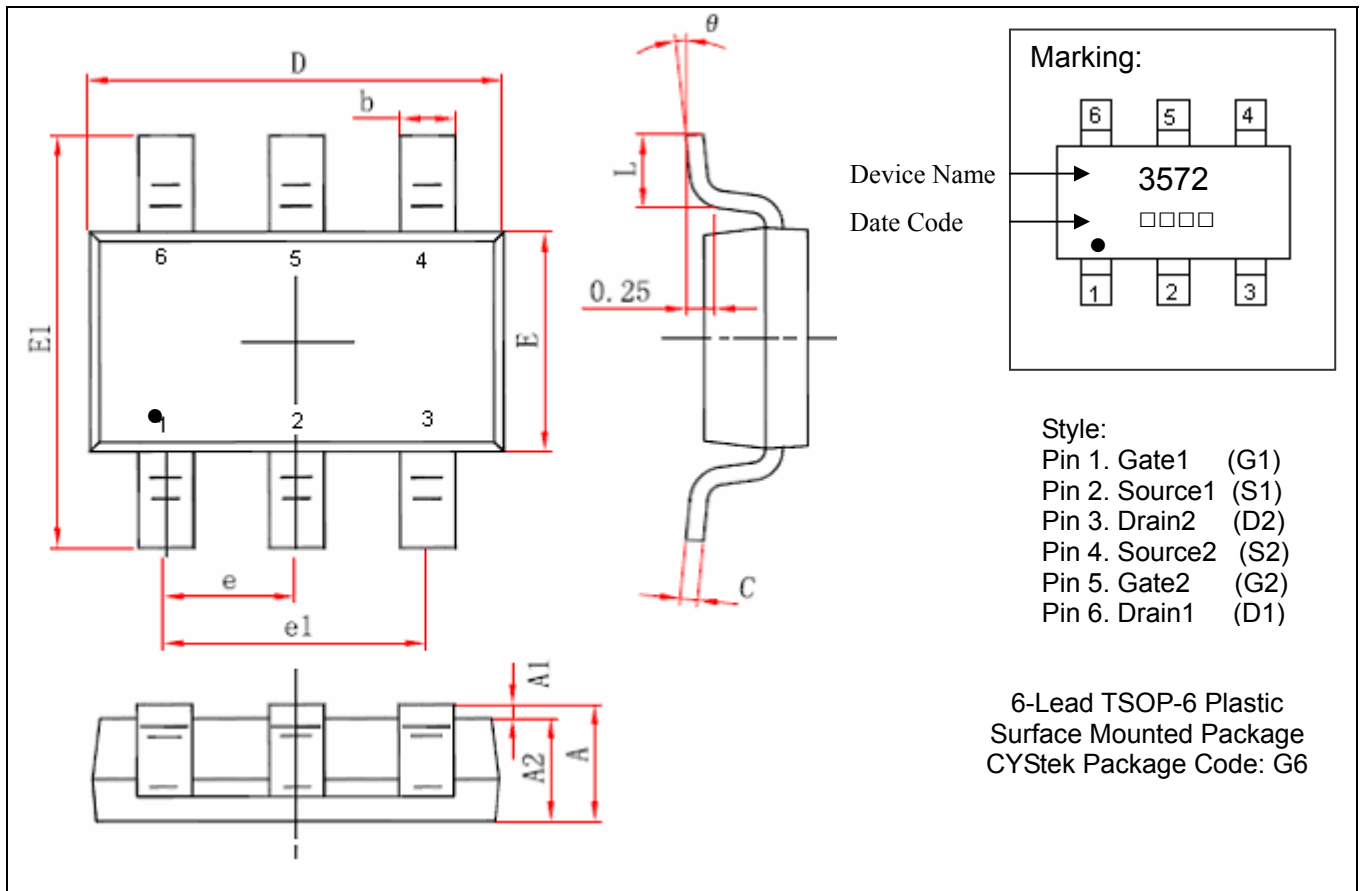
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T _{s min})	100°C	150°C
-Temperature Max(T _{s max})	150°C	200°C
-Time(t _{s min} to t _{s max})	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T _L)	183°C	217°C
- Time (t _L)	60-150 seconds	60-150 seconds
Peak Temperature(T _P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

TSOP-6 Dimension



DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.700	0.900	0.028	0.035	E	1.600	1.700	0.063	0.067
A1	0.000	0.100	0.000	0.004	E1	2.650	2.950	0.104	0.116
A2	0.700	0.800	0.028	0.031	e	0.95 (BSC)		0.037 (BSC)	
b	0.350	0.500	0.014	0.020	e1	1.90 (BSC)		0.075 (BSC)	
c	0.080	0.200	0.003	0.008	L	0.300	0.600	0.012	0.024
D	2.820	3.020	0.111	0.119	θ	0°	8°	0°	8°

Notes : 1.Controlling dimension : millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material :

- Lead : Pure tin plated.
- Mold Compound : Epoxy resin family, flammability solid burning class:UL94V-0.

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