

## GENERAL DESCRIPTION

This Trench MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for load switch and Back light inverter.

## FEATURES

- $V_{DSS}=60V$ ,  $I_D=4.5A$ .
- Drain-Source ON Resistance.

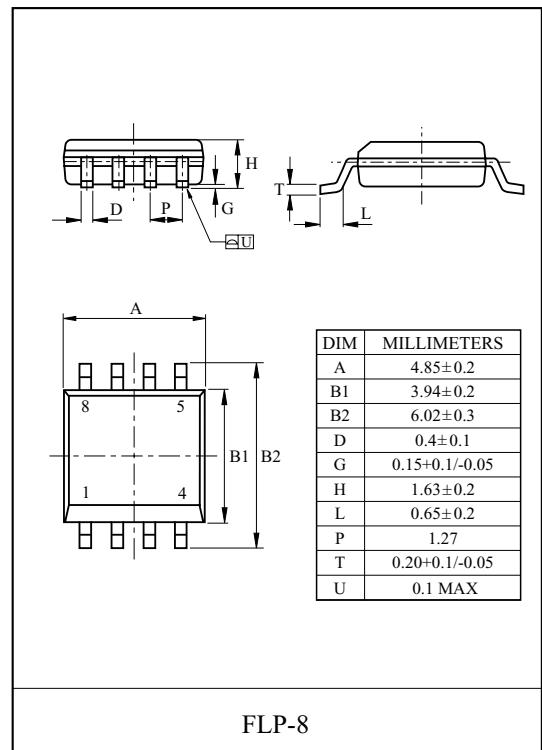
$R_{DS(ON)}=56m\Omega$  (Max.) @  $V_{GS}=10V$

$R_{DS(ON)}=77m\Omega$  (Max.) @  $V_{GS}=4.5V$

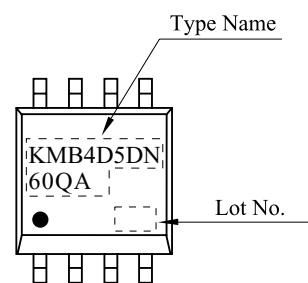
MOSFET Maximum Ratings ( $T_a=25^\circ C$  Unless otherwise noted)

CHARACTERISTIC		SYMBOL	PATING	UNIT
Drain Source Voltage		$V_{DSS}$	60	V
Gate Source Voltage		$V_{GSS}$	$\pm 20$	V
Drain Current	DC@ $T_a=25^\circ C$	$I_D^*$	4.5	A
	Pulsed	$I_{DP}$	20	A
Drain Source Diode Forward Current		$I_S$	3	A
Drain Power Dissipation	@ $T_a=25^\circ C$	$P_D^*$	2	W
Maximum Junction Temperature		$T_j$	150	
Storage Temperature Range		$T_{stg}$	-55~150	
Thermal Resistance, Junction to Ambient		$R_{thJA}^*$	62.5	/W

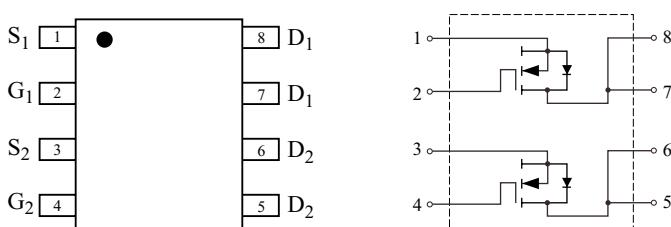
Note> \*Surface Mounted on 1 x 1 FR4 Board, t = 10sec.



## Marking



## PIN CONNECTION (TOP VIEW)



# KMB4D5DN60QA

## ELECTRICAL CHARACTERISTICS (Ta=25 °C) UNLESS OTHERWISE NOTED

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
<b>Static</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>DS</sub> =250 μA	60	-	-	V
Drain Cut-off Current	I <sub>DSS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =48V	-	-	1	μA
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
Gate Threshold Voltage	V <sub>th</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250 μA	1.0	-	3.0	V
Drain-Source ON Resistance	R <sub>DS(ON)*</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =4.5A	-	46	56	m
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A	-	64	77	
Forward Transconductance	g <sub>fs</sub> *	V <sub>DS</sub> =5V, I <sub>D</sub> =4.5A	-	11	-	S
<b>Dynamic</b>						
Input Capacitance	C <sub>iss</sub> *	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V, f=1MHz	-	490	-	pF
Output Capacitance	C <sub>oss</sub> *		-	45	-	
Reverse Transfer Capacitance	C <sub>rss</sub> *		-	25	-	
Total Gate Charge	V <sub>GS</sub> =10V	V <sub>DS</sub> =30V, V <sub>GS</sub> =10V, I <sub>D</sub> =4.5A	-	10.4	-	nC
	V <sub>GS</sub> =4.5V		-	5.1	-	
Gate-Source Charge	Q <sub>gs</sub> *		-	2.3	-	
Gate-Drain Charge	Q <sub>gd</sub> *		-	2.2	-	
Turn-On Delay Time	t <sub>d(on)*</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =10V I <sub>D</sub> =4.5A, R <sub>G</sub> =3	-	12.4	-	ns
Turn-On Rise Time	t <sub>r</sub> *		-	34.5	-	
Turn-Off Delay Time	t <sub>d(off)*</sub>		-	30.7	-	
Turn-Off Fall Time	t <sub>f</sub> *		-	5.0	-	
<b>Source-Drain Diode Ratings</b>						
Source-Drain Forward Voltage	V <sub>SDF*</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =1A	-	0.7	1.0	V
Note> *Pulse Test : Pulse Width 300μs, Duty Cycle 2%						

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Fig1.  $I_D$  -  $V_{DS}$

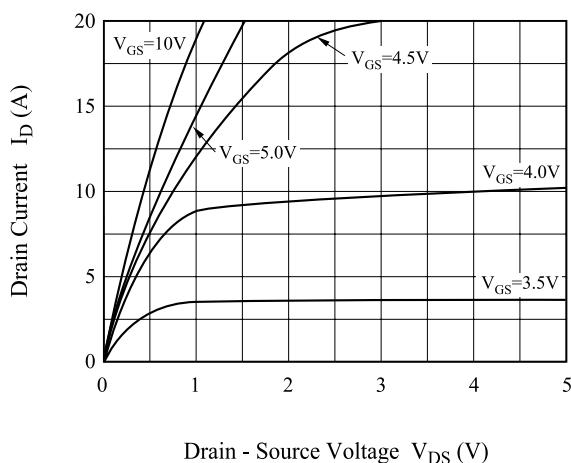


Fig2.  $R_{DS(ON)}$  -  $I_D$

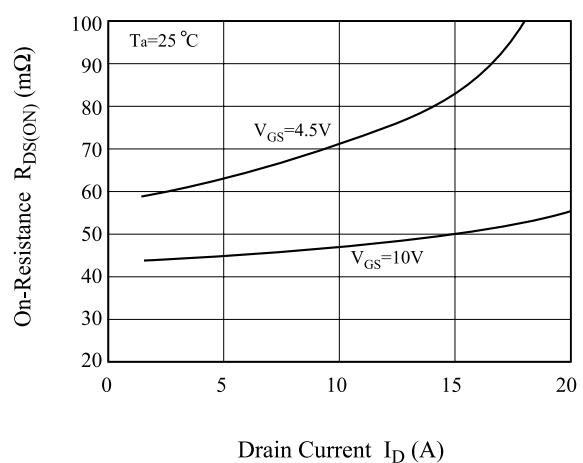


Fig3.  $I_D$  -  $V_{GS}$

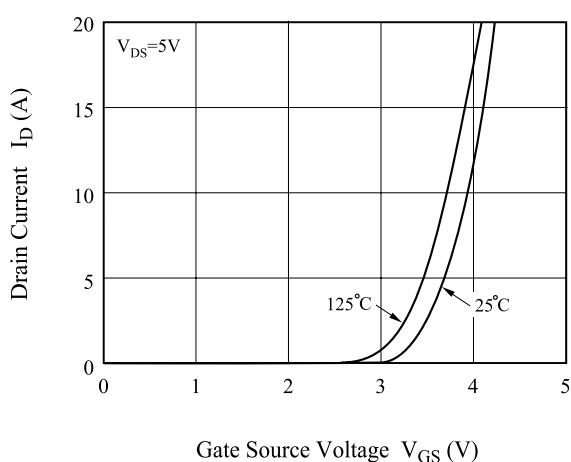


Fig4.  $R_{DS(on)}$  -  $T_j$

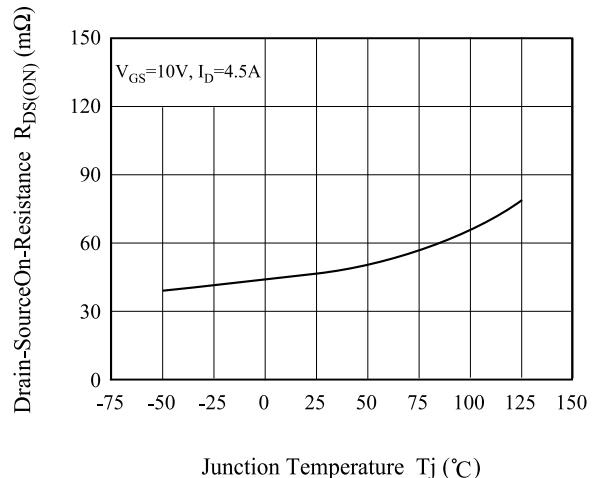


Fig5.  $V_{th}$  -  $T_j$

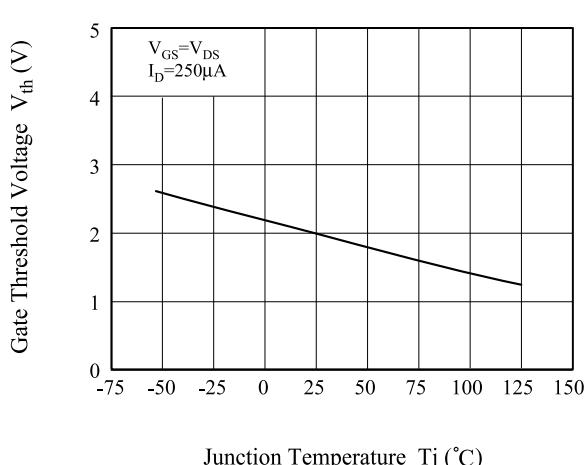
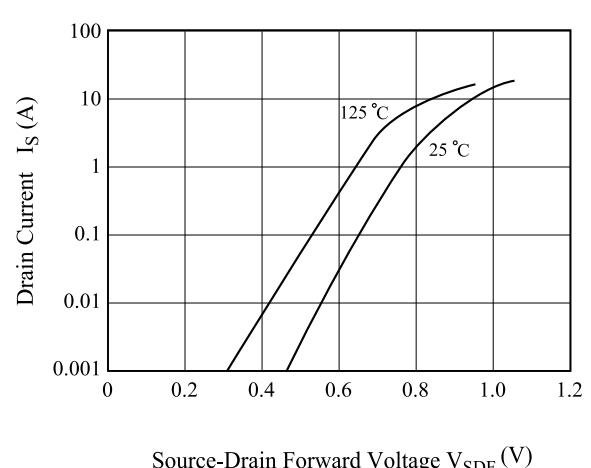


Fig 6.  $I_S$  -  $V_{SDF}$



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Fig7.  $V_{GS}$  -  $Q_g$

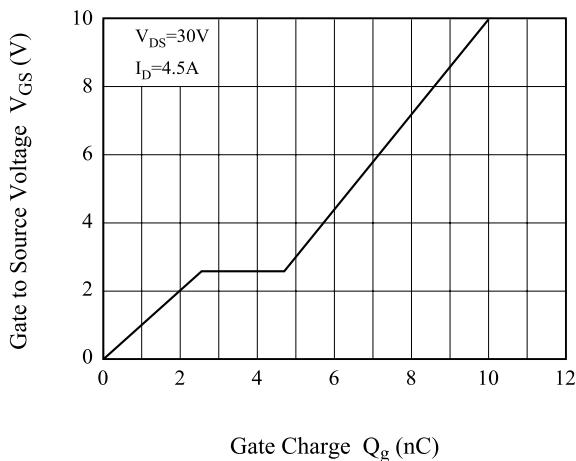


Fig8. C -  $V_{DS}$

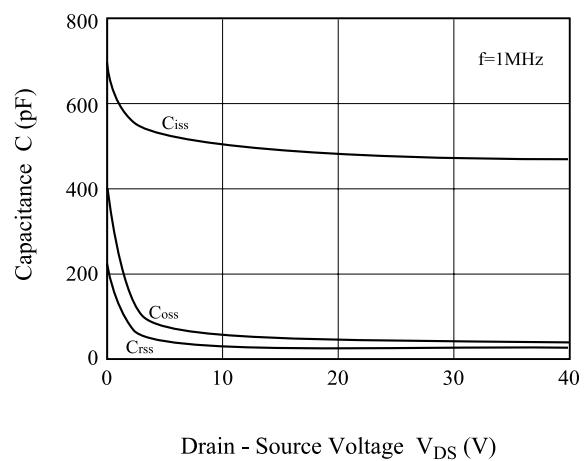


Fig9. Safe Operation Area

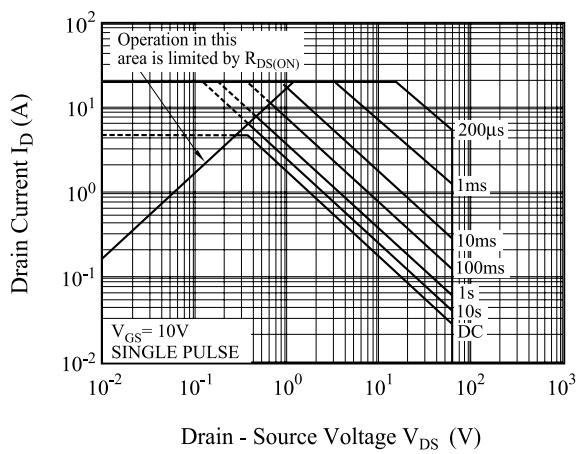


Fig9. Transient Thermal Response Curve

