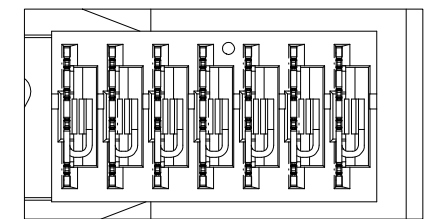
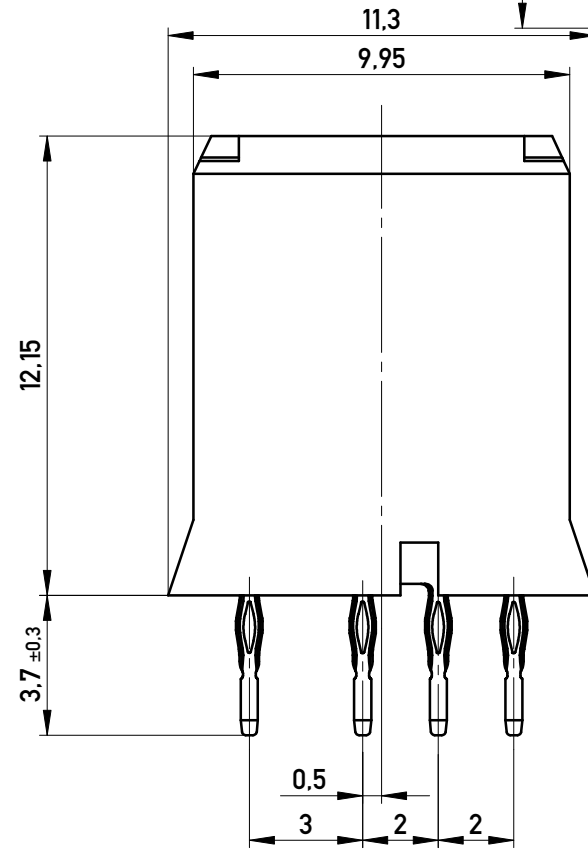
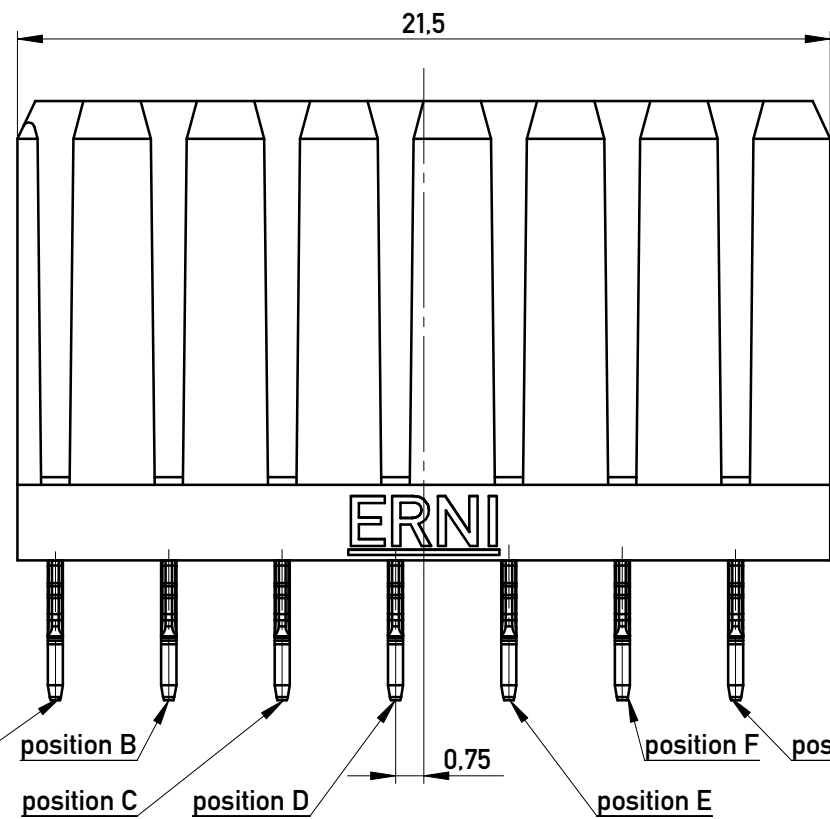
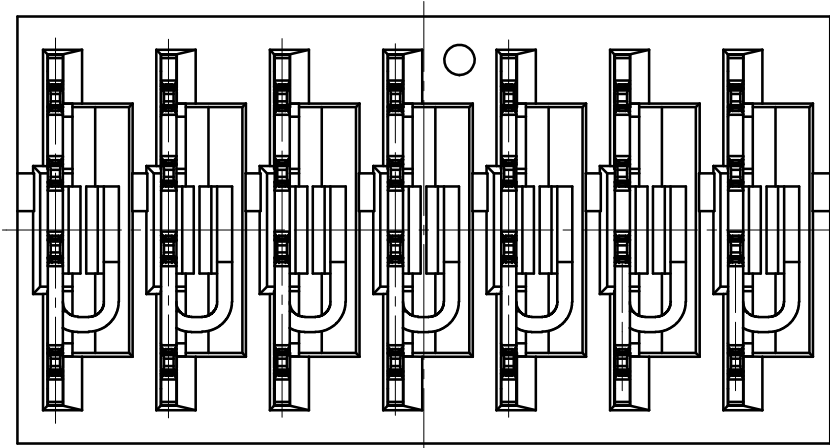
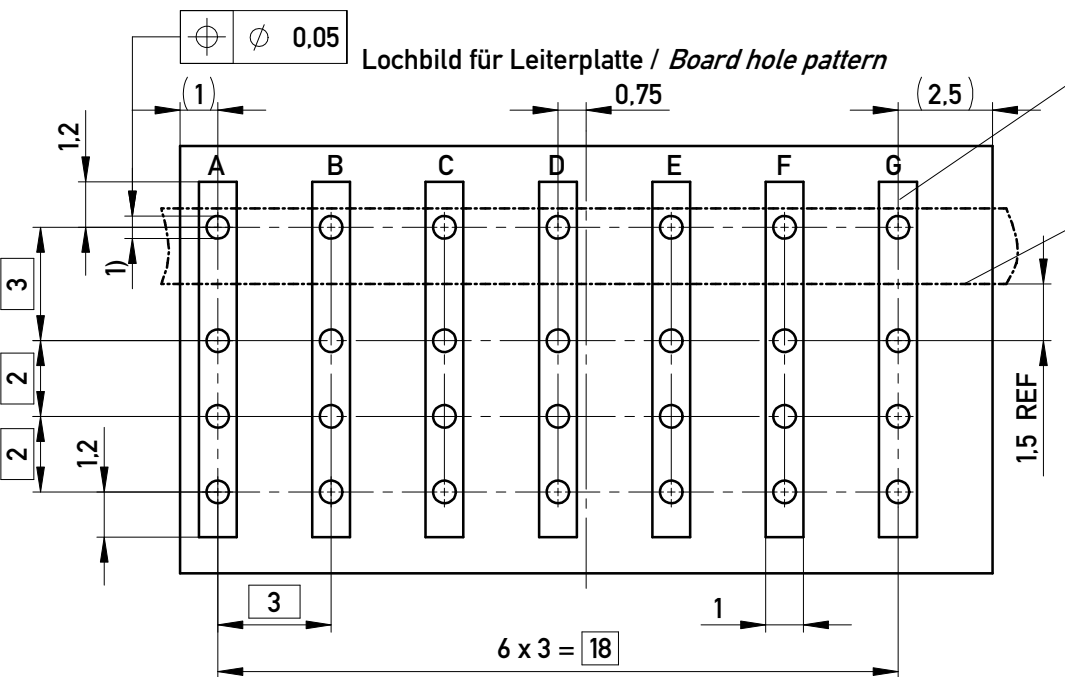


Verpackt in Tray
VPE : 65 Stck.

tray packaging
packaging unit 65 pcs.



DETAIL X
MAßSTAB 2 : 1



Lochbild für Leiterplatte / Board hole pattern

Dieser Bereich muß gleiches Potential auf LP-Oberfläche haben.
This area must have same electrical potential on surface of PCB

Oberkante Tochterkarte
to surface of daughter card REF

- 1) $\varnothing 0,6 \pm 0,05$ Durchmesser des metallisierten Loches
 $\varnothing 0,6 \pm 0,05$ Diameter of finished plated-through hole

Schichtaufbau im metallisierten Loch siehe Zeichnung 114406 oder 164062 Nr.1
Metal plating of plated-through hole see drawing 114406 or 164062 no.1

Gehäuse / housing:
Kontakte / contacts:

PBTP grau 30% GF / PBTP gray 30%GF
Phosphor Bronze / phosphor bronze

Oberflächenbeschichtung / plating:
Kontaktbereich / mating area:
Anschlussbereich / termination:

1,5-3 μ m Ni 1,2 μ m Au
1,5-3 μ m Ni 1 μ m Sn matt

Anforderungsstufe 1
class 1

| | | | | | |
|--|------------|--|--|---|-------|
| Information: | | Tolerances | | Scale | 5:1 |
| All rights reserved. Only for Information. To ensure that this is the latest version of this drawing, please contact one of the ERNI companies before using. | | Subject to modification without prior notice. Drawing will not be updated. | | Designation | |
| www.ERNI.com | | www.ERNI.com | | ERmet Federl. PM 7 Pol RC ERmet Female PM 7 Pin RC | |
| a | 27.12.2011 | Date | | 100200420001 | |
| Index | | | | K A3 | |
| | | | | Class | ERMPM |