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PCN Date: 11/16/2012	Effective Date: 11/22/2012	
Title: C8051F38x Datasheet Revision 1.2		
Originator: Pedro Pachuca	Phone: 512.532.5978	Dept: Marketing
Customer Contact: Kathy Haggar	Phone: 512.532.5261	Dept: Sales

PCN Type:

☒ Datasheet	☐ Foundry	☐ Packing
☐ Product Revision	☐ Assembly	☐ Labeling
☐ Discontinuance	☐ Test	☐ Other

Last Order Date: Not Applicable

PCN Details

Description of Change:
Silicon Labs is pleased to announce revision 1.2 of the datasheet for the C8051F38x products. A summary of the changes from 1.0 to 1.2 can be viewed on page 319 of the datasheet. Please see Appendix A for the changes to the datasheet.

The C8051F38x updated datasheet is available at www.silabs.com.

Reason for Change:
Clarification for electrical specifications and packaging dimensions.

Impact on Form, Fit, Function, Quality, Reliability:
No impact on form, fit, function, quality and reliability.

Product Identification:
Ordering Part Numbers
C8051F380-GQ
C8051F381-GQ
C8051F381-GM
C8051F382-GQ
C8051F383-GQ
C8051F383-GM
C8051F384-GQ
C8051F385-GQ
C8051F385-GM
C8051F386-GQ
C8051F387-GQ
C8051F387-GM

Last Date of Unchanged Product: Not Applicable

Qualification Samples:

Not Applicable.

Customer Early Acceptance Sign Off:

Customers may approve early PCN acceptance by completing the information below:

  Early Acceptance: **Date:** _____

                                  Name: _____

                                  Company: _____

Email your early Acceptance approval to: katherine.haggar@silabs.com

Qualification Data:

Not Applicable.

Appendix A:

Revision 1.0 to Revison 1.1

· Updated front-page diagram to reflect the correct number of Timers, 6 instead of 4.

· Updated Table 3.2, "TQFP-48 Package Dimensions," on page 26 with the following:

· · Fixed right-most column name from Min to Max.

· · Added max values for dimensions A, A1, A2, b, c, L, and q.

· Updated Figure 3.8 and Table 3.6 with correct QFN-32 package drawing and dimensions.

· Updated Table 4.10, "ADC0 Electrical Characteristics," on page 39 with new maximum value for ADC0 Power Supply Current. This addresses an item from the May 18th, 2012 Errata.

· Added Section 5.2 regarding the Temperature Sensor.

· Removed references to programmable gain in "5. 10-Bit ADC (ADC0, C8051F380/1/2/3 only)".

· Updated Table 14.1, "Special Function Register (SFR) Memory Map," on page 111 to fill in the missing row information for the 0xC8 row.

· Updated Table 15.1, "Interrupt Summary," on page 119. The TMR4CN register is not bit-addressable.

· Updated definition for the 000b value of the CLKSL bits in SFR Definition 18.1 (CLKSEL) to include the /4 factor.

Revision 1.1 to Revison 1.2

· Updated Comparator Input Offset Voltage specification in Table 4.13 on page 41.