

Description

The EC49121 series is a Low dropout linear regulator with ON/OFF control and EC49121 can discharge output capacitor charge fast. The device operates in the input voltage range from +2.4V to +7.0V and delivers 600mA output current.

The EC49121 is available in two types, fixed output voltage type and adjustable output voltage type. The fixed output voltage type is preset at an internally trimmed voltage 1.8V, 2.5V, or 3.3V. Other options 1.2V, 1.5V, 2.85V, 3.0V and 3.6V are available by special order only. The output voltage range of the adjustable type is from 1.2V to 5V.

The EC49121 (ADJ type) consists of a 1.175V band gap reference, an error amplifier, and a P-channel pass transistor. Other features include short-circuit protection and thermal shutdown protection

The EC49121 (Fixed type) consists of a 0.95V band gap reference, an error amplifier, and a P-channel pass transistor. Other features include short-circuit protection and thermal shutdown protection, the fixed type version has fast respond and discharge feature.

The EC49121 series devices are available in SOT-23-5L,TSOT-23-5L, SC-70-5L packages.

Features

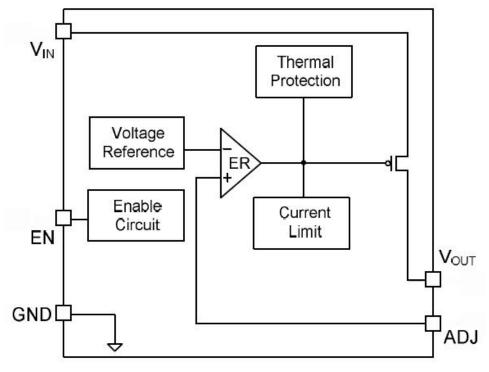
- Operating Voltage Range : +2.4V to +7.0V
- Output Voltages: +1.0V to +5.0V (0.1V Step)(Fixed), +1.2V to +5.0V(ADJ Type)
- Dropout Voltage : 500mV @ 600mA(Fixed)
- Fast Response in Turn-on Transient: 35µS (Type.)
- Low Current Consumption 60µA (Type.)
- Shutdown Current : 0.7µA (Type.)
- ±2% Output Voltage Accuracy (special ±1% highly accurate)
- Low ESR Capacitor Compatible
- High Ripple Rejection : 65 dB (Type.)
- Output Current Limit Protection 1.0A (Type.)
- Short Circuit Protection: 200mA (Type).
- Thermal Overload Shutdown Protection
- Control Output ON/OFF Function
- SOT23-5L 'TSOT23-5L and SC-70-5L Packages
- 100% Lead (Pb)-Free and Green (Halogen Free With Commercial Standard)

Application

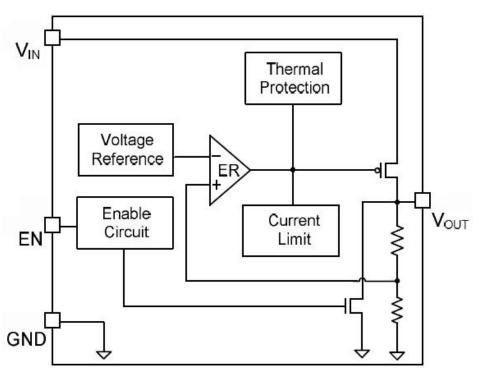
- Battery-Powered Equipments
- Graphic Card.
- Peripheral Card.
- PCMCIA & New Card
- Mini PCI & PCI-Express Cards.
- Digital Still Camera
- CDMA/GSM Cellular Handsets.
- Laptop, Palmtops, Notebook Computers
- Portable Information Application.



Block Diagram



Adjustable Voltage Type



Fixed Voltage Type



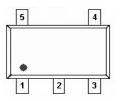
Pin Assignment

600mA LDO with Fast Enable

And Fast Discharge Function

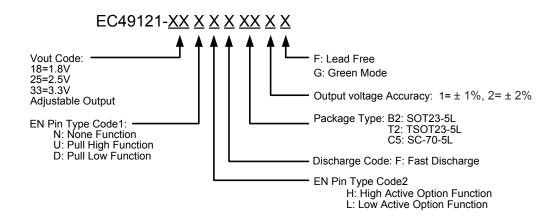
Pin Descriptions

SOT23-5L/TSOT23-5L /SC-70-5L (Top View)



Pin# SOT23-5L	Pin# SC-70-5L	Symbol	Function
1	1	V _{IN}	Regulator Input Pin.
2	2	GND	Ground Pin.
3	3	EN	Enable Pin.
4	4	NC/ADJ	No Connection for Fixed Voltage or ADJ Terminal Pin for Adjustable Voltage.
5	5	V _{OUT}	Regulator Output Pin.

Ordering Information



Package	Part Number	Information
		The XX character represents Output Voltage. Example: 18=1.8V, 25=2.5V, 33=3.3V, Default:
		Adjustable Output
SOT23-5L	EC49121-XXNHFB21G	The N character represents EN Pin type code 1: Example: N: None Function, U: Pull High
		Function, D: Pull Low Function
	The H character represents EN Pin type code 2 Example: H: High Active Option Function,	
TSOT23-5L	EC49121-XXNHFT21G	L:Low Active Option Function
		The F character represents discharge code. Example: F: Fast discharge.
		The B2 or C character represents package type. Example: B2: SOT23-5L, C: SC-70-5L.
SC-70-5L	EC49121-XXNHFC1G	The 1 character represents Output Voltage accuracy. Example:1:±1%, 2 :±2% .
30-70-3L		The last character" F" or "G" represents Lead Free code: Example: F: Lead Free, G: Green
		mode.



Marking Information

Package	Part Number	Marking	Marking Information
			The 1st character represents the products series Example: Part no:EC49121=6
SOT23-5L	EC49121-XXNHFB21G		The 2nd character represents the Output Voltage code. Please refer the table1
			for Output Voltage Code information.
			The 3rd character represents the Function code. Please refer the table2 for
TSOT23-5L		6VFXX	Function Code information.
130123-3L	EC49121-XXNHFT21G	011700	The XX character represents the Date Code.
			There is an under-line on 1st digit for A type package.
			There is an under-line on 5th digit for Pb-Free package.
SC-70-5L	EC49121-XXNHFC1G		There are two under-lines on 4th & 5th digit for Green package.
			There is a top-line on 1 st digit for ±1% Output voltage accuracy.

<u>Table1</u>

Mark code vs. Output Voltage code

Mark	Voltage	Mark	Voltage
4	1.0V	М	3.0V
5	1.2V	Ν	3.1V
8	1.5V	Q	3.3V
А	1.8V	V	3.6V
G	2.5V	Z	5.0V
L	2.85V	0	Adjustable Type

Table2

Mark code vs. Function code

		ENT	Гуре				EN ⁻		
Mark	Code	Туре	Туре	Discharge	Mark	Code	Туре	Туре	Discharge
		Code 1	Code 2				Code 1	Code 2	
А	NHF	None	High	Fast	G	UHN	Pull High	High Active	Normal
В	NLF	None	Low	Fast	Н	ULN	Pull High	Low Active	Normal
с	NHN	None	High	Normal	J	DHF	Pull Low	High Active	Fast
D	NLN	None	Low	Normal	к	DLF	Pull Low	Low Active	Fast
E	UHF	Pull	High	Fast	L	DHN	Pull Low	High Active	Normal
F	ULF	Pull	Low	Fast	М	DLN	Pull Low	Low Active	Normal

Absolute Maximum Ratings

Parameter		Symbol	Value	Units	
Input Voltage V _{IN} to GND		V _{IN}	9.0	V	
Output Current Limit, I	LIMIT)	I _{OUT}	1.0	А	
Junction Temperature		TJ	+155	°C	
Thermal Resistance	SOT23-5L	0	155	°C/W	
Thermal Resistance	SC-70-5L	ΘιΑ	333	0/10	
Power Dissipation	SOT23-5L	P _D -	400	mW	
Power Dissipation	SC-70-5L	FD	250	IIIVV	
Operating Ambient Ten	Operating Ambient Temperature		-40 to +85	°C	
Storage Temperature		T _{STG}	-55 to +150	°C	
Lead Temperature (Soldering, 10sec.)		_	+260	°C	
ESD Voltage			1K	V	

Note:

*The power dissipation of values are based on the condition that junction temperature T_J and ambient temperature T_A difference is 100°C

*Stresses beyond those listed under" absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and function operation of the device at these to any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum rated conditions for extended periods may affect device reliability.



Electrical Characteristics

(V_{IN}=5V, TA=25°C, unless otherwise noted.)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit	
V _{IN}	Input Voltage	_	2.4	_	7.0	V	
V _{OUT}	Output Voltage	Fixed Voltage Type V _{IN} =V _{OUT} +1.0,V I _{OUT} =1mA Adjustable Voltage Type	-1% -2% -1%	V _{OUT}	+1% +2% +1%	V	
		V _{IN} =V _{OUT} +1.2,V I _{OUT} =1mA	-1% -2%		+2%		
I _{MAX}	Output Current (see note *1)	$V_{OUT} + 1.0V {\leq} V_{IN} {\leq} 7.0V, V_{IN} {\geq} 2.4V$	0.6	_	_	А	
I _{LIMIT}	Current Limit	_	—	1.0		А	
V _{REF}	ADJ Pin Reference Voltage	For Adjustable Voltage Type	_	1.175	_	V	
	Dropout Voltage	I _{OUT} =100mA,V _{OUT} >3.0V	_	50	_		
V _{DROP}	(For Fixed Voltage Type)	I _{OUT} =600mA,V _{OUT} >3.0V	_	500		mV	
	Dropout Voltage (For ADJ Voltage Type)	I _{OUT} =600mA,V _{OUT} >3.0V	_	800			
		V_{OUT} +1.0V \leq $V_{IN} \leq$ 7.0V, I_{OUT} =1mA For Fixed Voltage Type	_	0.2	0.3	0 /	
ΔV_{LINE}	Line Regulation	V _{OUT} +1.2V≦V _{IN} ≦7.0V,I _{OUT} 1mA, VIN≧2.8V, For ADJ Voltage Type	_	_	0.2	%/V	
ΔV_{LOAD}	Load Regulation	V_{IN} = V_{OUT} +1V, 1mA \leq I _{OUT} \leq 100mA	—	0.01	0.02	%/mA	
Ι _Q	Ground Pin Current	I _{LOAD} =0mA to 1A,V _{IN} = V _{OUT} +1.0V	_	60		μA	
I _{SD}	Shutdown Current	VIN = V _{OUT} +1V, EN=0V, No Load	_	0.7	_	μA	
I _{ADJ}	ADJ Pin Current	I _{LOAD} =0mA to 600mA,V _{IN} = V _{OUT} +1.0V	_	0.1		μA	
I _{SC}	Short Circuit Current	_	_	200		mA	
PSRR	Ripple Rejection Rate	I _{OUT} =30mA, F=1KHz, C _{OUT} =3.3μF	—	65	—	dB	
e _N	Output Noise	Ι _{ΟUT} =100mA, F=1KHz, C _{OUT} =3.3μF	_	40	_	μV _(rms)	
V _{IH}	EN Pin Input Voltage "H"	V _{IN} ≦5.0V	1.6	_	_	V	
V _{IL}	EN Pin Input Voltage "L"	V _{IN} ≦5.0V	_	_	0.3	V	
R _{DIS}	Discharge Resistor	V _{EN} =0V, For Fixed Voltage Type	_	30	100	Ω	
T _{DIS}	Discharge Time	V _{OUT} =3.3V to 0V, C _{OUT} =1µF For Fixed Voltage Type	_	70	100	μS	
T _{SD}	Thermal Shutdown Temperature	_	_	155		°C	
T _{HYS}	Thermal Shutdown Hysteresis	_	_	20	—	°C	

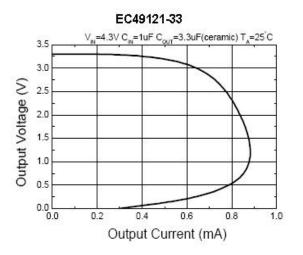
Note:

*1) Measured using a double sided board with 1"x2" square inches of copper area connected to the GND pins for "heat spreading".

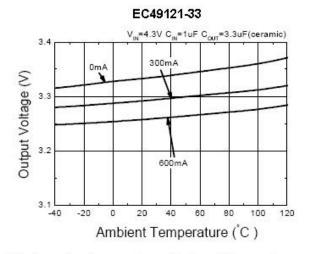


Typical Performance Characteristics

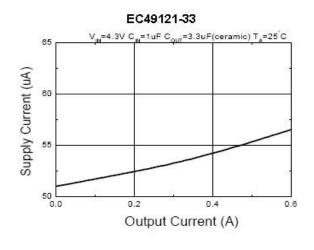
(1) Output Voltage vs. Output Current



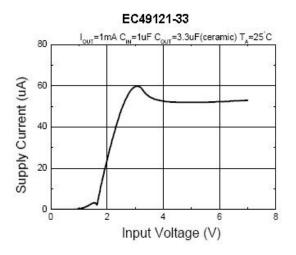
(3) Output Voltage vs. Ambient Temperature



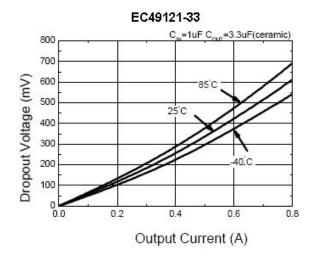
(5) Supply Current vs. Output Current



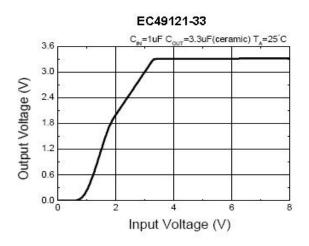
(2) Supply Current vs. Input Voltage



(4) Dropout Voltage vs. Output Current



(6) Output Voltage vs. Input Voltage

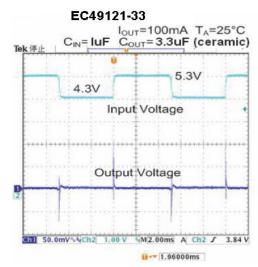


E-CMOS Corp. (www.ecmos.com.tw)

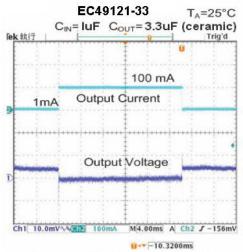


Typical Performance Characteristics (Continued)

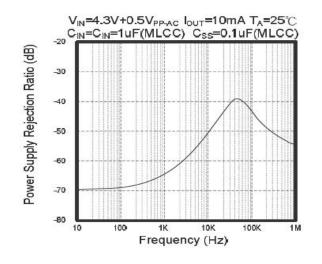
(7) Input Transient Response

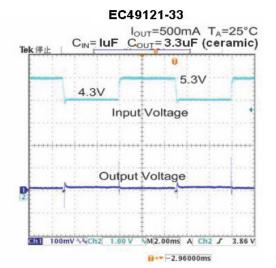


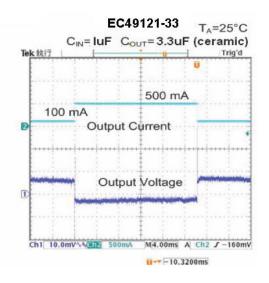
(8) Load Transient Response



(9) Power Supply Rejection Ratio

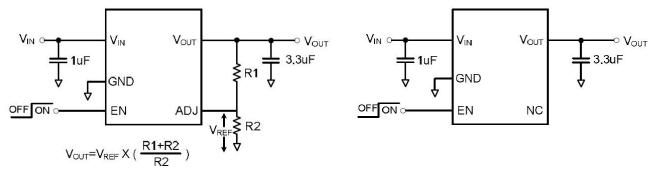








Simplified Application Circuit



Detail Description

The EC49121 is a low-dropout linear regulator. The device provides preset 1.8V, 2.5V and 3.3V output voltages for output current up to 600mA. Adjustable output voltage and other mask options for special output voltages are also available. As illustrated in function block diagram, it consists of a 1.175V band gap (Fixed voltage type is 0.95V) reference, an error amplifier, a P-channel pass transistor and an internal feedback voltage divider.

The band gap reference for fixed voltage types is connected to the error amplifier, which compares this reference with the feedback voltage and amplifies the voltage difference. If the feedback voltage is lower than the reference voltage, the pass-transistor gate is pulled lower, which allows more current to pass to the output pin and increases the output voltage. If the feedback voltage is too high, the pass transistor gate is pulled up to decrease the output voltage.

The output voltage is feed back through an internal resistive divider (or external resistive divider for adjustable output voltage type) connected to OUT pin. Additional blocks include an output current limiter, thermal sensor, and shutdown logic.

Internal P-Channel Pass Transistor

The EC49121 features a P-channel MOSFET pass transistor. Unlike similar designs using PNP pass transistors, P-channel MOSFET require no base drive, which reduces quiescent current. PNP based regulators also waste considerable current in dropout when the pass transistor saturates, and use high base-drive currents under large loads. The EC49121 does not suffer from these problems and consumes only 60µA (Typ.) of current consumption under heavy loads as well as in dropout conditions.

Enable Function

EN pin starts and stops the regulator. When the EN pin is switched to the power off level, the operation of all internal circuit stops, the build-in P-channel MOSFET output transistor between pins V_{IN} and V_{OUT} is switched off, allowing current consumption to be drastically reduced. The V_{OUT} Pin enters the GND level through the internal discharge path between V_{OUT} and GND pins.

Fast Discharge Function

The EC49121 fixed type has fast discharge Function on EN pin disable. When user turns off the device, its internal pull low resistor will discharge output capacitor charge. It'll avoid other device to arise wrong motions.



Output Voltage Selection

For fixed voltage type of EC49121, the output voltage is preset at an internally trimmed voltage. The first two digits of part number suffix identify the output voltage (see Ordering Information). For example; the EC49121-33 has a preset 3.3V output voltage.

For adjustable voltage type of EC49121, the output voltage is set by comparing the feedback voltage at adjust terminal to the internal band gap reference voltage. The reference voltage V_{REF} is1.175V.The out put voltage is given by the equation:

$$V_{OUT} = V_{REF} x \left(1 + \frac{R2}{R1} \right) + I_{ADJ} x R2$$

(See Typical Application Schematic)

Current Limit

The EC49121 also includes a fold back current limiter. It monitors and controls the pass transistor's gate voltage, estimates the output current, and limits the output current within 1.0A.

Thermal Overload Protection

Thermal overload protection limits total power dissipation of EC49121. When the junction temperature exceeds $T_J = +150^{\circ}$ C, a thermal sensor turns off the pass transistor, allowing the IC to cool down. The thermal sensor turns the pass transistor on again after the junction temperature cools down by 20°C, resulting in a pulsed output during continuous thermal overload conditions.

Thermal overload protection is designed to protect the EC49121 in the event of fault conditions. For continuous operation, the absolute maximum operating junction temperature rating of $T_J = +125^{\circ}C$ should not be exceeded.

Operating Region and Power Dissipation

Maximum power dissipation of the EC49121 depends on the thermal resistance of the case and printed circuit board, the temperature difference between the die junction and ambient air, and the rate of airflow. The power dissipation across the devices is $P=I_{OUT} \times (V_{IN}-V_{OUT})$. The resulting maximum power dissipation is

$$P_{MAX} = \frac{(T_J - T_A)}{\theta_{JC} + \theta_{CA}} = \frac{(T_J - T_A)}{\theta_{JA}}$$

Where (T_J-T_A) is the temperature difference between the EC49121 die junction and the surrounding air, θ_{JC} is the thermal resistance of the package chosen, and θ_{CA} is the thermal resistance through the printed circuit board, copper traces and other materials to the surrounding air. For better heat-sinking, the copper area should be equally shared between the V_{IN}, V_{OUT}, and GND pins.

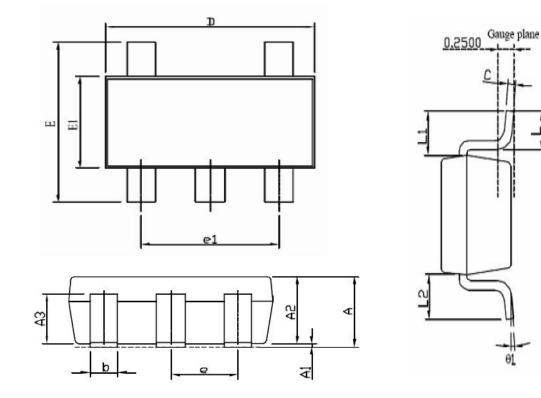
Dropout Voltage

A regulator's minimum input output voltage differential, or dropout voltage, determines the lowest usable supply voltage. In battery powered systems, this will determine the useful end of life battery voltage. The EC49121 uses a P-channel MOSFET pass transistor, its dropout voltage is a function of drain-to-source on-resistance (R_{DS (ON)}) multiplied by the load current.

$$V_{DROPOUT} = V_{IN} - V_{OUT} = R_{DS(ON)} x I_{out}$$



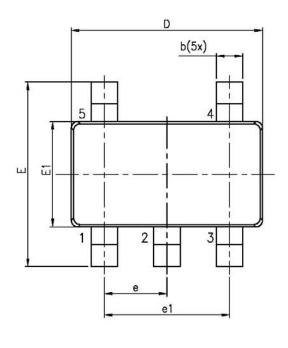
Mechanical Dimensions OUTLINE DRAWING SOT23-5L

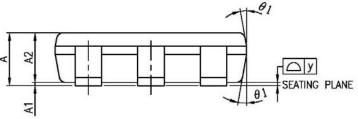


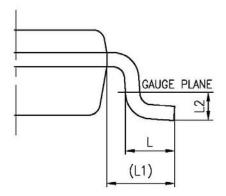
Symbole	Dimensions in Millimeters					
Symbols -	Min	Nom	Max			
A	1.00	1.10	1.40			
A1	0.00	-	0.10			
A2	1.00	1.10	1.30			
A3	0.70	0.80	0.90			
b	0.35	0.40	0.50			
С	0.12	0.125	0.225			
D	2.70	2.90	3.10			
E1	1.40	1.60	1.80			
e1	Notes to be	1.90(TYP)				
E	2.60	2.80	3.00			
L	0.37		 (
θ1	1°	5°	9°			
e	North State	0.95(TYP)				
L1	2000.000	0.6(REF)	0.000			
LI-L2			0.12			



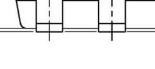
Mechanical Dimensions OUTLINE DRAWING TSOT23-5L

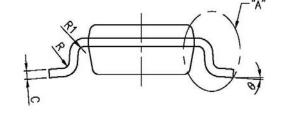












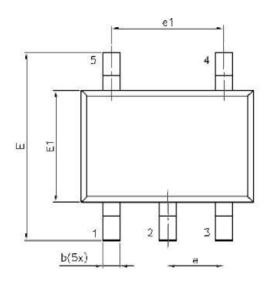
NOTE :

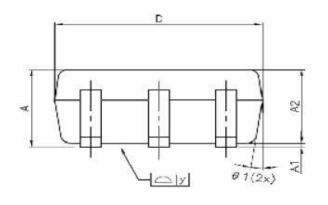
- 1. All dimensions are in millimeters.
- Dimension D does not include mold flash, 2. protrusion or gate burrs. Mold flash protrusions or gate burrs shall not exceed 0.15mm per end. Dimension E1 does not include interlead flash or protrusion. Interlead flash or protrusion shall not exceed 0.15mm per side.
- 3. The package top may be smaller then the package bottom. Dimensions D and E1 are determined at outermost extremes of the plastic body exclusive of mold flash, Tie bar burrs, Gate burrs and interlead flash, but including any mismatch between the top and bottom of the plastic body.
- 4. Dimension "b" does not include dambar protrusion. Allowable dambar protrusion shall be 0.08mm total in excess of the "b" dimension at maximum material condition. The dambar cannot be located on the lower radius of the foot. Minimum space between protrusion and an adjacent lead shall not be less than 0.07mm

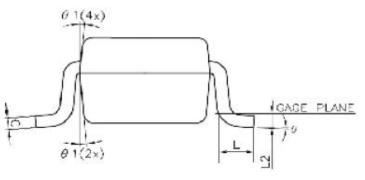
SYMBOLS	DIMENSI	ONS IN MILLI	METERS
STWDOLS	MIN	NOM	MAX
A	0.75		0.90
A1	0.00		0.10
A2	0.70	0.75	0.80
В	0.30		0.50
С	0.08		0.20
D	2.80	2.90	3.00
E	2.60	2.80	3.00
E1	1.50	1.60	1.70
е		0.95 BSC	
e1		1.90 BSC	
L	0.30	0.45	0.60
L1		0.60 REF	
L2		0.25 BSC	
R	0.10		
R1	0.10		0.25
θ	0°	4°	8°
θ1	4°	10°	12°
у			0.10



Mechanical Dimensions OUTLINE DRAWING SC-70-5L







Symbols		Millimeters	3		Inches	
Symbols	Min	Nom	Мах	Min	Nom	Max
Α	0.80		1.10	0.031	(****)	0.043
A1	0.00		0.10	0.000		0.004
A2	0.70	0.90	1.00	0.028	0.035	0.039
b	0.15		0.30	0.006		0.012
С	0.08		0.22	0.003		0.009
D	1.80	2.00	2.20	0.071	0.079	0.087
E	1.80	2.10	2.40	0.071	0.083	0.094
E1	1.15	1.25	1.35	0.045	0.049	0.053
е	S 	0.65		11222202	0.026	
e1	1000	1.30	107500	0.566.5	0.051	
L	0.26	0.36	0.46	0.010	0.014	0.018
L2		0.15			0.006	
У			0.10			0.004
θ	0°	4 °	8 °	0°	4 °	8°
θ1	4 °		12°	4°		12°