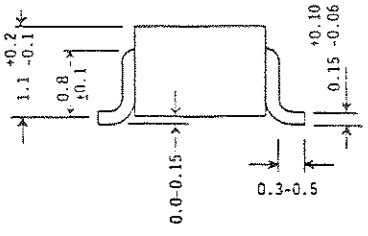
ROHM

CUSTOMER	NO. MMST5086
SUBJECT SOT-23 TRANSISTOR, PNP, SILICON	PAGE 1 of 2
	DATE January 14, 1987

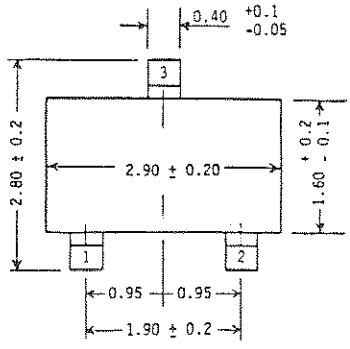
ABSOLUTE MAXIMUM RATINGS: (Ta = 25°C)

Collector-Base Voltage	V_{CBO}	50 V
Collector-Emitter Voltage	V_{CEO}	50 V
Emitter-Base Voltage	V_{EBO}	3 V
Collector Current	I_C	200 mA
Power Dissipation-Free Air	P_D	200 mW
Power Dissipation-Ceramic Substrate	P_D	350 mW
Operating and Storage Junction Temperature	T_J, T_{stg}	-55 to 150 °C
Solder Temperature (10 seconds)		260 °C

DIMENSIONS: Units (mm)



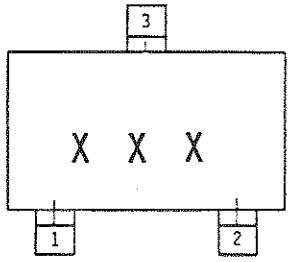
END VIEW



Pin-Out	
1	B
2	E
3	C

THE JAPANESE STYLE SC-59 PACKAGE

MARKING:



Three Digit Marking Code

R 2 P

PACKAGING:

____ BULK, 500 per BAG
____ MAGAZINES OF 50 EACH
____ 8mm T&R, T- 146 3K/REEL
____ 8mm T&R, T- 147 3K/REEL
____ 8mm T&R, T- 2461OK/REEL
____ 8mm T&R, T- 2471OK/REEL

REMARKS: PROCESS: A-22 Thermal Resistance $R_{\theta JA}$ 625 °C/Watt
FREE AIR, T_A = 25°C

ROHM ELECTRONICS
3034 Owen Drive, Antioch, TN 37013
TEL:(615)641-2020 FAX:(615)641-2022

APPROVAL	CHECK	DESIGN
10/12/84		

MASTER

ROHM

CUSTOMER	NO. MMST5086
	PAGE 2 of 2
SUBJECT TRANSISTOR, PNP ,SILICON SOT-23	DATE January 14, 1987

ELECTRICAL CHARACTERISTICS: (Ta = 25°C Unless Otherwise Specified)

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
BV_{CBO}	I_C = 100 uA	50			V
BV_{CEO}	I_C = 1.0 mA	50			V
I_{CBO}	V_{CB} = 45 V		0.7	100	nA
I_{CEO}	V_{CE} = 40 V		0.7	100	nA
I_{EBO}	V_{EB} = 4.5 V		0.7	100	nA
h_{FE}	I_C = 100 uA, V_{CE} = 5.0 V	150		500	
h_{FE}	I_C = 1.0 mA, V_{CE} = 5.0 V	150			
h_{FE}	I_C = 10 mA, V_{CE} = 5.0 V	150			
$V_{CE(SAT)}$	I_C = 10 mA, I_B = 1.0 mA		0.05	0.30	V
VBE(ON)	I_C = 1.0 mA, V_{CE} = 5.0 V		0.60	0.85	V
f_T	I_C = 500 uA, V_{CE} = 5 V, f = 20 MHz	40			MHz
h_{fe}	I_C = 1.0 mA, V_{CE} = 5 V, f = 1.0KHz	150		600	
C_{cb}	V_{CB} = 5.0 V, I_E = 0, f = 100 KHz		5.0	6.0	pF
NF	I_C = 20 uA, V_{CE} = 5.0V, Rs=10K Ω f = 10 Hz to 15.7 KHz			3.0	dB
NF	I_C = 100 uA, V_{CE} = 5.0V, Rs=3K Ω f = 1.0 KHz			3.0	dB

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